Global and Local Monitors to Enforce Noninterference in Concurrent Programs

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Abstract—Controlling confidential information in concurrent systems is difficult, due to covert channels resulting from interaction between threads. This problem is exacerbated if threads share resources at fine granularity.

In this work, we propose a novel monitoring framework to enforce strong information security in concurrent programs. Our monitors are hybrid, combining dynamic and static program analysis to enforce security in a sound and rather precise fashion. In our framework, each thread is guarded by its own local monitor, and there is a single global monitor. We instantiate our monitoring framework to support rely-guarantee style reasoning about the use of shared resources, at the granularity of individual memory locations, and then specialize local monitors further to enforce flow-sensitive progress-sensitive information-flow control. Our local monitors exploit rely-guarantee-style reasoning about shared memory to achieve high precision. Soundness of rely-guarantee-style reasoning is guaranteed by all monitors cooperatively. The global monitor is invoked only when threads synchronize, and so does not needlessly restrict concurrency. We prove that our hybrid monitoring approach enforces a knowledge-based progress-sensitive noninterference security condition.

Keywords—Language-based security; information-flow control for concurrent systems; hybrid information-flow monitor.

I. INTRODUCTION

Computer systems increasingly exhibit concurrency, including systems that handle confidential information. Providing confidentiality in concurrent systems is challenging, as sharing of resources and synchronization may create covert channels, thus facilitating inadvertent leakage of sensitive information. An additional challenge is to enforce confidentiality without unnecessarily limiting or reducing concurrency.

Most previous mechanisms to control the flow of sensitive information (and to prevent information leaks) are inadequate in the presence of modern concurrency features. Operating system abstractions (e.g., 2 3) are too coarse grained to control information flow between concurrent threads that share fine-grained resources. Purely static mechanisms (e.g., 4 5 6 7 8) are often too restrictive with respect to sharing of resources and synchronization between threads. Purely dynamic mechanisms (such as information-flow control monitors 9 10 11 12) have not yet been extended to handle fine-grained concurrency, due in part to the difficulties in dynamically and efficiently preventing covert channels resulting from thread interactions.

This is the full version of 1 and provides detailed proofs and additional material such as omitted calculus rules.

We investigate hybrid information-flow control for enforcing confidentiality in concurrent programs. Hybrid information-flow control monitors (or, simply, hybrid monitors) combine static and dynamic program analysis to secure the flow of information in a system. We present a general framework for monitoring concurrent programs, and instantiate this framework for information-flow security. In our framework, monitoring occurs at two levels: each thread is guarded by a local monitor, and there is a single global monitor to provide control across threads. We enable modular, rely-guarantee-style reasoning about the behavior of multi-threaded programs by extending the concepts of modes and mode states 13 to a dynamic setting. Our global monitor ensures that assumptions made by threads regarding the exclusive or shared use of memory are justified. Our local monitors ensure that individual threads provide the guarantees they promise. Our local monitors additionally control information flow within the guarded threads. The global monitor is not concerned with information-flow control; its sole purpose is to ensure that all assumptions are justified. Crucially, these assumptions can be exploited by the local monitors to establish information-flow control, both effectively and precisely.

Threads’ assumptions about memory resources may change only at synchronization points. Thus, the global monitor is accessed only when threads synchronize, and the global monitor does not needlessly restrict concurrency in the program. Existing hybrid information-flow control monitors for concurrent programs (e.g., 14) use a single monitor shared by all threads, thus causing unnecessary synchronization between threads.

This article’s contributions can be summarized as follows.

- We provide efficient and precise information-flow control for concurrent programs using hybrid monitors. Our monitoring approach is novel: each thread has its own local monitor, and there is a single global monitor. Efficiency is achieved because the global monitor is accessed only at thread synchronization points, and so does not needlessly restrict concurrency. We prove that our monitors enforce a progress-sensitive noninterference-like security guarantee.
- The generic framework for monitoring concurrent programs is itself a novel technical contribution. It enables sound rely-guarantee-style reasoning in a dynamic setting, where the global monitor ensures the compatibility of assumptions made with guarantees provided by threads, and the local monitors enforce the guarantees promised by threads. Local
monitors can be specialized to impose further constraints on the guarded thread. By exploiting assumptions, local monitors are able to effectively enforce also global system properties. This is the feature from which we benefit in our hybrid solution for information-flow security.

We consider a simple imperative concurrent calculus, with input and output operators and barrier synchronization. Our local monitors support flow-sensitive security types [15], which facilitates precise reasoning about information flow via program variables. A thread may soundly allow the security level of a variable to change only if the thread has exclusive read-access or exclusive write-access to the variable.

Our local monitors separately track upper bounds on information that may be revealed by the relative order of events (the timing level of a thread) and information that may be revealed by learning whether control has reached the current program point or diverged earlier (the termination level of a thread). These levels are analogous to the pc level traditionally used in security-type systems for non-concurrent programs [4]. The termination level enables us to enforce progress sensitive security [16], a generalization of termination sensitive security [17] to interactive programs. Most existing work on enforcing information-flow security ignores progress sensitivity due to the complexity and imprecision of the enforcement mechanism. Use of the timing level enables us to prevent internal timing leaks [18]. Although the termination level of a thread increases monotonically, we provide additional precision by lowering the timing level of threads at synchronization points, since synchronization between threads restricts the relative order of events before and after the synchronization. Our work leverages the insight that resetting the timing levels of threads at synchronization points is analogous to lowering the pc level at post-dominators of control-flow branches [18][4][19].

II. INFORMATION FLOW IN CONCURRENT PROGRAMS

In this section, we provide intuition for how concurrency and progress sensitivity complicate the enforcement of information-flow security, and for how our monitors enforce security in this challenging setting. We then present our general framework for monitoring concurrent programs (Sections [III][V]), instantiate it for rely-guarantee reasoning (Section [V]), and further instantiate it to enforce information-flow security (Sections [V][VI]).

Information flow through concurrent access: Confidential information may be inadvertently leaked through thread interaction. For example, consider the following (insecure) program, which consists of two threads that execute concurrently.

\textbf{Thread 1:} input \texttt{H} to \texttt{foo}; output \texttt{foo} \times 2 \texttt{H}
\textbf{Thread 2:} \texttt{foo} := 42; output \texttt{foo} to \texttt{L}

The first thread inputs confidential information from high-security channel \texttt{H}, stores it in variable \texttt{foo}, and then outputs 2 times \texttt{foo} to channel \texttt{H}. The other thread sets \texttt{foo} to the constant integer 42, then outputs \texttt{foo} to low-security channel \texttt{L}, which we assume can be observed by an adversary. Each of these threads is intuitively secure if it were executed in isolation. However, when executed concurrently, since they both access \texttt{foo}, it is possible that Thread 2 will output confidential information to channel \texttt{L}, violating security by revealing confidential information to the attacker. Indeed, if an observer of channel \texttt{L} sees an output of anything other than 42, she can infer the confidential input.

Fine-grained resource sharing: Following the work of Mantel, Sands, and Sudbrock [13], we support fine-grained reasoning about threads’ access to shared resources (like the variable \texttt{foo} in the example above) in order to prevent security violations via thread interaction through such resources. Each thread uses rely-guarantee reasoning about what variables it and other concurrently executing threads might read or write. Consider, for example, the following (secure) program, where Thread 1 assumes that no other threads will read variable \texttt{w} (note that Thread 1 stores confidential information in \texttt{w}), and Thread 2 assumes that no other threads will write variable \texttt{v} (note that Thread 2 sends the content of \texttt{v} to channel \texttt{L}). Provided both assumptions hold, the program is secure, even in the presence of additional threads.

\textbf{Thread 1:} input \texttt{H} to \texttt{w}; output \texttt{w} + \texttt{v} to \texttt{H}
\textbf{Thread 2:} \texttt{v} := 14; output \texttt{v} \times 42 to \texttt{L}

Threads’ assumptions and guarantees originate from protocols the concurrently running threads comply with, where coordination among threads is often achieved through synchronization mechanisms. In the following (secure) example, Thread 1 has exclusive access to variable \texttt{y} before the barrier, and Thread 2 has exclusive access after the barrier.

\textbf{Thread 1:} input \texttt{L} to \texttt{y}; barrier
\textbf{Thread 2:} barrier; output \texttt{y} to \texttt{L}

Assumptions and guarantees of threads must be compatible when threads are composed. We assume that a thread’s assumptions are stated explicitly. We use the global monitor to impose the corresponding guarantees on all other threads, and use the local monitors of the threads ensure that these guarantees are indeed provided. Thus, our approach supports fine-grained resource sharing while preventing information leakage through concurrent access.

Information flow through nondeterminism: Information can also be leaked when the relative order of observable events (such as outputs to low-security channels) depend on confidential information. In the following (insecure) example, the output on channel \texttt{L} is either 0 followed by 1, or vice versa, where the order likely depends on confidential information.

\textbf{Thread 1:} input \texttt{h} to \texttt{H}; while \texttt{h} > 0 do \texttt{h} := \texttt{h} − 1 od;
output 1 to \texttt{L}
\textbf{Thread 2:} output 0 to \texttt{L}

Our monitors prevent information leakage by this program as follows: Regardless of the value of the confidential input, Thread 1’s local monitor notices that the relative ordering between its own output and output of other threads might be influenced by the while loop, and, hence, the local monitor intervenes by blocking the thread’s execution before the output occurs. The following program is, however, secure and permitted by our monitors.

\textbf{Thread 1:} input \texttt{h} to \texttt{H}; while \texttt{h} > 0 do \texttt{h} := \texttt{h} − 1 od;
barrier; output 1 to \texttt{L}
\textbf{Thread 2:} barrier; output 0 to \texttt{L}

Even though the relative order of the low outputs is undetermined, the threads cannot perform the output until
after the barrier. Intuitively, the program is secure because the synchronization between threads ensures that the order of the low output does not depend on confidential information, even though the low output is nondeterministic \cite{20, 21}.

To correctly and precisely track what information might be revealed by the relative timing of events, each local monitor tracks the timing level, a security level that is an upper bound on information that has influenced the timing of the thread’s execution with respect to other threads. Since after synchronization, the relative timing of threads is independent of information that affected the timing before the synchronization, the timing level of a thread can be lowered immediately after synchronization. This is similar to information-flow control in single-threaded programs, where the pc level (a bound on the information that influences whether the current statement is executed) can be lowered at post-dominators of control flow decisions. That is, synchronization points are the post-dominators of concurrent programs, and allow a similar improvement in precision.

Precision of hybrid monitors: Our use of monitors improves the precision of security enforcement, since we consider the security of a single execution, and do not need to determine whether all possible program executions are secure. For example, our monitor permits complete execution of the following single-threaded program if the low input is positive. In contrast, a static analysis would have to classify this program as insecure, because executing it will leak confidential information if the low input is negative or zero.

\begin{verbatim}
input L to low;
  if low > 0 then input L to x else input H to x fi;
output x to L
\end{verbatim}

Progress sensitivity: If confidential information influences whether a program terminates, observing the termination or non-termination of a program can leak confidential information. This is exacerbated in interactive settings \cite{16} (i.e., where the program produces observable events before the end of the program) and concurrent settings (where many threads may each reveal a small amount of information). For example, in the following (insecure) program, the program silently diverges after outputting a low value that is equal to the secret (i.e., it diverges without producing any further output). Thus, an observer of channel L can learn the secret by noting the last value output.

\begin{verbatim}
input H to high; low := 0;
  while 1 do
    output low to L;
      if low < high then low := low+1 else (while 1 do skip od) fi od
\end{verbatim}

An additional concern is that an enforcement mechanism itself might reveal information. In the following (insecure) program, if the secret is positive, a monitor might intervene to prevent the insecure output of high to L by blocking the thread. As a consequence, the output of 42 to channel L would never occur and, hence, an observer of L could infer that the secret must be a positive value, once she realizes that 42 will not be output.

\begin{verbatim}
input H to high;
  if high > 0 then output high to L else skip fi;
output 42 to L
\end{verbatim}

We prevent information leaks via termination and monitor interventions by tracking the termination level and blocking level of each thread within our local monitors. These are upper bounds on information that might have influenced, respectively, the termination behavior of loops and the blocking behavior of the monitor. By tracking these levels, we ensure that the termination and blocking behavior does not leak information.

III. MONITORED MULTI-THREADED COMPUTATIONS

We propose a formal model for multi-threaded programs that interact with their environment via channels and whose concurrent threads communicate with each other using shared memory. In our model, we reuse the concepts of modes and of mode states from \cite{13} to facilitate rely-guarantee-style reasoning about the behavior of such programs.

The novelty of our model is that it supports reasoning about multi-threaded programs that are monitored. In our model, programs can be monitored at two levels: local monitors provide control over individual threads while global monitors provide control across threads. By lifting the concept of mode states to threads that are monitored, we enable rely-guarantee-style reasoning both about monitored programs and within monitors.

We recall the concepts of modes and of mode states in Section \cite[III-A] and also define a formal notation for mode state changes. In Section \cite[III-B] we introduce judgments that capture the behavior of local and global monitors. This provides the basis for lifting the concepts of mode states to multi-threaded programs that are monitored in Section \cite[III-C].

Notation: We denote the powerset of a set S by \(\mathcal{P}(S)\). We use \(A \to B\) and \(A \rightarrow B\) to denote the set of all total functions and all partial functions, respectively, with domain A and range B. We denote the pre-image of a function \(f : A \to B\) by \(\text{pre}(f)\), i.e., \(\text{pre}(f) = \{a \in A \mid f(a) \in B\}\). We denote the update of a function \(f\) by \(f[d \mapsto v]\), where \(f[d \mapsto v](d) = v\) and \(f[d \mapsto v](d') = f(d')\) for \(d' \notin \text{pre}(f)\) \(\setminus \{d\}\).

We refer to partial functions with domain \(\mathbb{N}_0\), range \(A\), and a finite pre-image of consecutive numbers starting at 0 as lists over \(A\), and denote the set of all such lists by \(A^*\). We use \(\epsilon\) to denote the empty list, \(l \cdot a\) to denote the list that results from appending an element \(a \in A\) to the end of a list \(l \in A^*\), and \(l_1 \cdot l_2\) to denote the concatenation of two lists \(l_1, l_2 \in A^*\). The function filter removes from a list over \(A\) those elements that do not satisfy a predicate \(p \subseteq A\), i.e., \(\text{filter}(p, \epsilon) = \epsilon\), \(\text{filter}(p, l \cdot a) = \text{filter}(p, l) \cdot a\) if \(a \in p\), and \(\text{filter}(p, l \cdot a) = \text{filter}(p, l)\) if \(a \notin p\).

Additional notation and notions are defined in Appendix A.

A. Modes, Mode States, and Annotations

We use modes \cite{13} to capture a program developer’s expectations about the environment in which a program shall run as well as his intentions. For instance, a program might be designed to use a particular communication protocol with its environment and, hence, a thread running such a program will
provide the desired functionality only in an environment that complies with this protocol. Such a convention incorporates both an expectation (namely that the environment will follow the protocol) and an intention (namely that the thread itself is programmed correctly to follow the protocol).

In general, modes can be used to express assumptions and guarantees about arbitrary entities that are relevant for a program’s behavior. In this article, we restrict ourselves to modes that express assumptions and guarantees about particular entities, namely program variables. More concretely, we focus on assumptions about which variables might be read and written by a thread’s environment and the dual guarantees. We use \texttt{Var} to denote the set of all variables.

We use the symbols A-NR and A-NW to express the assumption that a particular variable will not be read and will not be written, respectively, by the environment of a given thread. Moreover, we use the symbols G-NR and G-NW to express the guarantee that a thread will not read and will not write, respectively, a particular variable. That is, the modes A-NR and G-NR are dual to each other, and so are the modes A-NW and G-NW. We define the function \textit{invert} : \textit{Mod} \to \textit{Mod} to transform a mode \textit{mod} \in \textit{Mod} into its dual mode:

\[
\begin{align*}
\text{invert}(\text{A-NR}) &= \text{A-NW} \\
\text{invert}(\text{G-NR}) &= \text{A-NW} \\
\text{invert}(\text{A-NW}) &= \text{G-NW} \\
\text{invert}(\text{G-NW}) &= \text{A-NW}
\end{align*}
\]

We refer to the modes A-NR and A-NW as the \textit{no-read} and the \textit{no-write} assumption. Moreover, we refer to the modes G-NR and G-NW as the \textit{no-read} and the \textit{no-write} guarantee. Formally, we define the set of assumptions by \textit{Asm} = \{A-NR, A-NW\}, the set of guarantees by \textit{Gua} = \{G-NR, G-NW\}, and the set of all modes by \textit{Mod} = \textit{Asm} \cup \textit{Gua}.

We use \textit{mode states} \cite{13} to track which assumptions are made and which guarantees are provided. Formally, a mode state is a function \textit{mdst} : \textit{Mod} \to \mathcal{P}(\textit{Var}) that returns the set of all variables that are in a given mode. Accordingly, we define the set of all mode states by \textit{MdSt} = \textit{Mod} \to \mathcal{P}(\textit{Var}).

We use terms of the form \textit{acq}(\textit{mod}, \textit{X}) and \textit{rel}(\textit{mod}, \textit{X}) to specify that the mode \textit{mod} is acquired and released, respectively, for all variables in the set \textit{X} \subseteq \textit{Var}. We call such terms \textit{annotations} and define the set of all annotations by \textit{Ann} = \{\textit{acq}(\textit{mod}, \textit{X}), \textit{rel}(\textit{mod}, \textit{X}) \mid \textit{mod} \in \textit{Mod}, \textit{X} \subseteq \textit{Var}\}. For singleton sets of variables, we use a shorthand notation and write \textit{acq}(\textit{mod}, \textit{x}) instead of \textit{acq}(\textit{mod}, \{\textit{x}\}). Similarly, we write \textit{rel}(\textit{mod}, \textit{x}) instead of \textit{rel}(\textit{mod}, \{\textit{x}\}).

We introduce the predicate \textit{Has-Mode-In} \subseteq \textit{Ann} \times \mathcal{P}(\textit{Mod}) to identify annotations with particular modes. We define this predicate by \textit{ann Has-Mode-In M} if \textit{ann} \in \{\textit{acq}(\textit{mod}, \textit{X}), \textit{rel}(\textit{mod}, \textit{X}) \mid \textit{X} \subseteq \textit{Var} \& \textit{mod} \in \textit{M}\} and the projection of a list of annotations \gamma to a set of modes \textit{M} by \gamma \mid \textit{M} = \textit{filter}(\lambda \textit{ann} \in \textit{Ann} : \textit{ann Has-Mode-In M}, \gamma).

To model the effects of annotations on mode states, we define the function \textit{update} : (\textit{MdSt} \times \textit{Ann}) \to \textit{MdSt} by:

\[
\begin{align*}
\text{update}(\textit{mdst}, \textit{acq}(\textit{mod}, \textit{X})) &= \langle \textit{mdst}[\textit{mod} \to (\textit{mdst}(\textit{mod}) \cup \textit{X})] \rangle \\
\text{update}(\textit{mdst}, \textit{rel}(\textit{mod}, \textit{X})) &= \langle \textit{mdst}[\textit{mod} \to (\textit{mdst}(\textit{mod}) \setminus \textit{X})] \rangle \\
\end{align*}
\]

Overloading notation, we lift the function \textit{update} : (\textit{MdSt} \times \textit{Ann}) \to \textit{MdSt} to a function \textit{update} : (\textit{MdSt} \times \textit{Ann}*) \to \textit{MdSt} on lists of annotations by \textit{update}(\textit{mdst}, \epsilon) = \textit{mdst} and \textit{update}(\textit{mdst}, \gamma \textit{ann}) = \textit{update}(\textit{update}(\textit{mdst}, \gamma), \textit{ann}), where \textit{mdst} \in \textit{MdSt}, \textit{ann} \in \textit{Ann}, and \gamma \in \textit{Ann}.*

For instance, the annotation \textit{acq}(A-NW, \textit{x}) can be used to specify that a thread from now on runs under the assumption that variable \textit{x} is not written by other threads, \textit{mdst}(A-NW) equals the set of all variables for which the no-write assumption is made in mode state \textit{mdst}, and, in particular, \textit{x} \in (\textit{update}(\textit{mdst}, \textit{acq}(A-NW, \textit{x})))\textit{(A-NW)} holds.

This fact is explicated by the following proposition.

\textbf{Proposition 1.} Let \gamma, \gamma' \in \textit{Ann}*, \textit{mod} \in \textit{Mod}, and \textit{mdst}, \textit{mdst}' \in \textit{MdSt} be arbitrary. If \textit{mdst}(\textit{mod}) = \textit{mdst}'(\textit{mod}) and \gamma \mid \{\textit{mod}\} = \gamma' \mid \{\textit{mod}\} then (\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = (\textit{update}(\textit{mdst}', \gamma')(\textit{mod})).

\textbf{Proof:} Let \textit{mod} \in \textit{Mod} be arbitrary. We first prove the following implication by induction over the length of \gamma:

\[
\forall \gamma \in \textit{Ann}^* : \forall \textit{mdst}, \textit{mdst}' \in \textit{MdSt} : (\textit{mdst}(\textit{mod}) = \textit{mdst}'(\textit{mod}) \implies ((\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = (\textit{update}(\textit{mdst}', \gamma \mid \{\textit{mod}\}))(\textit{mod}))).
\]

Let \textit{mdst}, \textit{mdst}' \in \textit{MdSt} be arbitrary with \textit{mdst}(\textit{mod}) = \textit{mdst}'(\textit{mod}).

Base case: Assume \gamma = \epsilon. From \gamma = \epsilon, we obtain \gamma \mid \{\textit{mod}\} = \epsilon. From the definition of \textit{update}, \gamma = \epsilon, \textit{mdst}(\textit{mod}) = \textit{mdst}'(\textit{mod}) and \gamma \mid \{\textit{mod}\} = \epsilon, we obtain (\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = \textit{mdst}(\textit{mod}) = \textit{mdst}'(\textit{mod}) = (\textit{update}(\textit{mdst}', \gamma \mid \{\textit{mod}\}))(\textit{mod})).

Step case: Assume \gamma = \gamma^x\textit{ann} for some \gamma^x \in \textit{Ann}*. Let \textit{mdst}^x = \textit{update}(\textit{mdst}, \gamma^x) and \textit{mdst}'^x = \textit{update}(\textit{mdst}', \gamma^x \mid \{\textit{mod}\}). From the induction assumption, we obtain \textit{mdst}^x(\textit{mod}) = \textit{mdst}'^x(\textit{mod}).

If \textit{ann} \mid \{\textit{mod}\} = \epsilon \textit{then } (\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = (\textit{update}(\textit{mdst}, \gamma^x))(\textit{mod}). \textit{From } \textit{mdst}^x(\textit{mod}) = \textit{mdst}'^x(\textit{mod}) \textit{and } \gamma \mid \{\textit{mod}\} = (\gamma^x \textit{ann}) \textit{we obtain } (\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = (\textit{update}(\textit{mdst}, \gamma^x))(\textit{mod}).

If \textit{ann} \mid \{\textit{mod}\} \neq \epsilon \textit{then } (\textit{update}(\textit{mdst}, \gamma))(\textit{mod}) = (\textit{update}(\textit{mdst}, \gamma^x), \textit{ann}))(\textit{mod}) = \textit{mdst}^x(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'^x(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'(\textit{mod}) \cup \{\textit{x}\} = \textit{mdst}'(\texti
(update(mdst, ann))(mod) = (update(mdst, γ | {mod}))(mod).

If \( ann = \text{rel}(mod, x) \) for some \( x \in \text{Var} \) then
\[
(\text{update}(mdst, γ))(mod) = (\text{update}(mdst, ann))(mod) = \text{mdst}(mod) \land \{x\} = \text{mdst}'(mod) \land \{x\} = \text{(update}(mdst, ann))(mod) = (\text{update}(mdst, γ | {mod}))(mod).
\]

This concludes the proof of Proposition 1. It remains to argue that the overall proposition follows.

Let \( mdst, mdst' \in \text{MdSt} \) and \( γ, γ' \in \text{Ann}^* \) be arbitrary with \( \text{mdst}(mod) = \text{mdst}'(mod) \) and \( γ | \{mod\} = γ' | \{mod\} \). From Proposition 1 and \( \text{mdst}(mod) = \text{mdst}'(mod) \), we obtain
\[
(\text{update}(mdst, γ))(mod) = (\text{update}(mdst', γ | \{mod\}))(mod).
\]

From \( γ | \{mod\} = γ' | \{mod\} \), we obtain
\[
(\text{update}(mdst', γ | \{mod\}))(mod) = (\text{update}(mdst', γ' | \{mod\}))(mod).
\]

Thus,
\[
(\text{update}(mdst, γ))(mod) = (\text{update}(mdst', γ' | \{mod\}))(mod).
\]

B. Monitors and Events

To control the behavior of individual threads, each thread is guarded by a local monitor which is invoked each time the thread performs a computation step. We use local events to capture information about computation steps needed by a local monitor. We denote the set of all local events by \( Ev \), use \( α ∈ Ev \) as a meta-variable, use \( LMon \) to denote the set of all possible internal states of local monitors, and use \( lmon ∈ LMon \) as a meta-variable for local monitor states.

We employ the judgment \( lmon \rightarrow δ, α \text{perm} lmon' \) to capture that a local monitor in state \( lmon ∈ LMon \) permits the combination of \( α ∈ Ev \) and \( δ ∈ \text{Ann}^* \). The local monitor’s internal state is updated to \( lmon' ∈ LMon \).

To control the behavior across threads, a multi-threaded program is guarded by one global monitor. We use global events to capture the information about such steps that is needed by the global monitor. We denote the set of all global events by \( GEv \), use \( β ∈ GEv \) as a meta-variable, use \( GMon \) to denote the set of all possible internal states of global monitors for pool states with \( n \) threads, and identify the initial global monitor states in these sets by \( gmon_{\text{init}, n} ∈ GMon_n \). We define the set of all global monitor states by \( GMon = \bigcup_{n ∈ \mathbb{N}} GMon_n \) and use \( gmon ∈ GMon \) as a meta-variable.

In this article, we use the global monitor only to control mode-state updates. To simplify our exposition, we assume that threads update their mode state only when synchronizing with other threads, and we consider only one synchronization primitive, namely global barrier synchronization. Consequently, the global monitor needs to be invoked only when the program passes a barrier and only needs to distinguish between two global events: sync for barrier synchronization and \( ε \) for all other steps. Throughout this article, the set of all global events is \( GEv = \{\text{sync}, ε\} \). We use a function \( χ : Ev → GEv \) to extract the corresponding global event from a local event.
of all values, define the set of states of the shared memory by $\text{Mem} = \text{Var} \rightarrow \text{Val}$, and use $\text{Ch}$ to denote the set of all channels. We model that a value $v \in \text{Val}$ is received from channel $v \in \text{Ch}$ and that $v$ is output on $\text{ch}$ by the terms $\text{in}(\text{ch}, v)$ and $\text{out}(\text{ch}, v)$, respectively. We refer to such terms as interactions and denote the set of all interactions by $\text{IO}$. We call lists of interactions traces, define the set of all traces by $\text{Tr} = \text{IO}^*$, and use $\tau \in \text{Tr}$ as a meta-variable for traces.

We capture the behavior of a program’s environment by a communication strategy. A strategy determines which input the environment supplies to a program on a given channel after a sequence of prior interactions. Formally, a strategy is a function $\sigma : (\text{Tr} \times \text{Ch}) \rightarrow \text{Val}$ such that $\sigma(\tau, \text{ch})$ is the value supplied next on channel $\text{ch}$ if all prior interactions are captured by the trace $\tau$. We use $\Sigma$ to denote the set of all such strategies.

**Configurations:** For capturing snapshots during program execution, we employ three layers of configurations: global configurations, local configurations, and command configurations, where global and local configurations incorporate the state of a global monitor and of a local monitor, respectively.

We use global configurations to model global snapshots during a program run. Formally, a global configuration $\text{gcnf}$ is a quadruple $\langle \text{pool, mem, } \tau, \text{gmon} \rangle$, where $\text{pool} \in \text{PSt}$, $\text{mem} \in \text{Mem}$, $\tau \in \text{Tr}$, and $\text{gmon} \in \text{GMon}$. In the global configuration $\text{gcnf}$, the pool state $\text{pool}$ captures the local state of each thread of the multi-threaded program, the memory state $\text{mem}$ contains the content of all memory locations, the trace $\tau$ captures the inputs and outputs that have occurred so far, and $\text{gmon}$ captures the internal state of the global monitor.

We use local configurations to capture the local view of individual threads during a run. Formally, a local configuration $\text{lcnf}$ is a triple $\langle \text{thread, mem, } \tau \rangle$ where $\text{thread} \in \text{ThSt}$, $\text{mem} \in \text{Mem}$, and $\tau \in \text{Tr}$. While the thread state $\text{thread}$ models a thread’s local state, $\text{mem}$ and $\tau$ model the content of the memory and the prior communications, respectively.

In a global configuration $\langle \text{pool, mem, } \tau, \text{gmon} \rangle$, the local configuration of thread $i$ is $\langle \text{pool}(i), \text{mem}, \tau \rangle$.

We use command configurations to define the local effects of computation steps. Formally, a command configuration $\text{ccnf}$ is a triple $\langle \text{com, mem, } \tau \rangle$, where $\text{com} \in \text{Com}$, $\text{mem} \in \text{Mem}$, and $\tau \in \text{Tr}$. While $\text{com}$ models a thread’s internal state, $\text{mem}$ and $\tau$ model the memory content and the prior communications, respectively. The command configuration of a local configuration $\langle \text{com, lmon, mdst} \rangle$, mem, $\tau \rangle$ is $\langle \text{com, mem, } \tau \rangle$.

We use $\text{Gcnf}$, $\text{Lcnf}$, and $\text{Ccnf}$ to denote the set of all global, local, and command configurations, respectively.

**JUDGMENTS:** For capturing the effects of computation steps at the level of global, local, and command configurations, respectively, we employ the following three judgments:

$$\text{gcnf} \rightarrow_{\sigma} \text{gcnf}' \quad \text{lcnf} \xrightarrow{\beta, \gamma, \delta}_{\sigma} \text{lcnf}' \quad \text{ccnf} \xrightarrow{\alpha, \gamma}_{\sigma} \text{ccnf}'$$

A strategy $\sigma \in \Sigma$ appears as a subscript of the arrow in all three judgments. It captures the communication strategy of the program’s environment. For instance, the first judgment captures that a transition from a global configuration $\text{gcnf}$ to a global configuration $\text{gcnf}'$ is possible under the strategy $\sigma$. The arrow in the second judgment carries three additional annotations: a global event $\beta \in \text{GEv}$ and two lists of annotations $\gamma, \delta \in \text{Ann}^*$ that serve different purposes. While $\gamma$ captures which changes to its mode state a thread desires, $\delta$ captures which mode state changes are imposed on the thread in response. The arrow in the third judgment carries as annotations a local event $\alpha \in \text{Ev}$ and a list of annotations $\gamma \in \text{Ann}^*$ that captures which changes to its mode state a thread desires.

In the remainder of this section, we provide calculi for the judgments $\text{lcnf} \xrightarrow{\beta, \gamma, \delta} \text{lcnf}'$ and $\text{ccnf} \xrightarrow{\alpha, \gamma} \text{ccnf}'$. An exemplary calculus for the judgment $\text{ccnf} \xrightarrow{\alpha, \gamma} \text{ccnf}'$ is provided in Section VI together with an instantiation of $\text{Com}$.

As usual, these calculi induce transition relations. For instance, the calculus for local configurations induces a family of transition relations $(\beta, \gamma, \delta)_{\sigma} \in \text{GEv}, \gamma, \delta \in \text{Ann}^*, \sigma \in \Sigma$, where $(\beta, \gamma, \delta)_{\sigma}$ relates two local configurations $\text{lcnf}$ and $\text{lcnf}'$ iff $\text{lcnf} \xrightarrow{\beta, \gamma, \delta} \text{lcnf}'$ is derivable. We use the usual notation for the reflexive, transitive closure of such relations, i.e., for instance, $(\beta, \gamma, \delta)^*_{\sigma}$ denotes the reflexive, transitive closure of $(\beta, \gamma, \delta)_{\sigma}$.

**Local Transitions:** The judgment $\text{lcnf} \xrightarrow{\beta, \gamma, \delta} \text{lcnf}'$ defines which transitions between local configurations are possible. The only rule for deriving instances of this judgment is depicted in Fig. [I].

The judgment for transitions between command configurations in the first premise of the rule in Fig. [I] reflects that a thread’s behavior is determined by the command that this thread is executing. In this article, we employ local monitors to guard the behavior of individual threads. Accordingly, the judgment for transitions between local monitor states is used in the second premise to capture that the local monitor must deem the step acceptable. Note that the transition between local monitor states is based on $\delta$ and not on $\gamma$, i.e., it is based on the mode state changes imposed on the thread, not on the mode state changes requested by the thread. Also note that $\delta$ is used to update the mode state in the third premise. The global event $\beta$ is extracted from the local event $\alpha$ using the function $\chi : \text{Ev} \rightarrow \text{GEv}$ in the last premise of the rule.

This fact is explicated by the following proposition.

**Proposition 2.** Let $\text{com}, \text{com}' \in \text{Com}$, $\text{lmon}, \text{lmon}' \in \text{LMon}$, $\text{mdst}_1, \text{mdst}'_1, \text{mdst}_2, \text{mdst}'_2 \in \text{MdSt}$, $\text{mem}, \text{mem}' \in \text{Mem}$, $\tau, \tau' \in \text{Tr}$, $\beta \in \text{GEv}$, $\gamma, \delta \in \text{Ann}^*$, and $\sigma \in \Sigma$ be arbitrary.

If

$$\langle \text{com}, \text{lmon}, \text{mdst}_1 \rangle, \text{mem}, \tau \rangle \xrightarrow{\beta, \gamma, \delta}_{\sigma} \langle \text{com}', \text{lmon}', \text{mdst}_1' \rangle, \text{mem}', \tau' \rangle$$

is derivable then there exists a mode state $\text{mdst}'_2 \in \text{MdSt}$ such that

$$\langle \text{com}, \text{lmon}, \text{mdst}_2 \rangle, \text{mem}, \tau \rangle \xrightarrow{\beta, \gamma, \delta}_{\sigma} \langle \text{com}', \text{lmon}', \text{mdst}'_2 \rangle, \text{mem}', \tau' \rangle$$

is derivable.
Proof: The mode state before a transition between local configurations can neither prevent the transition nor affect the thread state, memory, or trace after the transition according to the rule in Fig. 1.

Global Transitions: Transitions between global configurations are captured by the judgment \( gcnf \rightarrow^\sigma gcnf' \). To simplify our presentation, we make three restrictions. First, we assume that the process structure is static, i.e., if \( \langle pool, mem, \tau, gmon \rangle \rightarrow^\sigma \langle pool', mem', \tau', gmon' \rangle \) then \( \text{pre}(pool') = \text{pre}(pool) \). Second, we assume that threads are scheduled nondeterministically. Third, as stated before, we assume that thread synchronization is the only synchronization primitive and that threads only request changes to their mode state when they pass a barrier.

The two rules for deriving instances of the judgment \( gcnf \rightarrow^\sigma gcnf' \) are depicted in Fig. 2. The first rule captures steps by individual threads, and the second rule captures a barrier synchronization across all threads.

According to the first rule in Fig. 2 an alive thread \( i \) (first premise) may be chosen nondeterministically to perform a computation step (second premise). This step must not involve synchronization (third premise), must neither request nor impose mode state changes (fourth premise), and must not affect the global monitor state (fifth premise). Such a step by an individual thread might affect the thread’s control state, the state of the local monitor supervising this thread, the shared memory, and the trace. It cannot affect the mode state of this thread (since \( \delta = \epsilon \)), the local states of other threads (conclusion of the rule), and the global monitor state (since \( gmon' = gmon \)).

The second rule in Fig. 2 captures synchronization steps. This rule requires that all alive threads jointly pass a barrier (last premise of the rule), which faithfully reflects the intuition of a barrier synchronization. In order to perform a synchronization step, at least one thread must be alive (first premise). The function \( \Gamma \) captures which mode state changes are requested by the individual alive threads (last premise). That is, \( \Gamma(i) \) is the list of annotations capturing the mode state changes requested by the \( i \)th thread, where \( i \in \text{alive}(pool) \). The function \( \Delta \) captures the mode state changes that are imposed on the individual threads (last premise). Which mode state changes are imposed on the individual threads in response to their requests is determined by the global monitor (third premise). Note that the set of threads cannot change during a synchronization step (fourth premise) and that the thread states of terminated threads remain unmodified (fifth premise). Also note that a synchronization step cannot affect the shared memory or the trace (conclusion of the rule). Synchronization steps can only affect the thread state of all alive threads and the state of the global monitor (conclusion of the rule).

Reachability: We say that a global configuration \( gcnf' \) is reachable from a global configuration \( gcnf \) under a strategy \( \sigma \) iff \( gcnf \rightarrow^\sigma gcnf' \) holds. We assume that runs of multi-threaded programs start in an initial memory \( mem_{init} \) that assigns a dedicated value \( v_{init} \) to all variables (i.e., \( \forall x \in \text{Var} : mem_{init}(x) = v_{init} \)) and with an empty initial trace \( \tau_{init} \) (i.e., \( \tau_{init} = \epsilon \)). We say that a global configuration \( gcnf' \) is reachable from a pool state \( pool \) under a strategy \( \sigma \) iff \( gcnf' \) is reachable from the global configuration \( \langle pool, mem_{init}, \tau_{init}, gmon_{init,n} \rangle \) under \( \sigma \), where \( n = |\text{pre}(pool)| \). We use \( \text{reach}_{\sigma}(gcnf) \) and \( \text{reach}_{\sigma}(pool) \) to denote the set of all global configurations reachable from \( gcnf \in Gcnf \) and \( pool \in Psst \), respectively, under \( \sigma \in \Sigma \).

IV. Semantics of Modes

We formally define what it means for a thread to provide a guarantee and what it means for an assumption to be justified. While we define the semantics of G-NR and G-NW in terms of transitions between local configurations, we define the semantics of A-NR and A-NW for a given thread in terms of guarantees given by other threads in a global configuration. Based on the formal semantics of modes, we define conditions that allow one to soundly exploit assumptions when reasoning about the behavior of multi-threaded programs.

Semantics of G-NW and G-NR: We say that a local configuration \( lcnf = \langle thread, mem, \tau \rangle \) provides the no-write guarantee for a variable \( x \) iff the value of \( x \) will remain unmodified by each next possible step of the thread. This
We say that \( \forall \sigma \in \Sigma : \forall \beta \in GEV : \forall \gamma, \delta \in Ann^* : \forall (\text{thread}', \text{mem}', \tau') \in L\text{CNF} : \langle \text{thread}, \text{mem}, \tau \rangle \xrightarrow{\beta, \gamma, \delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \implies \text{mem}'(x) = \text{mem}(x) \) 

We say that a local configuration \( \text{lcnf} = \langle \text{thread}, \text{mem}, \tau \rangle \) provides the no-read guarantee for a variable \( y \) iff
\[
\forall \sigma \in \Sigma : \forall \beta \in GEV : \forall \gamma, \delta \in Ann^* : \forall (\text{thread}', \text{mem}', \tau') \in L\text{CNF} : \langle \text{thread}, \text{mem}, \tau \rangle \xrightarrow{\beta, \gamma, \delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \\
\forall v \in \text{Val} : \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta, \gamma, \delta}_\sigma \langle \text{thread}', \text{mem}'[y \mapsto v], \tau' \rangle \\
\forall v \in \text{Val} : \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta, \gamma, \delta}_\sigma \langle \text{thread}', \text{mem}'[y \mapsto v], \tau' \rangle
\]

That is, a no-read guarantee for a variable \( y \) ensures that changing the value of \( y \) before a computation step does not alter the effects of this computation step. The two disjuncts on the right-hand side of the implication correspond respectively to the cases where \( y \) is overwritten and where \( y \) is not overwritten in a computation step.

A local configuration \( \text{lcnf} = \langle \text{com}, \text{lmon}, \text{mdst}, \text{mem}, \tau \rangle \) provides its guarantees iff it provides the no-write guarantee for each variable \( x \in \text{mdst}(G\text{-NW}) \) and the no-read guarantee for each \( y \in \text{mdst}(G\text{-NR}) \). Consequently, if \( \text{lcnf} \) provides its guarantees then the values of all variables in \( \text{mdst}(G\text{-NW}) \) will remain unchanged by the thread’s next step and the values of all variables in \( \text{mdst}(G\text{-NR}) \) will not affect the thread’s next step. We say that a thread state \( \text{thread} \) provides its guarantees iff, for all \( x \in \text{Mem} \) and all \( \tau \in \text{Tr} \), the local configuration \( \langle \text{thread}, \text{mem}, \tau \rangle \) provides its guarantees. Moreover, we say that \( \text{gcnf} = \langle \{\text{pool}, \text{mem}, \tau, \text{gmon}\} \rangle \) provides its guarantees iff \( \text{pool}(i) \) provides its guarantees for all \( i \in \text{pre}(\text{pool}) \).

Semantics of A-NW and A-NR: Given a global configuration \( \text{gcnf} = \langle \{\text{pool}, \text{mem}, \tau, \text{gmon}\} \rangle \), we say that \( \text{gcnf} \) justifies the assumption A-NW of a thread \( i \in \text{pre}(\text{pool}) \) about a variable \( x \) iff every other alive thread has acquired the mode G-NW for \( x \). Similarly, we say that \( \text{gcnf} \) justifies the assumption A-NR of a thread \( i \in \text{pre}(\text{pool}) \) about a variable \( y \) iff every other alive thread has acquired the mode G-NR for \( y \).

A global configuration \( \text{gcnf} = \langle \{\text{pool}, \text{mem}, \tau, \text{gmon}\} \rangle \) justifies its assumptions iff \( \text{gcnf} \) justifies both the assumption A-NW about each variable in \( \text{pool}(i).\text{mdst}(\text{A-NW}) \) and the assumption A-NR about each variable in \( \text{pool}(i).\text{mdst}(\text{A-NR}) \) for all \( i \in \text{pre}(\text{pool}) \), or formally:
\[
\forall i \in \text{pre}(\text{pool}) : \forall x \in \text{Var} : \forall \text{mod} \in \text{Asm} : x \in (\text{pool}(i).\text{mdst}(\text{mod})) \implies \forall j \in (\text{alive}(\text{pool}) \setminus \{i\}) : x \in (\text{pool}(j).\text{mdst}(\text{invert}(\text{mod})))
\]

Note that assumptions of all threads, including terminated threads, must be justified. In contrast, only alive threads need to explicitly provide the dual guarantees for assumptions of other threads. Terminated threads need not acquire the modes G-NW and G-NR because, it is clear that they will not be able to write or read variables in the future.

**Sound use of modes:** We say that a global configuration \( \text{gcnf} \) ensures a sound use of modes iff, for each strategy \( \sigma \in \Sigma \), each reachable global configuration \( \text{gcnf}' \in \text{reach}_\sigma(\text{gcnf}) \) provides its guarantees and justifies its assumptions.

Moreover, we say that a pool state \( \text{pool} \) ensures a sound use of modes iff \( \langle \{\text{pool}, \text{mem}_{\text{init}}, \tau_{\text{init}}, \text{gmon}_{\text{init}, n}\} \rangle \) ensures a sound use of modes. If a pool state \( \text{pool} \) ensures a sound use of modes then, at each intermediate state during each possible run, each assumption made is justified by guarantees that are, indeed, provided. Hence, all assumptions made can be exploited soundly when reasoning about possible behaviors.

V. A Monitoring Framework

We propose a framework for monitoring multi-threaded programs based on our model of computation from Section III. Our monitoring framework consists of the definition of a global monitor and of a local monitor. The role of these monitors is complementary. Our global monitor ensures that assumptions made by threads are, indeed, justified. Our local monitor ensures that an individual thread, indeed, provides the guarantees that it promises to provide. The combination of one global monitor and a local monitor at each thread jointly ensure a sound use of modes and, hence, the soundness of modular, rely-guarantee-style reasoning about the behavior of multi-threaded programs.

Our local monitor can be specialized to enforce additional properties. By exploiting assumptions, local monitors can not only establish properties of individual threads, but also global properties of entire multi-threaded programs. We present a specialization of local monitors for information-flow control in Section VII and demonstrate that this specialization soundly enforces end-to-end information-flow security.

**Global Monitoring of Multi-threaded Programs:** We define a global monitor that grants all acquisitions and releases of assumptions exactly as desired by each thread. In addition, our global monitor ensures that all assumptions of all threads are justified. To justify all assumptions, guarantees might be needed that differ from the guarantees that the individual threads desire to provide. Consequently, our global monitor cannot always grant modifications of guarantees according to these desires.

Our global monitor keeps track of both the assumptions that each alive thread currently makes and the assumptions that each terminated thread had made when it terminated. Formally, a global monitor state for \( n \) threads is a function that returns a mode state for each thread identifier in \( \{0, \ldots, n-1\} \). Accordingly, we instantiate the set of all global monitor states for \( n \) threads by \( G\text{Mon}_n = \{0, \ldots, n-1\} \rightarrow \text{MdSt} \) and \( g\text{mon}_{\text{init}, n} \in G\text{Mon}_n \), the initial global monitor state for \( n \) threads, by \( g\text{mon}_{\text{init}, n}(i) = \{\} \) for all \( i \in \{0, \ldots, n-1\} \).

We say that a global monitor state \( g\text{mon} \in G\text{Mon} \) (recall \( G\text{Mon} = \bigcup_{n \in \mathbb{N}_0} G\text{Mon}_n \)) is compatible with a pool state \( \text{pool} \in \text{PSt} \) iff \( \text{pre}(g\text{mon}) = \text{pre}(\text{pool}) \) and
\[
gmon' = gmon\text{-update}(gmon, \Gamma)
\]
\[
\text{pre}(\Gamma) \subseteq \text{pre}(gmon) \quad \Delta = \text{gmon\text{-impose}}(gmon', \Gamma)
\]
\[
gmon \rightarrow \Gamma \cdot \Delta \cdot gmon'
\]

Fig. 3. Transitions between global monitor states

if \((gmon(i))(\text{mod}) = (\text{pool}(i)).\text{mdst}(\text{mod})\) for each \(i \in \text{pre(pool)}\) and \(\text{mod} \in \text{Asm}\).

When threads modify their mode state, the global monitor state is updated accordingly. To capture such updates of the global monitor state, we define the function \(gmon\text{-update} : (\text{GMon} \times (\text{N}_0 \rightarrow \text{Ann}^*)) \rightarrow \text{GMon}\) by

\[
gmon\text{-update}(gmon, \Gamma) =
\]
\[
\begin{align*}
\text{let } & NW = \lambda i \in \text{pre}(gmon)' : \\
& \bigcup \{(gmon'(j)) \cdot (\text{A\cdotNW}) \mid j \in \text{pre}(gmon') \setminus \{i\}\} \\
& NR = \lambda i \in \text{pre}(gmon)' : \\
& \bigcup \{(gmon'(j)) \cdot (\text{A\cdotNR}) \mid j \in \text{pre}(gmon') \setminus \{i\}\}
\end{align*}
\]
\[
in \lambda i \in \text{pre}(\Gamma) : (\Gamma(i) \upharpoonright \text{Asm}) \\
\cdot \text{acq}(\text{G\cdotNW, NW}(i)) \cdot \text{acq}(\text{G\cdotNR, NR}(i)) \\
\cdot \text{rel}(\text{G\cdotNW, pre}(gmon') \cdot \text{NW}(i)) \\
\cdot \text{rel}(\text{G\cdotNR, pre}(gmon') \cdot \text{NR}(i))
\]

For each pair \((gmon', \Gamma)\) with \(\text{pre}(\Gamma) \subseteq \text{pre}(gmon')\), the function \(\text{gmon\text{-impose}}\) is well defined and returns a function with the same pre-image as \(\Gamma\).

Fig. 3 presents our global monitor. It is the only rule for deriving instances of the judgment for transitions between global monitor states. In the first premise of this rule, the function \(\text{gmon\text{-update}}\) is used to update the global monitor state based on the acquisitions and releases of assumptions in \(\Gamma\). The second premise ensures that the global monitor is aware of all threads that request mode state changes. In the third premise, the function \(\text{gmon\text{-impose}}\) is used to determine \(\Delta\), i.e., the mode state changes to be imposed on all threads that requested mode state changes. Due to the second premise of the rule, \(\text{gmon\text{-impose}}\) is well defined for the arguments used.

Note that, for each \(i \in \text{pre}(gmon')\) and each assumption in \(gmon'(i)\), the corresponding guarantee is acquired in \((\text{gmon\text{-impose}}(gmon', \Gamma))(j)\) for all \(j \in \text{pre}(\Gamma) \setminus \{i\}\). That is, programs do not need to explicitly contain annotations to acquire guarantees, since guarantees will be imposed on threads if needed. This means that no program analysis or human effort is required to determine the guarantees that threads provide. Annotations for assumptions, however, do need to be provided explicitly. There are practical analyses that can infer, e.g., whether a memory location is thread-local (i.e., exclusively accessed by a thread). Such analyses might be suitable building blocks to infer assumption annotations for programs.

Note also that guarantees are acquired in \((\text{gmon\text{-impose}}(gmon', \Gamma))(i)\) even if the \(i\)th thread is providing these guarantees already. Analogously, guarantees are released even if the \(i\)th thread is not providing them. Such unnecessary acquisitions and releases of guarantees could be avoided by letting the global monitor keep track of the guarantees that the individual threads provide. We refrain from elaborating this optimization here in more detail.

**Local Monitoring of Individual Threads:** Our local monitor keeps track of assumptions that a monitored thread makes and of guarantees that a thread provides. In this section, we assume that the state of a local monitor incorporates a mode state, but otherwise leave local monitor states under-specified. We use \(\text{lmon\cdotmdst}\) to denote the mode state within \(\text{lmon} \in \text{LMon}\), and we say that a thread state \([\text{com, lmon, mdst}]\) is well formed iff \(\text{lmon\cdotmdst} = \text{mdst}\) holds.

We say that a calculus for local monitor transitions properly tracks modes iff the derivability of \(\text{lmon} \longrightarrow_{\text{perm}} \text{lmon}'\) implies \(\text{lmon\cdotmdst} = \text{update}(\text{lmon\cdotmdst}, \delta)\). Moreover, we say that a calculus for local monitor transitions enforces guarantees iff it ensures that every well-formed thread state provides its guarantees. We leave the calculus for local monitor transitions unspecified. Such a calculus and a concrete definition of \(\text{LMon}\) are provided in Section VII.

**Sound Use of Modes:** We say that a global configuration \(\text{gcnf} = \{\text{pool, mem, } \tau, \text{gmon}\}\) is well formed iff \(\text{gmon}\) is compatible with \text{pool} and \text{pool}(i) is a well-formed thread state for each \(i \in \text{pre(pool)}\).

The following theorem states that our framework soundly enables rely-guarantee-style reasoning. This result is conditional on two assumptions about the calculus for local monitor transitions, which we discharge in Section VII (see Theorem 2).

**Theorem 1.** Let \(\text{gcnf}\) be a well-formed global configuration that justifies its assumptions. If the calculus for local monitor transitions properly tracks modes and enforces guarantees then \(\text{gcnf}\) ensures a sound use of modes.

**Proof sketch:** Well-formedness is an invariant for global configurations and justifying all assumptions is an invariant for well-formed global configurations if the calculus for local monitor transitions properly tracks modes. From these invariants, we conclude that every global configuration \(\text{gcnf}'\) that is reachable from \(\text{gcnf}\) is also well formed and justifies its assumptions by induction on the number of steps from \(\text{gcnf}\) to \(\text{gcnf}'\). From the well-formedness of \(\text{gcnf}'\) and the assumption that the calculus for local monitor transitions enforces guarantees, we conclude that \(\text{gcnf}'\) provides its guarantees. Hence, \(\text{gcnf}\) ensures a sound use of modes.
A detailed proof of Theorem \[\text{I}\] appears in Appendix \[\text{D}\].

VI. Example Programming Language

As an example language, we use a simple concurrent imperative language that supports multi-threading, communication between threads using shared memory, coordination between threads using barrier synchronization, and interaction between a program and its environment using channels.

Expressions: We use \(\text{Exp}\) to denote the set of expressions in our language and leave this set under-specified. We assume that the expressions are free of side effects, and use judgment \(e,\text{mem} \downarrow v\) to model that \(e \in \text{Exp}\) evaluates to \(v \in \text{Val}\) in memory state \(\text{mem} \in \text{Mem}\). Function \(\text{vars} : \text{Exp} \rightarrow P(\text{Var})\) retrieves from an expression \(e \in \text{Exp}\) a set of variables that contains all variables that the value of \(e\) might depend on. That is, for all \(e \in \text{Exp}\) and \(\text{mem},\text{mem}' \in \text{Mem}\), we have

\[
(\forall x \in \text{vars}(e) : \text{mem}(x) = \text{mem}'(x)) \Rightarrow (e,\text{mem} \downarrow v) \Rightarrow (e,\text{mem}' \downarrow v)
\]

Commands: The set of commands \(\text{Com}\) is defined by:

\[
\text{com} ::= x := e | \text{skip} | \text{com} ; \text{com}.
\]

if \(e\) then \(\text{com}\) else \(\text{com}\) fi | while \(e\) do \(\text{com}\) od | input \(ch\) to \(x\) | output \(e\) to \(ch\) | //\(\gamma//\) barrier | stop | join | more \(e\) do \(\text{com}\) od | term

where \(x \in \text{Var}, e \in \text{Exp}, ch \in \text{Ch}, \) and \(\gamma \in \text{Ann}^*\). Terms of the form \(\text{stop}\), \(\text{join}\), more \(e\) do \(\text{com}\) od, and term capture snapshots of the control state at intermediate computation points, and are not meant to be part of the surface syntax. The sub-language without these terms is the programming language to be used by a programmer.

The behavior of assignments, skip, semicolon, conditionals, and loops is as usual. A command input \(ch\) to \(x\) reads the next input from the channel \(ch\) into the variable \(x\), and a command output \(e\) to \(ch\) sends the value of the expression \(e\) on the channel \(ch\). The command \text{barrier} causes a thread to block until all non-terminated threads jointly pass the barrier. Annotations that request a mode state change are placed as a comment in front of barrier commands as, e.g., in //\(e\)-aq(A-NR, \(x//)//\) barrier; skip. The control state stop models that the execution of a subprogram has completed. The control state join models that the join point of a conditional has been reached. The control state more \(e\) do \(\text{com}\) od models that a loop with the guard \(e\) and the body \(\text{com}\) has been entered and that it will be decided next whether to execute the body or to leave the loop. Finally, the control state term models that the execution of an entire program has terminated.

Local Events: We define the set of local events \(E_v\) for our example language by the grammar:

\[
\alpha ::= a(x,e) | s | b(e,\text{com}_1,\text{com}_2) | \text{join} | \text{enter}(e,\text{com}) | \text{more}(e,\text{com}) | \text{leave}(e,\text{com}) | \text{input}(x,ch,v) | \text{output}(ch,e,v) | \text{sync} | \text{term}
\]

and \(\chi : E_v \rightarrow GE_v\), the abstraction function from local events to global events, as follows:

\[
\chi(\alpha) = \begin{cases} 
sync & \text{if } \alpha = \text{sync} \\
\epsilon & \text{otherwise} \end{cases}
\]

A local event \(a(x,e)\) models that the value of \(e \in \text{Exp}\) is being assigned to \(x \in \text{Var}\). The local event \(s\) models that a skip command is being executed. A local event \(b(e,\text{com}_1,\text{com}_2)\) models that a conditional with guard \(e \in \text{Exp}\) and the branches \(\text{com}_1\) and \(\text{com}_2\) is being executed, where \(\text{com}_1\) and \(\text{com}_2\) are the “then” and “else” branches respectively. The local event \text{join} models that the join point of a conditional is being passed. The local event \text{enter}(e,\text{com})\) models that a loop while \(e\) do \(\text{com}\) od is being entered. A local event more\((e,\text{com})\) models that the guard \(e \in \text{Exp}\) of a loop with body \(\text{com}\) has evaluated to a non-zero value, and the loop body \(\text{com}\) is being entered. The local event \text{leave}(e,\text{com})\) models that a loop with guard \(e \in \text{Exp}\) and with body \(\text{com}\) is being left. A local event \text{output}(ch,e,v)\) models that a value \(v \in \text{Val}\) resulting from the evaluation of expression \(e \in \text{Exp}\) is being output to channel \(ch \in \text{Ch}\). A local event \text{input}(x,ch,v)\) models that \(v \in \text{Val}\) is being received from \(ch \in \text{Ch}\) and stored in \(x \in \text{Var}\). The local events \text{sync} and \text{term} model that a barrier is being passed and that the thread is about to terminate, respectively.

Note that our language for local events closely resembles the syntax of our programming language, though there is no one-to-one correspondence. Note also that some of our local events capture information that goes beyond the actual next computation step. For instance, \(b(e,\text{com}_1,\text{com}_2)\) provides complete information about both branches of a conditional. That is, this local event captures information about the next computation steps, about computation steps that will occur sometime in the future, and about computation steps that would have occurred if the control flow were resolved differently.

Formal Semantics: Fig. 4 shows selected inference rules for the calculus that defines which transitions on command configurations are possible. The first two rules capture the execution of assignments and skip. The third rule captures the passing of a barrier. The fourth rule captures the choice of a branch in a conditional. It inserts join into the resulting control state to mark the join point. The fifth rule captures the passing of a join point. All rules are shown in Appendix B including rules for sequential composition, loops, input and output, and termination of programs.

The requested mode state change, i.e., the first label on the arrow, is empty (i.e., \(\gamma = \epsilon\)) in the conclusions of all rules except for in the rule for barriers and one of the rules for sequential composition. In the rule for barriers, the list of annotations is retrieved from the comment that precedes the barrier command. In the sequential composition rule, the list of annotations is simply propagated from the premise to the conclusion. This reflects our simplifying assumption from Section III-B that threads request mode state changes only when synchronizing with other threads.

VII. Enforcing Information Flow Security through Local Monitoring

We present our novel hybrid approach to establish information-flow security for multi-threaded programs, building on our monitoring framework from Section C. We specialize our generic local monitor definition to a monitor that tracks...
and controls information flow. This specialization satisfies the requirements of Section [V] modes are properly tracked and guarantees are enforced. Our solution does not require any modification of the global monitor definition from Section [V].

We capture information-flow requirements by multi-level security policies and prove the soundness of our approach with respect to a knowledge-based definition of information-flow security à la [22]. We are able to establish such an end-to-end security property through thread-local checks by exploiting the assumptions that a thread makes about its environment. The ability to perform rely-guarantee-style reasoning about information-flow security within local monitors of individual threads is a distinctive technical feature of our approach. The practical value of this feature is that it substantially improves precision of local monitoring. Without being able to exploit assumptions, a local monitor would have to conservatively secure the guarded thread for all possible environments, resulting in severe restrictions on the behavior of threads.

The knowledge-based security definition requires that an attacker cannot distinguish a given program run from certain other hypothetical runs. To perform such counter-factual reasoning, information about other possible runs is needed within local monitors. This information is provided by the local events that are emitted during steps of a thread. For instance, the evaluation of the guard of a conditional emits a local event \( b(e, com_1, com_2) \), which provides information about the guard and both branches of the conditional. That is, the approach to information-flow security proposed in this section is a hybrid approach.

### A. Information-Flow Security

A security policy is a tuple \( SP = (\text{Lev}, \sqsubseteq, \sqcup, \sqcap) \) consisting of a set of security levels \( \text{Lev} \), a partial order \( \sqsubseteq \subseteq \text{Lev} \times \text{Lev} \), a least-upper-bound operator \( \sqcup : (\text{Lev} \times \text{Lev}) \rightarrow \text{Lev} \), and a least security level \( \sqcap \in \text{Lev}. A \text{domain assignment} is a function \( \text{chlev} : \text{Ch} \rightarrow \text{Lev} \) that associates a security level with each channel. Intuitively, the security level \( \text{chlev}(ch) \) of a channel \( ch \) is the upper bound on the confidentiality of information that the channel’s endpoint (e.g., a user or another system) is permitted to learn. Thus, \( \text{chlev}(ch) \) constitutes an upper bound on the confidentiality of information that might be received from \( ch \) and of information that may be sent over \( ch \). When it is clear from context, we conflate channels with their security levels, and write, e.g., \( ch \sqsubseteq \ell \) instead of \( \text{chlev}(ch) \sqsubseteq \ell \).

**Attacker Model:** We assume that each attacker is associated with a security level, where an attacker at level \( \ell \) can observe all interactions on channels \( ch \) with \( ch \sqsubseteq \ell \), but cannot observe interactions on other channels. To express what an attacker at level \( \ell \) observes during a program run, we project the trace emitted during the run to the level \( \ell \). We define the projection of trace \( \tau \) to security level \( \ell \) by

\[
\tau \downarrow \ell \triangleq \text{filter}(\tau, \{\text{inp}(ch, v), \text{out}(ch, v) \mid ch \in \text{Ch}, \text{chlev}(ch) \sqsubseteq \ell, v \in \text{Val}\})
\]

That is, if \( \tau \) is the trace produced by some run of a multi-threaded program then an attacker at level \( \ell \) observes \( \tau \downarrow \ell \).

Based on his observations, an attacker can try to infer information about the communication strategy used. To capture an upper bound on the attacker’s knowledge about which communication strategy might be in use, we define the function \( \kappa : (\text{Lev} \times \text{PSt} \times \text{Tr}) \rightarrow \text{P}(\Sigma) \) by

\[
\kappa(\ell, \text{pool}, \tau) = \left\{ \sigma \in \Sigma \mid \exists \langle p, \text{mem}', \tau', \text{gmon}' \rangle \in \text{reach}_\sigma(\text{pool}) : \tau' \downarrow \ell = \tau \downarrow \ell \right\}
\]

The set \( \kappa(\ell, \text{pool}, \tau) \) contains all strategies that are compatible with the observation \( \tau \downarrow \ell \) and that thus, from the perspective of an attacker at level \( \ell \), might be in use. The smaller the set \( \kappa(\ell, \text{pool}, \tau) \), the more accurate the attacker’s knowledge. The longer the trace that the attacker observes, the more accurate is the attacker’s knowledge, i.e., attacker knowledge is monotonic in the length of the trace the attacker observes.

Note that our definition of \( \kappa \) conservatively allows an attacker to know the program. That is, \( \kappa(\ell, \text{pool}, \tau) \) is an upper bound on the knowledge of an attacker at level \( \ell \) after this attacker observes trace \( \tau \), even if the attacker knows the program that is contained in state \( \text{pool} \). However, we assume the attacker has no a-priori knowledge about which strategy is used.

**Security Property:** We regard strategies as confidential information. An attacker at level \( \ell \) should not be able to distinguish two strategies that provide identical inputs at level \( \ell \) and below when all prior interactions at level \( \ell \) and below are identical. We capture classes of strategies that should be indistinguishable by the notion of \( \ell \)-equivalence, defined by

\[
\sigma_1 =_\ell \sigma_2 \triangleq \forall ch \in \text{Ch} \colon \forall \tau_1, \tau_2 \in \text{Tr} : (ch \sqsubseteq \ell \land \tau_1 \downarrow \ell_1 = \tau_2 \downarrow \ell_2) \implies \sigma_1(\tau_1, ch) = \sigma_2(\tau_2, ch)
\]

We use a knowledge-based definition of information-flow security, inspired by [22]. The property that we define is progress-sensitive and suitable for our model from Section [III].

**Definition 1.** We say that a pool state \( \text{pool} \in \text{PSt} \) is secure for a level \( \ell \in \text{Lev} \) iff

\[
\forall \sigma \in \Sigma : \forall \langle p, \text{mem}', \tau', \text{gmon}' \rangle \in \text{reach}_\sigma(\text{pool}) : \ldots
\]
\[ \kappa(\ell, \text{pool}, \tau') \supseteq \{ \sigma' \in \Sigma \mid \sigma =_\ell \sigma' \} \]

Our security property requires that if an attacker at level \( \ell \) observes an execution starting in \( \text{pool} \) under strategy \( \sigma \), then his knowledge must be bounded by the set of strategies that are \( \ell \)-equivalent to \( \sigma \). That is, the attacker cannot learn anything about the behavior of the actual strategy on any channel \( ch \not\subseteq \ell \).

### B. A Specialized Local Monitor

Our local monitor is parametric in the security policy \( \text{SP} = (\text{Lev}, \llbracket\cdot\rrbracket, \cup, \downarrow, \downarrow) \) and in the domain assignment \( \text{chlev} : \text{Ch} \rightarrow \text{Lev} \). A third parameter is a function \( \mathcal{L} : \text{Var} \rightarrow \text{Lev} \) that assigns a default security level to each variable. These three parameters must be chosen identically for the local monitors of all threads of a multi-threaded program.

Our local monitor maintains a local copy of the mode state of the guarded thread. As a convention, we use \( \text{lnmdst} \) as a meta-variable for such copies of a thread’s mode state.

**Typing Environment:** Information flow into and out of a variable \( x \) is constrained by the local monitor based on the default security level \( \mathcal{L}(x) \). However, if the guarded thread has exclusive write-access to \( x \) and the thread previously wrote information into \( x \) that is less confidential than \( \mathcal{L}(x) \) then the local monitor can use that. Moreover, if the guarded thread has exclusive read-access to \( x \) then the local monitor may allow the thread to temporarily store information in \( x \) that is more confidential than \( \mathcal{L}(x) \). The local monitor uses a typing environment \( \Gamma \) to track the actual security level of variables for which the guarded thread has exclusive access in some sense. Formally, a typing environment \( \Gamma : \text{FloatVar} \rightarrow \text{Lev} \) where \( \text{FloatVar} \subseteq \text{Var} \). The variables whose security level may float might be limited, for instance, because the run-time environment accesses some variables while relying that they store information of a particular security level (e.g., variables that define thread priorities, accessed by a priority-based scheduler). The set of variables whose security level must not float is \( \text{NonFloatVar} = \text{Var} \setminus \text{FloatVar} \).

We lift a typing environment \( \Gamma : \text{FloatVar} \rightarrow \text{Lev} \) to a total function in \( \text{Var} \rightarrow \text{Lev} \) by

\[ \Gamma(x) = \begin{cases} \Gamma(x) & \text{if } x \in \text{prev}(\Gamma) \\ \mathcal{L}(x) & \text{otherwise} \end{cases} \]

We write \( \Gamma(e) \) for \( \bigcup_{x \in \text{vars}(e)} \Gamma(x) \).

**Mode-State-Says Notation:** To improve readability, we introduce a notation for properties of mode states. We write \( \text{mdst} \triangleright \text{fact} \) (read “\( \text{mdst} \) says \( \text{fact} \)” if mode state \( \text{mdst} \) has the property expressed by a fact from the following language:

- \( \text{mayread}(x) \mid \text{maywrite}(x) \mid \text{exclusiveread}(x) \mid \text{exclusivewrite}(x) \mid \text{othersmightread}(x) \mid \text{othersmightwrite}(x) \)

with \( x \in \text{Var} \). The semantics of \( \text{mdst} \triangleright \text{fact} \) are defined by:

- \( \text{mdst} \triangleright \text{mayread}(x) \triangleq x \not\in \text{mdst}(\text{G-NR}) \)
- \( \text{mdst} \triangleright \text{maywrite}(x) \triangleq x \not\in \text{mdst}(\text{G-NW}) \)
- \( \text{mdst} \triangleright \text{exclusiveread}(x) \triangleq x \in \text{mdst}(\text{A-NR}) \)
- \( \text{mdst} \triangleright \text{exclusivewrite}(x) \triangleq x \in \text{mdst}(\text{A-NW}) \)
- \( \text{mdst} \triangleright \text{othersmightread}(x) \triangleq x \not\in \text{mdst}(\text{A-NR}) \)
- \( \text{mdst} \triangleright \text{othersmightwrite}(x) \triangleq x \not\in \text{mdst}(\text{A-NW}) \)

For brevity, we write \( \text{mdst} \triangleright [\text{fact}_1, \ldots, \text{fact}_n] \) instead of \( \text{mdst} \triangleright \text{fact}_1, \ldots, \text{mdst} \triangleright \text{fact}_n \), a list of mode-state-says statements concerning the same mode state. We write \( \text{mdst} \triangleright \text{mayread}(e) \) instead of \( \text{mdst} \triangleright \text{mayread}(x_1), \ldots, \text{mdst} \triangleright \text{mayread}(x_n) \), where \( \text{vars}(e) = \{x_1, \ldots, x_n\} \).

**Local Monitor States:** A local monitor state is a tuple \( (\Gamma, \text{lnmdst}, \text{pc}, \text{br}, \text{time}, \text{term}, \text{block}) \). Typing environment \( \Gamma : \text{FloatVar} \rightarrow \text{Lev} \) tracks the actual security level of variables to which the guarded thread has exclusive access, as already described. Mode state \( \text{lnmdst} \in \text{MdSt} \) is the local monitor’s copy of the mode state of the guarded thread. The pc stack \( \text{pc} \) and branch environment stack \( \text{br} \) summarize, respectively, the control flow decisions to reach the current program point, and the behavior of the local monitor on execution paths not taken. The pc stack is a stack of security levels, and the branch environment stack is a stack of tuples, described below. Each time the thread enters a conditional or loop, a security level that bounds the control flow decision is pushed on the pc stack, and a tuple that approximates the monitor’s behavior on the branch not taken is pushed on the branch environment stack. When a conditional or loop is exited, the top element of each stack is popped.

To track information flow via internal timing, progress channels, and monitor interventions, local monitor states include timing level \( \text{time} : \text{Lev} \), termination level \( \text{term} : \text{Lev} \), and blocking level \( \text{block} \) is an upper bound on information that influenced termination of loops prior to this point in the guarded thread’s execution. The termination level only increases during thread execution. Blocking level \( \text{block} \) is an upper bound on information that influenced whether the monitor blocked or allowed thread execution prior to this point in the execution. The blocking level captures information flow via monitor interventions (or lack of interventions), and only increases during execution.

Timing level \( \text{time} \) is an upper bound on information since the last synchronization that influenced \( \text{time} \) when the guarded thread reaches its current state. The timing level is lowered after synchronization barriers, but otherwise only increases during execution. The timing level describes the information that may affect the relative timing of this guarded thread with respect to other threads and is used to prevent internal timing leaks.

When a conditional or loop is exited, the timing, termination, and blocking levels are updated to account for information flows due to execution paths that could have been taken, but weren’t. For example, when a loop is exited, the termination level is increased to ensure that it is an upper bound of the information that influenced the loop guard expression, which determines how many times the loop is executed.

**Calculus for Local Monitor Transitions:** Selected inference rules for local monitor transitions are shown in Fig. 5. All inference rules are presented and explained in Appendix C.

Rule (M-Assign) is used for an assignment \( x := e \) when \( x \) is readable by other threads, according to the current mode
M-ASSIGN1
\[ l_{\text{mdst}} \triangleright [\text{maywrite}(x), \text{mayread}(e), \text{othersmightread}(x)] \]
\[ \ell = \Gamma'(e) \sqcup \text{time} \sqcup (\cup \text{perm}) \sqcup \text{term} \sqcup \text{block} \quad \ell \subseteq \mathcal{L}(x) \quad \Gamma' = \Gamma(x \mapsto l_{\text{mdst}} \ell) \]
\[ (\Gamma, l_{\text{mdst}}, \underbrace{\text{pc}, \overline{br}, \text{time}, \text{term}, \text{block}}\text{)} \rightarrow_{\text{perm}}^\ell \langle \Gamma', l_{\text{mdst}}, \text{pc}, \overline{br}, \text{time}, \text{term}, \text{block} \rangle \]

M-ASSIGN2
\[ l_{\text{mdst}} \triangleright [\text{maywrite}(x), \text{mayread}(e), \text{exclusiveread}(x)] \]
\[ \ell = \Gamma'(e) \sqcup \text{time} \sqcup (\cup \text{perm}) \sqcup \text{term} \sqcup \text{block} \quad \ell \subseteq \mathcal{L}(x) \quad \Gamma' = \Gamma(x \mapsto l_{\text{mdst}} \ell) \]
\[ (\Gamma, l_{\text{mdst}}, \underbrace{\text{pc}, \overline{br}, \text{time}, \text{term}, \text{block}}\text{)} \rightarrow_{\text{perm}}^\ell \langle \Gamma', l_{\text{mdst}}, \text{pc}, \overline{br}, \text{time}, \text{term}, \text{block} \rangle \]

M-BARRIER-LOCAL
\[ l_{\text{mdst}} \triangleright \text{exclusivewrite}(x) \quad \Rightarrow \quad \Gamma'(x) = \Gamma(x) \quad \text{iff} \quad \Gamma(x) \sqcup \Gamma' \sqcup \text{exclusivewrite}(x) \]
\[ \text{if} \quad \Gamma(x) \sqcup \text{exclusivewrite}(x) \quad \text{and} \quad l_{\text{mdst}} \triangleright \text{othersemightwrite}(x) \quad \text{and} \quad l_{\text{mdst}} \triangleright \text{exclusiveread}(x) \quad \text{and} \quad \text{exclusivewrite}(x) \]
\[ \Gamma'(x) = \Gamma(x) \quad \text{iff} \quad \Gamma(x) \sqcup \text{exclusivewrite}(x) \quad \text{and} \quad l_{\text{mdst}} \triangleright \text{othersmightwrite}(x) \quad \text{and} \quad \text{exclusivewrite}(x) \]
\[ (\Gamma, l_{\text{mdst}}, \underbrace{\text{pc}, \overline{br}, \text{time}, \text{term}, \text{block}}\text{)} \rightarrow_{\text{sync}}^\gamma \langle \Gamma', l_{\text{mdst}}, \text{pc}, \overline{br}, \perp, \perp, \perp, \perp \rangle \]

Fig. 5. Local monitoring: selected rules

state \( l_{\text{mdst}} \). Level \( \ell \) bounds the information that might be revealed by evaluating \( e \) at this point in the execution: it is influenced by the level of the variables in \( e \), by the decision to execute this command (\( \cup \text{perm} \), the join of the pc stack), the fact that the monitor did not previously block the thread (\( \text{block} \)), the fact that the thread did not previously diverge (\( \text{term} \)), and the relative timing of this thread with respect to others (\( \text{time} \)).

Since other threads might read \( x \), we require that \( \ell \) is bounded above by \( \mathcal{L}(x) \), the default security level of \( x \). Rule (M-Assign2) is similar, but applies when \( x \) cannot be read by other threads, which allows us to treat its level flow-sensitively.

In both cases \( \Gamma' \) is computed using operator \( \Gamma(x \mapsto l_{\text{mdst}} \ell) \) (defined in Fig. 5) that returns an updated environment with the type of variable \( x \) updated to \( \ell \) depending on mode state \( l_{\text{mdst}} \). Both rules require \( x \) to be writable and all variables in \( e \) to be readable according to \( l_{\text{mdst}} \).

Rules (M-Branch) and (M-Join) handle conditionals. Recall that monitor event \( b(e, \text{com}1, \text{com}2) \) and join are emitted, respectively, when a thread enters and exits a conditional if \( e \) then \( \text{com}1 \) else \( \text{com}2 \) fi. Rule (M-Branch) requires that the local monitor’s mode state allows the thread to read the conditional expression: \( l_{\text{mdst}} \triangleright \text{mayread}(e) \). It uses the static bounds oracle function \( SB(\text{com}, \Gamma, l_{\text{mdst}}, \text{pc}, \overline{br}, \text{time}, \text{term}, \text{block}) \) to approximate the behavior of the monitor on both branches. This

is an on-the-fly static analysis (thus making the local monitor hybrid) needed for the soundness of information-flow tracking. Security level \( \ell_{\text{ab}} \) returned by the oracle is an upper bound on the decision about which branch to take, and the monitor pushes it on pc stack \( \text{pc} \). The other elements returned describe, respectively, upper bounds on the timing level, termination level, blocking level, and typing environment that the local monitor would have after completing the conditional. The analysis in essence considers all possible executions of the conditional, and approximates the behavior of the guarded thread in these hypothetical executions. Level \( \ell_{\text{ab}} \) is an upper bound on information that affects whether the execution of the conditional will terminate or diverge, \( \text{block}_{\text{ab}} \) is an upper bound on the information that affects whether the monitor will block the thread while executing the conditional, and \( \Gamma_{\text{ab}} \) describes upper bounds on the typing

\[ \text{else} \]

\[ \text{undef} \]

\[ \text{if} \]

\[ \ell \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]

\[ \ell \sqcup \mathcal{L}(x) \]
environment when the conditional terminates. Note that the oracle is a partial function: if the result is undefined the monitor blocks. For example, the result is undefined if a branch contains a barrier command and the branch condition is not $\perp$, since synchronizations are publicly observable and should not depend on confidential information. A full description of the static bounds oracle (including the oracle’s semantic interface and an implementation) is available in Appendices [E] and [F].

Rule (M-Join) pops the top elements of the pc stack and the static branching environment stack, and updates the variable context, timing level, termination level, and blocking level to account for potential information flows on the branch not taken.

Rule (M-Barrier-Local) regulates when a thread may synchronize. The first premise $((\sqcup \mathcal{P}) \sqcup \text{term} \sqcup \text{block} = \perp)$ ensures that the decision to reach a barrier is influenced only by public information. The second premise computes the updated mode state $\text{Indst}'$. The remaining premises ensure that the typing environments before and after the barrier ($\Gamma$ and $\Gamma'$ respectively) are appropriate based on the access this and other threads may have to variables before and after the barrier.

The other monitor rules, not presented here, are: (M-Skip) (for skip commands); (M-Input1), (M-Input2), and (M-Output) for input and output commands; (M-Enter), (M-More), and (M-Leave) for loops; and (M-Term) for terminated threads.

**Theorem 2.** Our calculus for local monitor transitions properly tracks modes and enforces guarantees.

**Proof sketch:** Given a derivation of $\text{lmon} \rightarrow_{\delta, \gamma} \text{lmon}'$, we show $\text{lmon}' \cdot \text{mdst} = \text{update}(\text{lmon}, \text{mdst}, \delta)$. This implies that our calculus properly tracks modes. To prove that our calculus enforces guarantees, we show that every local configuration $\langle\text{com}, \text{lmon}, \text{mdst}, \text{mem}, \tau\rangle$ with a well-formed thread state provides the no-write guarantee for each variable $x \in \text{mdst}(G-NW)$ and the no-read guarantee for each variable $y \in \text{mdst}(G-NR)$ by a case distinction on the local event emitted when this local configuration performs a step. ■

A detailed proof of Theorem 2 appears in Appendix [D]. From Theorems [7] and [8] we obtain the following corollary.

**Corollary 1.** If a global configuration is well formed and justifies its assumptions then it ensures a sound use of modes.

**Soundness of Information-Flow Control:** Given a multi-threaded program $\text{com}_1 \cdots \text{com}_n \in \text{Com}^*$, we define the initial pool state for this program by

$$\text{pool}_{\text{com}_1 \cdots \text{com}_n} \triangleq \{\text{com}_1, \text{lmon}_\text{init}, \text{mdst}_\text{init}\} \cdots \{\text{com}_n, \text{lmon}_\text{init}, \text{mdst}_\text{init}\}$$

where $\text{mdst}_\text{init}$ is the initial mode state, defined by

$$\text{mdst}_\text{init}(G-NR) = \{\} \quad \text{mdst}_\text{init}(G-NW) = \{\}$$

$$\text{mdst}_\text{init}(A-NR) = \{\} \quad \text{mdst}_\text{init}(A-NW) = \{\}$$

and $\text{lmon}_\text{init}$ is the initial local monitor state, defined by

$$\text{lmon}_\text{init} \triangleq \langle \Gamma_\text{init}, \text{mdst}_\text{init}, \epsilon, \perp, \perp, \perp, \perp \rangle$$

where $\Gamma_\text{init} : \text{FloatVar} \rightarrow \text{Lev}$ is defined by $\text{pre}(\Gamma_\text{init}) = \{\}$.

**Theorem 3.** If $\text{com}_1 \cdots \text{com}_n \in \text{Com}^*$ is a multi-threaded program such that each command $\text{com}_i$ is in the sub-language meant to be used by the programmer (as defined in Section 7) then $\text{pool}_{\text{com}_1 \cdots \text{com}_n}$ is secure for every level $\ell \in \text{Lev}$.

**Proof sketch:** The full proof is in Appendix [E]. Let global configuration $\text{gcnf} = \langle\text{pool}_{\text{com}_1 \cdots \text{com}_n}, \text{mem}_\text{init}, \tau_\text{init}, \text{gmon}_\text{init}\rangle$. Since $\text{gcnf}$ is well-formed and justifies its assumptions, we can soundly use rely-guarantee reasoning based on Definition [1] and Corollary [1]. The rest of the proof is lengthy, but uses established proof techniques. It shows that for any security level $\ell$, given an execution of $\text{gcnf}$ with strategy $\sigma$ that produces trace $\tau$, and given an $\ell$-equivalent strategy $\sigma'$, there exists an execution of $\text{gcnf}$ with $\sigma'$ that produces trace $\tau'$ such that $\tau \downarrow \ell = \tau' \downarrow \ell$. Thus, the knowledge of an attacker at level $\ell$ that observes trace $\tau$ will include both $\sigma$ and $\sigma'$.

When our monitoring framework is instantiated with the information-flow control local monitors, it accepts all the secure executions from Section [II] (with appropriate annotations to indicate assumptions), and correctly rejects the insecure executions (by a local monitor blocking at an appropriate point in the execution). This work presents the first hybrid progress-sensitive information-flow control monitoring framework for concurrent programs that uses fine-grained rely-guarantee reasoning about shared memory. As such, it is the only sound monitoring framework that accepts all secure execution examples from Section [III]. The examples, though simple, exhibit typical patterns of concurrent programs, and of programs that manipulate information of differing sensitivity.

**VIII. RELATED WORK**

**Static enforcement:** Most existing work on information-flow security in concurrent programs uses static techniques. Volpano and Smith [23] provide a type system that enforces probabilistic noninterference in concurrent programs by providing an atomic construct, and preventing high-security loop guards. Russo and Sabelfeld [24] remove the need for the atomic construct under cooperative scheduling. Sabelfeld and Sands [25] provide a type system that ensures probabilistic noninterference for a wide class of schedulers, that also prevents high-security loop guards, and uses Agat’s padding technique to prevent timing leaks [26]. Smith [27] presents a type system that reasons precisely about what information influences the timing of executions, prevents timing leaks, and thus enforces probabilistic noninterference for concurrent programs.

Andrews and Reitman [28] present an axiomatic program logic to reason about information flow in sequential and concurrent programs. They use two special “certification variables” in their logic, local and global, which correspond to the pc level and the termination level respectively.

Boudol and Castellani [29] analyze the program and scheduler, and give a type system such that well-typed schedulers and threads satisfy noninterference, which Barthe and Nieto [30] verify. Barthe et al. [31] add mechanisms during compilation to enable a security-aware scheduler to enforce security.
Sabelfeld [8] considers a concurrent language with semaphores, and provides a type system that enforces security. High loops are not allowed, and padding is used to prevent timing leaks, for both branches and fork commands.

Zdancewic and Myers [18] propose that non-determinism should not be observable by low-security users (including non-determinism arising from scheduling, data races, etc.) and present a type system that enforces low-observational determinism for single memory locations. Their enforcement mechanism allows the pc level to be reset at thread synchronization points, similar to our lowering of the timing level at barrier synchronizations. Huijsman et al. [32] note that the security condition may permit more information flow than intended and strengthen the condition. Terauchi [33] further improves this condition and enforces it via a fractional-capability type system, which permits updates of a thread’s capabilities upon synchronization. This bears similarities to our updates of modes at synchronization points, but fractional capabilities and modes are different. For instance, in our framework a thread may have exclusive write access to a variable without exclusive read access. In contrast to all three articles, our security condition does not demand low-observable determinism.

Mantel and Sudbrock [34] present a security condition that allows nondeterminism in concurrent programs provided secret information does not influence this nondeterminism. They present a type system that enforces security for a broad class of schedulers: once a thread’s timing or termination behavior is influenced by secret information, it may not interact with low-security threads. Muller and Chong [21] also permit low-observable nondeterminism via a type system for an extension of the X10 programming language.

Mantel, Sands, and Sudbrock [13] use rely-guarantee reasoning to support flow-sensitive security types in concurrent programs, thus allowing more precise enforcement of security. Our approach is inspired by theirs, but we exploit and justify rely-guarantee reasoning dynamically rather than statically.

**Hybrid and dynamic enforcement:** By contrast with static enforcement techniques, our approach is hybrid, combining static and dynamic techniques. This enables more precise enforcement of security, since static techniques must accept or reject a program in its entirety, whereas dynamic and hybrid techniques can accept or reject single executions.

Le Guernic [14] presents the first hybrid monitor for concurrent programs. It is flow sensitive, but uses a single monitor (and single type environment) for the entire thread pool, which restricts concurrency. To handle locks, the monitor uses static analysis to determine when a thread might require a lock, and acquires it before any high branch in that thread, thus ensuring lock acquisition does not depend on secret information. Le Guernic’s monitor suppresses insecure output instead of blocking the thread. We block threads rather than modify the semantics of programs by altering or suppressing outputs.

Stefan et al. [35] present a dynamic termination-sensitive information-flow control mechanism. Their mechanism does not rely on a single global monitor but rather uses coarse-grained containers with “floating labels,” where the label of the container is increased based on information read by the container, and the label restricts writes and other observable effects. In addition to preventing internal timing leaks, they mitigate external timing leaks using predictive mitigation [36]. We do not address external timing leaks, but enforce security at finer granularity (i.e., per program variable) and with greater precision (through flow-sensitivity).

**IX. Conclusion**

We have developed a novel framework to monitor concurrent programs, and instantiated this framework to enforce a knowledge-based progress-sensitive noninterference security condition in concurrent programs where threads share memory resources at the granularity of individual memory locations.

The framework uses a single global monitor to ensure that threads can soundly use rely-guarantee reasoning about shared memory. Each thread has its own local monitor that both enforces thread guarantees regarding shared memory, and also tracks and controls information flow within the thread. The global monitor is accessed only when threads synchronize, ensuring that the monitoring framework does not needlessly restrict concurrency. The local monitors are hybrid: they combine dynamic techniques for information-flow control with on-the-fly static program analysis to approximate information flow on untraced execution paths. Local monitor precision is improved by using rely-guarantee reasoning.

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**REFERENCES**


APPENDIX A

BASIC NOTIONS AND NOTATION

Sets: We use the usual operators for intersection, union, and subtraction of sets. In addition, we use \( P(S) \) to denote the powerset of a set \( S \) and \( S = D \setminus S \) to denote the complement of a set \( S \) in a given domain \( D \).

Functions: We use \( A \to B \) and \( A \to B \) to denote the set of all total functions and the set of all partial functions, respectively, with domain \( A \) and range \( B \). For a function \( f : A \to B \), we use \( \text{pre}(f) \) to denote the pre-image of \( f \), i.e., \( \text{pre}(f) = \{ a \in A \mid f(a) \in B \} \). We use \( f[d \mapsto v] \) to denote the update of a function \( f \) at position \( d \), i.e., \( f[d \mapsto v](d) = v \) and \( f[d \mapsto v](d') = f(d') \) for all \( d' \in (\text{pre}(f) \setminus \{d\}) \). Note that a function update might augment the pre-image of a partial function. We use a similar notation to define functions: Given a set \( B \) and \( r_1, \ldots, r_n \in R \), we use \([d_1 \mapsto r_1] \ldots [d_n \mapsto r_n] \) to denote the total function \( f : \{d_1, \ldots, d_n\} \to R \) that maps \( d_i \) to \( r_i \) for each \( i \in \{1, \ldots, n\} \). Moreover, we use \( \lambda \)-calculus to define functions: We write \( \lambda x \in A : e \) for the function that takes as argument an element of domain \( A \), binds this value to the variable \( x \), and returns the value of the expression \( e \).

Predicates: A predicate over a set \( A \) is a subset of \( A \). In addition to \( a \in p \), we use \( p(a) \) to denote that a predicate \( p \) over \( A \) holds at \( a \in A \).

Lists: We refer to partial functions with domain \( \mathbb{N}_0 \), range \( A \), and a finite pre-image of consecutive numbers starting at 0 also as lists over \( A \). That is, a partial function \( l : \mathbb{N}_0 \to A \) is a list over \( A \) if \( i \in \text{pre}(l) \) for all \( i < |\text{pre}(l)| \). We use \( A^* \) to denote the set of all lists over a set \( A \). As a convention, we use \( \epsilon \) to denote the empty list, \( l \cdot a \) to denote the list that results from appending an element \( a \in A \) to the end of a list \( l \in A^* \), and \( a \cdot l \) to denote the list that results from appending \( a \in A \) to the beginning of \( l \in A^* \). We use \( \text{pre}(\epsilon) = \emptyset \), \( \text{pre}(l \cdot a) = \text{pre}(l) \cup \{|\text{pre}(l)| \} \), \( l \cdot a = \text{pre}(l) \cdot \text{pre}(l) \cdot a \), \( (a \cdot l)(0) = a \), \( (a \cdot l)(i + 1) = l(i) \) for all \( i \in \text{pre}(l) \), \( l_1 \cdot l_2(j) = l_1(j) \) for all \( j \in \text{pre}(l_1) \), and \( (l_1 \cdot l_2)(k + |\text{pre}(l_1)|) = l_2(k) \) for all \( k \in \text{pre}(l_2) \) hold.

We write \( a \in l \) to denote that \( a \in A \) is contained in the list \( l \in A^* \). We write \( a \not\in l \) if \( a \) is not contained in list \( l \).

We define the functional map that applies a function \( f : A \to B \) to each element of a list over \( A \) by \( \text{map}(f, \epsilon) = \epsilon \) and \( \text{map}(f, l \cdot a) = \text{map}(f, l) \cdot f(a) \). Moreover, we define the functional filter that removes from a list over \( A \) those elements that do not satisfy a predicate \( p \subseteq A \) by \( \text{filter}(p, \epsilon) = \epsilon \), \( \text{filter}(p, l \cdot a) = \text{filter}(p, l) \cdot a \) if \( p(a) \), and \( \text{filter}(p, l \cdot a) = \text{filter}(p, l) \) if \( \neg p(a) \).
Appendix B
Command Configuration Rules

The following is a complete list of the inference rules for the transitions between command configurations. Selected rules were shown in Figure 4.

We briefly discuss some of the rules that were not explained previously. The rule for loops inserts more $e$ do $\text{com}$ od into the control state. There are two rules that capture the step in a control state of the form more $e$ do $\text{com}$ od where the body of the loop is executed and where the loop is left, respectively.

There is one rule for input, and one rule for output. Note that input commands accept arbitrary values as input, but which input is supplied by the environment depends on its strategy $\sigma$ (due to the premise $v = \sigma(\tau, ch)$).

\[
\frac{\text{e, mem } \downarrow v}{(\text{stop; com, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com, mem, } \tau)}
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{more e do com od, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com, mem, } \tau)}
\]

\[
\frac{(\text{com, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com, mem, } \tau)}{(\text{com_1; com_2, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com_1; com_2, mem, } \tau')}\quad \text{com_1} \neq \text{stop}
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{if e then com_1 else com_2 fi, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com_1; join, mem, } \tau)}
\]

\[
\frac{(\text{while e do com od, mem, } \tau)}{(\text{more e do com od, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{com; more e do com od, mem, } \tau)}
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{leave, com, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{stop, mem, } \tau)}
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{output, e to ch, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{stop, mem, } \tau')}
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{input ch to x, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{stop, mem, } \tau')}\quad \tau' = \tau \cdot \text{inp}(ch, v)
\]

\[
\frac{\text{e, mem } \downarrow v}{(\text{output ch to e, mem, } \tau) \not \vdash_{\varphi_\sigma} (\text{stop, mem, } \tau')}\quad \tau' = \tau \cdot \text{out}(ch, v)
\]
Figure 6 shows the rules for skip, assignment, and I/O rules. The rule for skip, (M-Skip), leaves the monitor state unchanged. The two rules for inputs are analogous to the rules for M-Output. The only rule for outputs is analogous to (M-Assign1). There is no corresponding rule for (M-Assign2), because channel levels are fixed.

Figure 7 shows the rules for branches and loops. Rule (M-Branch) requires that the local monitor’s mode state allows the thread to read the conditional expression: \( \text{lmdst} \triangleright \text{mayread}(e) \). It pushes on to the program counter stack an upper bound of the decision about which branch to take (as returned by the static branch oracle) and pushes the other results of SB(if \( e \) then \( \text{com}_1 \) else \( \text{com}_2 \), \( \text{br} \), \( \text{time} \), \( \text{term} \), \( \text{block} \)) on to the stack of static branch environments. See below for more details and intuition of the computation of static branch environments and the definition of SB(\( \text{com} \), \( \Gamma \), \( \text{lmdst} \), \( \text{br} \), \( \text{time} \), \( \text{term} \), \( \text{block} \)).

Once the end of the conditional is reached (i.e., the join event is emitted), rule (M-Join) pops off the top elements of program counter stack and the static branch environment stack. Recall that static branch environment is a tuple \( (\text{time}_{ab}, \text{term}_{ab}, \text{block}_{ab}, \Gamma_{ab}) \) where \( \text{time}_{ab} \), \( \text{term}_{ab} \), \( \text{block}_{ab} \), and \( \Gamma_{ab} \) are upper bounds on, respectively, the timing level, termination level, blocking level, and typing environment of a local monitor for any execution of the conditional. At the end of the conditional, we raise the current timing level, termination level, blocking level, and typing environment to these upper bounds, to ensure that they account for information flow that may have occurred due to the non-execution of the other branch. Thus, the new termination level is the old termination level joined with the termination level in the static branch environment, the new blocking level is the old blocking level joined with the blocking level in the static branch environment, and the typing
environment raises the level of any variable that is both in the domain of the current typing environment and the typing environment of the static branch environment. Note that the new timing level is the join of the old timing level, the timing level of the static branch environment, and $\ell$ (i.e., an upper bound on the information that influences the value of the branch condition). Although it can be shown that $\ell \subseteq time_{sb}$, we fold in $\ell$ explicitly for clarity.

Note that in (M-Join), the domain of the new typing environment $I''$ is (and must be) the same as the domain of the original typing environment $I'$. Consider a conditional expression with a low branch, and only one of the two branches has a barrier in it. Since the domain of the typing environment may change at barriers, the domain of $I'$ may differ from the domain of the static branch environment at the top of the stack ($I''$ in the (M-Join) rule). The definition of $I''$ in (M-Join) is careful to ensure that $I''$ has the same domain as $I'$, regardless of the domain of $I''$.

Recall that when a thread enters a loop while $e$ do $com$ od it emits an enter($e,com$) event, and the loop becomes more $e$ do $com$ od. Each time the thread enters the loop body (i.e., given more $e$ do $com$ od, expression $e$ evaluates to a non-zero value, and more $e$ do $com$ od will step to $com$; more $e$ do $com$ od) it emits a more($e,com$) event. When the loop terminates (i.e., given more $e$ do $com$ od, expression $e$ evaluates to zero, and more $e$ do $com$ od will step to stop) the thread emits a leave($e,com$) event.

Upon receiving an enter($e,com$) event, the thread monitor will either block (if execution of the loop may be insecure) or Rule (M-Enter) will push onto the program counter stack an upper bound of the decision about which branch to take (as returned by the static bounds oracle) and pushes the other results of SB(while $e$ do $com$ od, $I'$, lmdst, $\overline{br}$, time, term, block) on to the stack of static branch environments. See below for more details and intuition of the computation of static bounds. In addition, the rule requires that the thread’s mode permits it to read expression $e$.

Upon receiving a more($e,com$) event, we simply check that the thread is still permitted to read expression $e$. We do not modify the stack of program counter levels, the timing level, termination level, blocking level, or stack of static branch environments. Intuitively, this is because the result returned by the static oracle already conservatively approximated an upper bound for the information that may be learned by evaluating the loop guard $e$ in any iteration of the loop (and thus the top element of the program counter stack does not need to be modified) and also produced a static branch environment that conservatively approximates the effects on the timing, termination, blocking and typing context from zero or more iterations of
the loop. Note that the timing level, termination level, blocking level and typing context from the static branching environment for the loop will be incorporated into the local monitor state when the loop exits.

\[
\begin{align*}
M\text{-TERM} & \quad \frac{\text{pc} = \epsilon \quad \text{br} = \epsilon \quad \text{term} = \perp \quad \text{block} = \perp}{(\Gamma, \text{lmdst}, \text{pc}, \text{br}, \text{time}, \text{term}, \text{block}) \rightarrow^{\text{term}} (\Gamma, \text{lmdst}, \text{pc}, \text{br}, \text{time}, \text{term}, \text{block})}
\end{align*}
\]

\[
\begin{align*}
M\text{-BARRIER-LOCAL} & \quad \frac{(\bigcup \text{pc}) \sqcup \text{term} \sqcup \text{block} = \perp \quad \text{lmdst}' = \text{update}(\text{lmdst}, \delta)}{\text{pre}(\Gamma') = \{ x \mid x \in \text{FloatVar} \land (\text{lmdst}' \triangleright \text{exclusiveread}(x) \lor \text{lmdst}' \triangleright \text{exclusivewrite}(x)) \}}
\end{align*}
\]

Figure 8 shows the rules for termination and barrier synchronization.

Rule (M-Term) restricts when a thread may observably terminate. Recall that a thread issues a term event when the thread changes from stop to term (see Appendix B), and that the thread transition rules ensure that term appears only at the top level (i.e., not as a subcommand). Unlike a blocked thread, a terminated thread does not prevent barrier synchronization from occurring, and thus there is a difference between a terminated thread and a blocked thread. So, (M-Term) requires that a thread may terminate only when the termination level \text{term} and blocking level \text{block} are \perp. Otherwise, the fact that the thread is terminated rather than blocked may reveal confidential information. (This is the same reason that \text{M-BARRIER-LOCAL} requires \((\bigcup \text{pc}) \sqcup \text{term} \sqcup \text{block} = \perp.)\) Note also that since the term should only appear at the top level, we require that the program counter stack \text{pc} and the stack of static branching environments \text{br} are empty.

Rule (M-BARRIER-LOCAL) regulates when a local thread may synchronize. The first premise \((\bigcup \text{pc}) \sqcup \text{term} \sqcup \text{block} = \perp\) ensures that the decision to reach a barrier is influenced only by public information. The remaining premises ensure that the local typing environments before and after the barrier (\(\Gamma\) and \(\Gamma'\) respectively) are appropriate based on the accesses this and other threads may have to variables before and after the barrier synchronization.

The second premise of (M-BARRIER-LOCAL) computes the updated mode state \text{lmdst}'. The third premise \(\text{pre}(\Gamma') = \{ x \mid x \in \text{FloatVar} \land (\text{lmdst}' \triangleright \text{exclusiveread}(x) \lor \text{lmdst}' \triangleright \text{exclusivewrite}(x)) \}\) ensures that the domain of the new local typing environment is exactly the variables for the security level is allowed to float \((x \in \text{FloatVar})\) and the thread has either exclusive read or exclusive write access (since if the thread does not have either exclusive read or exclusive write access, then the security level of the variable is fixed).

The fourth premise \((\text{lmdst}' \triangleright \text{exclusiveread}(x) \land \text{lmdst}' \triangleright \text{othersmightread}(x)) \implies \Gamma(x) \sqsubseteq \mathcal{L}(x)\) requires that if this thread is releasing exclusive read access, the security level of information of \(x\) before the barrier \(\Gamma(x)\) is less than or equal to the default security level for the variable \(\mathcal{L}(x)\).

The fifth premise \(\text{lmdst}' \triangleright \text{exclusivewrite}(x) \implies \Gamma'(x) = \Gamma(x)\) requires that if the thread has exclusive write access to \(x\) after the barrier, then the local typing environment must map \(x\) to the security level of \(x\) before the barrier.

The sixth premise \((\text{lmdst}' \triangleright \text{othersmightwrite}(x) \land \text{lmdst}' \triangleright \text{exclusiveread}(x)) \implies \Gamma'(x) = \Gamma(x)\) similarly requires that the local typing environment after the barrier must map \(x\) to the security level of \(x\) before the barrier if this thread did not have exclusive write access before the barrier, and has exclusive read access after the barrier.

The seventh and final premise \((\text{lmdst}' \triangleright \text{exclusivewrite}(x) \land \text{lmdst}' \triangleright [\text{exclusiveread}(x), \text{othersmightwrite}(x)]) \implies \Gamma'(x) = \Gamma(x) \sqcup \mathcal{L}(x)\) requires that if this thread is giving up exclusive write access to \(x\) and has exclusive read access after the barrier then the local typing environment after the barrier must map \(x\) to the join of the security level of \(x\) before the barrier and the default security level of \(x\).

A. Computation of static bounds

When execution of a command branches (either by an if statement or entering a while loop) the monitor statically analyzes the command, in order to approximate what the monitor would do on any possible execution of the command. The result of this analysis is used by the monitor in two ways: (1) to add a security level to the program counter stack \text{pc} that captures an upper bound on the decision of which branch to execute, or whether to execute the loop body; and (2) upon exiting the branch or loop to increase the timing level, termination level, and typing context to account for the possible behavior of the monitor on other executions of the command. In Appendices D and E we present further details, including a semantic specification of the analysis, and a static analysis that meets this specification.
APPENDIX D
SOUNDNESS OF RELY-GUARANTEE STYLE REASONING

In this section, we provide the detailed proof of Theorem 1 and of Theorem 2.

The following proposition clarifies that gmon-impose, indeed, ensures for each \( i \in \text{pre}(gmon') \) and each assumption in gmon'(i) that the corresponding guarantee is acquired in \((gmon-impose(gmon', \Gamma))(j) \) for each \( j \in \text{pre}(\Gamma) \) with \( j \neq i \).

**Proposition 3.** Let gmon, gmon' \( \in \text{GMon} \), \( \Gamma : \text{pre}(gmon) \rightarrow \text{Ann}^* \), \( i \in \text{pre}(gmon) \), and mod \( \in \text{Asm} \), be arbitrary. Let \( \Delta = gmon-impose(gmon', \Gamma) \). If \( x \in \text{gmon}(i) \) then \( \text{acq}(\text{invert}(\text{mod})) \), \( x \in \Delta(j) \) and \( \text{rel}(\text{invert}(\text{mod}), x) \notin \Delta(j) \) holds for all \( j \in \text{pre}(\Gamma) \) \( \setminus \{i\} \).

**Proof:** Assume \( \Delta = gmon-impose(gmon', \Gamma) \). Let \( x \in (\text{gmon}(i)) \) and \( j \in \text{pre}(\Gamma) \) \( \setminus \{i\} \) be arbitrary.

Since mod \( \in \text{Asm} \), either \( \text{mod} = \text{A-NW} \) or \( \text{mod} = \text{A-NR} \). We only prove the first case, the other is analogous.

If \( \text{mod} = \text{A-NW} \) then \( x \in \text{NW}(j) \) holds for the local variable NW in the body of the function gmon-impose because \( x \in (\text{gmon}(i)) \) and \( i \in \text{pre}(\Gamma) \) \( \setminus \{j\} \). Hence, \( \text{acq}(\text{G-NW}, x) \in \Delta(j) \) and \( \text{rel}(\text{G-NW}, x) \notin \Delta(j) \) because \( \Delta(j) = (\Gamma(j) \setminus \text{Asm}) \cdot \text{acq}(\text{G-NW}, \text{NW}(j)) \cdot \text{acq}(\text{G-NR}, \text{NR}(j)) \cdot \text{rel}(\text{G-NW}, \text{NW}(j)) \cdot \text{rel}(\text{G-NR}, \text{NR}(j)) \), \( x \in \text{NW}(j) \), and \( x \notin \text{NW}(j) \). Thus, \( \text{acq}(\text{invert}(\text{mod}), x) \notin \Delta(j) \) hold.

The following proposition explicates some facts about transitions of the global monitor.

**Proposition 4.** Let gmon, gmon' \( \in \text{GMon} \) and \( \Gamma, \Delta : \text{pre}(gmon) \rightarrow \text{Ann}^* \) be arbitrary. If \( gmon \rightarrow^{\Gamma, \Delta} gmon' \) then

1) \( \text{pre}(gmon') = \text{pre}(gmon) \),
2) \( \text{pre}(\Delta) = \text{pre}(\Gamma) \), and
3) \( (\Gamma(i)) \setminus \{\text{mod}\} = (\Delta(i)) \setminus \{\text{mod}\} \) for each \( i \in \text{pre}(\Gamma) \) and mod \( \in \text{Asm} \).

**Proof:** Assume \( gmon \rightarrow^{\Gamma, \Delta} gmon' \).

1) According to the definition of the function gmon-update, gmon-update(gmon, \( \Gamma \)) is a function with pre-image \( \text{pre}(gmon) \).
2) According to the definition of the function gmon-impose, gmon-impose(gmon', \( \Gamma \)) is a function with pre-image \( \text{pre}(\Gamma) \).
3) Let \( i \in \text{pre}(\Gamma) \) and mod \( \in \text{Asm} \) be arbitrary. From \( gmon \rightarrow^{\Gamma, \Delta} gmon' \) and Figure 3, we obtain \( \Delta = gmon-impose(gmon', \Gamma) \).

The following theorem implies that being well formed is an invariant for global configurations.

**Theorem 4.** Let \( \text{gcn}(\text{f}) = \langle \text{pool}, \text{mem}, \tau, \text{gmon} \rangle \) and \( \text{gcn}(\text{f}') = \langle \text{pool}', \text{mem}', \tau', \text{gmon}' \rangle \) be two global configurations such that \( gcn(f) \rightarrow_{\sigma} gcn(f') \) is derivable for some \( \sigma \in \Sigma \).

1) If \( \text{gmon} \) is compatible with \( \text{pool} \) then \( \text{gmon}' \) is compatible with \( \text{pool}' \).
2) If \( \text{pool}(i) \) is a well-formed thread state for each \( i \in \text{pre}(\text{pool}) \) and the calculus for local monitor transitions properly tracks modes then \( \text{pool}'(j) \) is a well-formed thread state for each \( j \in \text{pre}(\text{pool}') \).

**Proof:** Assume \( gcn(f) \rightarrow_{\sigma} gcn(f') \) is derivable for \( \sigma \in \Sigma \).

**Proof of 1st Proposition:** Assume \( \text{gmon} \) is compatible with \( \text{pool} \). To prove that \( \text{gmon}' \) is compatible with \( \text{pool}' \), we need to show \( \text{pre}(\text{pool}') = \text{pre}(\text{gmon}') \) and \( (\text{pool}'(i)) \cdot \text{mdst}(\text{mod}) = (\text{gmon}(i)) \cdot (\text{mod}) \) for all \( i \in \text{pre}(\text{pool}') \) and mod \( \in \text{Asm} \). Let \( i \in \text{pre}(\text{pool}') \) and mod \( \in \text{Asm} \) be arbitrary.

From \( i \in \text{pre}(\text{pool}') \) and \( gcn(f) \rightarrow_{\sigma} gcn(f') \), we obtain \( i \in \text{pre}(\text{pool}) \). Since \( \text{gmon} \) is compatible with \( \text{pool} \) and \( i \in \text{pre}(\text{pool}) \), we have \( \text{pre}(\text{pool}) = \text{pre}(\text{gmon}) \) and \( (\text{pool}(i)) \cdot \text{mdst}(\text{mod}) = (\text{gmon}(i)) \cdot (\text{mod}) \). Depending on which rule in Figure 2 was used to derive \( gcn(f) \rightarrow_{\sigma} gcn(f') \), we distinguish two cases.

**Case 1** If the first rule in Figure 2 was used, we have \( gmon' = \text{gmon} \) and \( \text{pool}(j) \rightarrow_{\delta} \text{thread}' \) for some \( j \in \text{alive}(\text{pool}) \) and \( \text{thread}' \in \text{ThSt} \). Consequently, \( \text{pre}(\text{pool}') = \text{pre}(\text{pool}) = \text{pre}(\text{gmon}') \) hold. Hence, \( \text{pre}(\text{pool}') = \text{pre}(\text{gmon}') \) follows from \( \text{pre}(\text{pool}) = \text{pre}(\text{gmon}) \).

We distinguish whether the \( i \)th thread performed the computation step (Case 1a) or some other thread (Case 1b).

**Case 1a** If the \( i \)th thread performed the step then \( (\text{pool}(i), \text{mem}, \tau) \rightarrow_{\delta} (\text{thread}', \text{mem}', \tau') \) follows from the second, third, and fourth premise of the first rule in Figure 2. The only rule for deriving this judgment is the rule in Figure 2 and the third premise of this rule (i.e., \( \text{mdst} = \text{update}(\text{mdst}, \delta) \) for \( \delta = e \)) ensures that the mode state of the \( i \)th thread remains unmodified. Hence, \( (\text{pool}(i)) \cdot \text{mdst}(\text{mod}) = (\text{gmon}(i)) \cdot (\text{mod}) \) follows from \( \text{gmon}' = \text{gmon} \).

**Case 1b** If the \( i \)th thread did not perform the step then \( (\text{pool}(i)) \cdot \text{mdst}(\text{mod}) = (\text{gmon}(i)) \cdot (\text{mod}) \) follows from \( \text{pool}' = \text{pool} \) and
\text{pool}[j \mapsto \text{thread'}] for some } j \neq i, \:( \text{pool}(i)).\text{mdst}(\text{mod}) = (\text{gmon}(i))(\text{mod}), \: \text{and } \text{gmon} = \text{gmon}'.

\textbf{Case 2)} According to the second rule in Figure 2 we have $\text{gmon} \rightarrow^{\Gamma, \Delta} \text{gmon}'$ for some $\Gamma, \Delta : \text{alive(pool)} \rightarrow \text{Ann}^*$. Consequently, $\text{pre}(\text{gmon}') = \text{pre}(\text{gmon})$ follows from Item 1 of Proposition 3. Moreover, according to the fourth premise of the second rule in Figure 2, we have $\text{pre}(\text{pool}') = \text{pre}(\text{pool})$. Hence, $\text{pre}(\text{pool}') = \text{pre}(\text{gmon}')$ follows from $\text{pre}(\text{gmon}) = \text{pre}(\text{pool})$.

We distinguish whether the $i$th thread is alive (Case 2a) or not (Case 2b).

\textbf{Case 2a)} If $i \in \text{alive(pool)}$ then $i \in \text{pre}(\Gamma)$ follows from the second premise of the second rule in Figure 2. From Item 2 of Proposition 3 we obtain $\text{pre}(\Delta) = \text{pre}(\Gamma)$ and, thus, $i \in \text{pre}(\Delta)$. That is $\Gamma$ and $\Delta$ are both defined at $i$.

Let $\text{mdst} = (\text{pool}(i)).\text{mdst}$ and $\text{mdst}' = (\text{pool}(i)).\text{mdst}$ (hence, $\text{mdst}(\text{mod}) = (\text{gmon}(i))(\text{mod})$ holds). From the last premise of the second rule in Figure 2 we obtain $(\text{pool}(i), \text{mem}, \tau) \xrightarrow{\text{sync}.\Gamma(i), \Delta(i)} (\text{pool}(i), \text{mem}, \tau')$. The only rule for deriving this judgment is the rule in Figure 1 and this rule ensures that $\text{mdst}' = \text{update}(\text{mdst}, \Delta(i))$.

From $\text{gmon} \rightarrow^{\Gamma, \Delta} \text{gmon}'$, the first premise of the rule in Figure 3 and the definition of $\text{gmon-update}$, we obtain $\text{gmon}'(i) = \text{update}(\text{gmon}(i), (\Gamma(i) \mid \text{Asm}))$. Moreover, $\Delta(i) \mid \{ \text{mod} \} = (\Gamma(i) \mid \text{Asm}) \mid \{ \text{mod} \}$ follows from $\text{mod} \in \text{Asm}$ and $\Delta(i) \mid \{ \text{mod} \} = (\Gamma(i) \mid \{ \text{mod} \})$ (Item 3 of Proposition 3). Since $\text{mdst}(\text{mod}) = (\text{gmon}(i))(\text{mod})$ and $\Delta(i) \mid \{ \text{mod} \} = (\Gamma(i) \mid \text{Asm}) \mid \{ \text{mod} \}$, we can apply Proposition 1 to obtain $(\text{update}(\text{mdst}, \Delta(i)))(\text{mod}) = (\text{update}(\text{gmon}(i), (\Gamma(i) \mid \text{Asm})))(\text{mod})$, and, hence, $\text{mdst}'(\text{mod}) = (\text{gmon}'(i))(\text{mod})$ holds.

Consequently, $(\text{pool}(i)).\text{mdst}(\text{mod}) = \text{mdst}'(\text{mod}) = (\text{gmon}'(i))(\text{mod})$ holds.

\textbf{Case 2b)} If $i \notin \text{terminated(pool)}$ then $\text{pool}'(i) = \text{pool}(i)$ according to the fifth premise of the second rule in Figure 2. Since $\text{gmon} \rightarrow^{\Gamma, \Delta} \text{gmon}'$, the first premise of the rule in Figure 3 $i \notin \text{pre}(\Gamma)$, and the definition of $\text{gmon-update}$, we obtain $\text{gmon}'(i) = \text{gmon}(i)$. Consequently, $(\text{pool}(i)).\text{mdst}(\text{mod}) = (\text{pool}(i)).\text{mdst}(\text{mod}) = (\text{gmon}(i))(\text{mod}) = (\text{gmon}'(i))(\text{mod})$ holds.

That is, $\text{gmon}'$ is compatible with $\text{pool}'$.

\textbf{Proof of 2nd Proposition:} Assume $\text{pool}(i)$ is a well-formed thread state for each $i \in \text{pre(pool)}$ and that the calculus for local monitor transitions properly tracks modes. We need to show that $\text{pool}'(j)$ is a well-formed thread state for each $j \in \text{pre(pool)}$.

Let $j \in \text{pre(pool)}$ be arbitrary.

From $j \in \text{pre(pool)}$ and $\text{gcnf} \rightarrow_{\sigma} \text{gcnf}'$, we obtain $j \in \text{pre(pool)}$. Hence, $\text{pool}(j)$ is a well-formed thread state.

Depending on which rule in Figure 2 was used to derive $\text{gcnf} \rightarrow_{\sigma} \text{gcnf}'$, we distinguish two cases.

\textbf{Case 1)} If the first rule in Figure 2 was used, we have $(\text{pool}(k), \text{mem}, \tau) \xrightarrow{\text{sync}.\gamma, \delta} (\text{pool}(k), \text{mem}', \tau')$ and $\text{pool}' = \text{pool}[k \mapsto \text{thread'}]$ for some $k \in \text{alive(pool)}$, $\beta \in \text{GEv}$, and $\gamma, \delta \in \text{Ann}^*$. If $k \neq j$ then $\text{pool}'(j) = \text{pool}(j)$ holds and, thus, $\text{pool}'(j)$ is a well-formed thread state.

We assume $k = j$ from now. Let $(\text{com}, \text{lmon}, \text{mdst}) = \text{pool}(j)$ and $(\text{com}', \text{lmon}', \text{mdst}') = \text{pool}'(j)$. From $(\text{pool}(j), \text{mem}, \tau) \xrightarrow{\text{sync}.\gamma, \delta} (\text{pool}'(j), \text{mem}', \tau')$ and the rule in Figure 1 we obtain $\text{mdst}' = \text{update}(\text{mdst}, \delta)$ and that $\text{lmon} \rightarrow^{\delta, \alpha} \text{lmon}'$ is derivable for some $\alpha \in \text{Ev}$ with $\chi(\alpha) = \beta$. From $\text{lmon} \rightarrow^{\delta, \alpha} \text{lmon}'$ and our assumption that the calculus for local monitor transitions properly tracks modes, we obtain $\text{lmon}'.\text{mdst} = \text{update}(\text{lmon}.\text{mdst}, \delta)$. We obtain $\text{lmon}.\text{mdst} = \text{mdst}'$ from $\text{lmon}'.\text{mdst} = \text{update}(\text{lmon}.\text{mdst}, \delta)$, $\text{mdst}' = \text{update}(\text{mdst}, \delta)$, and $\text{lmon}.\text{mdst} = \text{mdst}$.

\textbf{Case 2)} If the second rule in Figure 2 was used, we have $\text{pool}'(k) = \text{pool}(k)$ for all $k \in \text{terminated(pool)}$. Hence, if $k \in \text{terminated(pool)}$ then $\text{pool}'(j) = \text{pool}(j)$ holds and, thus, $\text{pool}'(j)$ is a well-formed thread state.

We assume $k \in \text{alive(pool)}$ from now. From the second rule in Figure 2 we obtain that $(\text{pool}(j), \text{mem}, \tau) \xrightarrow{\text{sync}.\gamma, \delta} (\text{pool}'(j), \text{mem}', \tau')$ is derivable for some $\gamma, \delta \in \text{Ann}^*$. From $(\text{pool}(j), \text{mem}, \tau) \xrightarrow{\text{sync}.\gamma, \delta} (\text{pool}'(j), \text{mem}', \tau')$ and the rule in Figure 1 we obtain $\text{mdst}' = \text{update}(\text{mdst}, \delta)$ and that $\text{lmon} \rightarrow^{\delta, \alpha} \text{lmon}'$ is derivable for some $\alpha \in \text{Ev}$ with $\chi(\alpha) = \gamma$. From the latter fact and from our assumption that the calculus for local monitor transitions properly tracks modes, we obtain $\text{lmon}'.\text{mdst} = \text{update}(\text{lmon}.\text{mdst}, \delta)$. We obtain $\text{lmon}.\text{mdst} = \text{mdst}'$ from $\text{lmon}'.\text{mdst} = \text{update}(\text{lmon}.\text{mdst}, \delta)$, $\text{mdst}' = \text{update}(\text{mdst}, \delta)$, and $\text{lmon}.\text{mdst} = \text{mdst}$.

That is, $\text{pool}'(j)$ is a well-formed thread state.

The following theorem implies that justifying all assumptions is an invariant for well-formed global configurations.

\textbf{Theorem 5.} Let $\text{gcnf} = \langle \text{pool}, \text{mem}, \tau, \text{gmon} \rangle$ and $\text{gcnf}' = \langle \text{pool}', \text{mem}', \tau', \text{gmon}' \rangle$ be two global configurations such that $\text{gcnf} \rightarrow_{\sigma} \text{gcnf}'$ is derivable for some $\sigma \in \Sigma$.

If all assumptions in $\text{gcnf}$ are justified and $\text{gmon}$ is compatible with $\text{pool}$ then all assumptions in $\text{gcnf}'$ are justified.

\textit{Proof:} Assume all assumptions in $\text{gcnf}$ are justified, and $\text{gmon}$ is compatible with $\text{pool}$.

According to the definition of justified assumptions, we need to show

$$\forall i \in \text{pre(pool)} : \forall \text{mod} \in \text{Asm} : \forall x \in (\text{pool}(i)).\text{mdst}(\text{mod}) : \forall j \in \text{alive(pool)} \setminus \{i\} : x \in (\text{pool}(j)).\text{mdst}(\text{invert}(\text{mod}))$$
Let \( i \in \text{pre}(pool'), \mod \in \text{Asm}, x \in (pool(i)).\text{mdst}({\mod}), \) and \( j \in \text{alive}(pool') \) and \( \text{alive}(pool') \subseteq \text{alive}(pool) \), we have \( j \in \text{alive}(pool) \). Depending on which rule in Figure 2 was used to derive \( \text{genf} \rightarrow_{\sigma} \text{genf}' \), we distinguish two cases:

**Case 2ai.** According to the first rule in Figure 2 mode states of threads remain unchanged when this rule is applied. Hence, we obtain that \((pool(i)).\text{mdst}({\mod}) = (pool(i)).\text{mdst}({\mod})\) and \((pool(j)).\text{mdst}({\mod}) = (pool(j)).\text{mdst}({\mod})\) hold. Thus, \( x \in (pool(i)).\text{mdst}({\mod}) \) follows from \( x \in (pool(i)).\text{mdst}({\mod}) \). Since all assumptions in \( \text{genf} \) are justified, \( x \in (pool(j)).\text{mdst}({\mod}) \).

**Case 2aii.** Let \([\text{com}, l\text{mon}, j, \text{mdst}] = (pool(j))\) and \([\text{com}', l\text{mon}', j, \text{mdst}'] = (pool(j))\). According to the second rule in Figure 2 we have \( \text{gmon} \rightarrow \Gamma, \Delta \text{gmon}' \) for some \( \Gamma, \Delta : \text{alive}(pool) \rightarrow \text{Ann} \). Hence, we obtain \( \Delta = \text{gmon-imp}(\text{gmon}' \), \( \Gamma) \) from the rule in Figure 3.

Since \( x \in (pool(i)).\text{mdst}({\mod}) \), we obtain \( x \in (\text{gmon}(i))({\mod}) \) from \( \text{genf} \rightarrow_{\sigma} \text{genf}' \), the assumption that \( \text{genf} \) is well formed, and Theorem 2. Since \( x \in (\text{gmon}((i))({\mod}), \mod \in \text{Asm}, \) and \( \Delta = \text{gmon-imp}(\text{gmon}', \Gamma) \), we obtain \( \text{acq}({\mod}, x) \in \Delta(j) \) and \( \text{rel}({\mod}, x) \notin \Delta(j) \) from Proposition 3. Consequently, \( x \in (\text{update}({\mod}, j))(\text{mdst}({\mod})) \) according to the definition of the function \( \text{update} \).

We have \( \langle\langle \text{com}, l\text{mon}, j, \text{mdst}, \text{mem}, \tau \rangle \rangle_{\sigma} \rightarrow \langle\langle \text{com}', l\text{mon}', j, \text{mdst}', \text{mem}, \tau \rangle \rangle_{\sigma} \) according to the second rule in Figure 2 because \( j \in \text{alive}(pool) \). The only rule for deriving this judgment is the rule in Figure 1 and the premise of this rule ensures that \( \text{mdst}' = \text{update}({\mod}, j)(\Delta(j)) \) holds and, thus, \( x \in \text{mdst}'(j)(\text{mdst}) \). Consequently, \( x \in (pool(j)).\text{mdst}({\mod}) \).

That is, all assumptions in \( \text{genf}' \) are justified.

The following theorem implies Theorem 1 and, hence, that rely-guarantee-style reasoning is sound under the assumption that the calculus for local monitor transitions properly tracks modes and enforces guarantees.

**Theorem 6.** Let \( \text{genf} = \langle\langle \text{pool}, \text{mem}, \tau, \text{gmon} \rangle \rangle \) and \( \text{genf}' = \langle\langle \text{pool}, \text{mem}, \tau', \text{gmon} \rangle \rangle \) be two global configurations such that \( \text{genf} \in \text{greach}_{\kappa}({\text{genf}}) \) for some \( \sigma \in \Sigma \).

If all assumptions in \( \text{genf} \) are justified, if \( \text{genf} \) is well formed, and if the calculus for local monitor transitions properly tracks modes and enforces guarantees then \( \text{genf} \rightarrow \) justifies its assumptions and provides its guarantees.

**Proof:** We prove that \( \text{genf} \rightarrow \) is a well-formed global configuration that justifies its assumptions by induction over the number of steps by which \( \text{genf} \rightarrow \) is reached from \( \text{genf} \) using Theorems 4 and 5 in the induction step.

Since \( \text{genf} \rightarrow \) is well formed, \( \text{pool}(i) \) is a well-formed thread state for each \( i \in \text{pre}(pool') \). From our assumption that the calculus for local monitor transitions enforces guarantees, we obtain that for each \( i \in \text{pre}(pool') \), the thread state \( \text{pool}(i) \) provides its guarantees. Consequently, \( \text{genf} \rightarrow \) provides its guarantees.

**Proof of Theorem 2.** We prove the two propositions of the theorem in Part 1 and 2, respectively.

**Part 1.** To prove that the calculus properly tracks modes, we need to show that if \( l\text{mon} \rightarrow_{\delta, \text{perm}} l\text{mon}' \) is derivable then \( l\text{mon}'.\text{mdst} = \text{update}(l\text{mon}.\text{mdst}, \delta) \). We assume that \( l\text{mon} \rightarrow_{\delta, \text{perm}} l\text{mon}' \) is derivable and make a case distinction about the calculus rule applied at the root of this derivation. If (M-Barrier-Local) is applied at the root of the derivation then \( l\text{mon}'.\text{mdst} = \text{update}(l\text{mon}.\text{mdst}, \delta) \) holds due to the premises of this rule. If some other rule than (M-Barrier-Local) is applied at the root of the derivation then \( \delta = \epsilon \) and \( l\text{mon}'.\text{mdst} = l\text{mon}.\text{mdst} \) hold according to the conclusion of each such rule. Hence, \( l\text{mon}'.\text{mdst} = \text{update}(l\text{mon}.\text{mdst}, \delta) \) follows from \( \text{update}(l\text{mon}.\text{mdst}, \delta) = l\text{mon}.\text{mdst} \) (holds due to the definition of the function \( \text{update} \) for \( \delta = \epsilon \)).

**Part 2.** Let \( \text{thread} = [\text{com}, l\text{mon}, \text{mdst}] \) be an arbitrary well-formed thread state. That is, \( l\text{mon}.\text{mdst} = \text{mdst} \) holds. Let \( \text{mem} \in \text{Mem} \) and \( \tau \in \text{Tr} \) be arbitrary. We need to show that the local configuration \( \text{lenf} = (\text{thread}, \text{mem}, \tau) \) provides its guarantees. We first show that \( \text{lenf} \) provides the no-write guarantee for each variable \( x \in \text{mdst}(G-NW) \) (Part 2a) and then show that \( \text{lenf} \) also provides the no-read guarantee for each variable \( y \in \text{mdst}(G-NR) \) (Part 2b).

**Part 2a.** We assume that \( \text{mdst}(G-NW) \neq \emptyset \) because, otherwise, the proposition is trivially fulfilled. Let \( x \in \text{mdst}(G-NW) \) be arbitrary. Moreover, let \( \sigma \in \Sigma, \beta \in \text{GEV}, \gamma, \delta \in \text{Ann}^*, \) and \( \text{lenf}' = \text{Lcnf}' \) be arbitrary such that \( \text{lenf} \rightarrow_{\beta, \gamma, \delta, \sigma} \text{lenf}' \) is derivable. Finally, let \( \langle\langle \text{thread}, \text{mem}', \tau \rangle \rangle = \text{lenf}' \) and \( \langle\langle \text{com}, l\text{mon}', \text{mdst} \rangle \rangle = \text{thread}' \).

From the derivability of \( \text{lenf} \rightarrow_{\beta, \gamma, \delta, \sigma} \text{lenf}' \) and the rule in Figure 1, which is the only rule for deriving this judgment, we obtain that there exists a local event \( \alpha \in \text{Ev} \) such that \( \langle\langle \text{com}, \text{mem}, \tau \rangle \rangle \rightarrow_{\gamma, \sigma} \langle\langle \text{com}', \text{mem}', \tau \rangle \rangle \) is derivable, \( l\text{mon} \rightarrow_{\delta, \text{perm}} l\text{mon}' \) is derivable, and \( \beta = \chi(\alpha) \) holds. We perform a case distinction on \( \alpha \).

**Case 2ai.** If \( \alpha = a(x, e) \) for some \( e \in \text{Exp} \) then the judgment \( l\text{mon} \rightarrow_{\delta, \text{perm}} l\text{mon}' \) must have been derived with the rule
(M-Assign1) or with the rule (M-Assign2) according to our calculus for local monitor transitions. Both of these rules require \( lmon \triangleright \text{maywrite}(x) \) in their premises. Hence, \( x \notin \text{mdst}(G-NW) \) because of \( lmon \triangleright \text{mdst} \). This contradicts our assumption \( x \in \text{mdst}(G-NW) \) and, hence, Case 2ai is impossible.

**Case 2aii.** If \( \alpha = \text{input}(x,ch,v) \) for some \( ch \in Ch \) and \( v \in Val \) then the judgment \( lmon \rightarrow^{\delta,\alpha}_{\text{perm}} lmon' \) must have been derived with the rule (M-Input1) or with the rule (M-Input2) according to our calculus for local monitor transitions. Both of these rules require \( lmon \triangleright \text{maywrite}(x) \) in their premises. Hence, \( x \notin \text{mdst}(G-NW) \) because \( lmon \triangleright \text{mdst} \). This contradicts our assumption \( x \in \text{mdst}(G-NW) \) and, hence, Case 2aii is impossible.

**Case 2auii.** If \( \alpha \notin \{ \text{a}(y,e), \text{input}(y,ch,v) \mid y = x, e \in \text{Exp}, ch \in Ch, v \in Val \} \) then we obtain \( \text{mem}'(x) = \text{mem}(x) \) from the derivability of \( (\text{com}, \text{mem}, \tau) \xrightarrow{\alpha,\gamma}_v (\text{com}', \text{mem}', \tau') \) and the language semantics (see Appendix B).

That is, \( \text{lenf} \) provides the no-write guarantee for each variable \( x \in \text{mdst}(G-NW) \).

**Part 2b.** We assume that \( \text{mdst}(G-NR) \neq \emptyset \) because, otherwise, the proposition is trivially fulfilled. Let \( y \in \text{mdst}(G-NR) \) be arbitrary. Moreover, let \( \sigma \in \Sigma, \beta \in GEv, \gamma, \delta \in \text{Ann}^*, \) and \( \text{lenf}^{'} \in \text{LCnf} \) be arbitrary such that \( \text{lenf} \xrightarrow{\beta,\gamma,\delta}_\sigma \text{lenf}^{'} \) is derivable. Finally, let \( \langle \text{thread}', \text{mem}', \tau' \rangle = \text{lenf}' \) and \( \langle \text{com}', \text{mem}', \text{mdst}' \rangle = \text{thread}' \).

From the derivability of \( \text{lenf} \xrightarrow{\beta,\gamma,\delta}_\sigma \text{lenf}^{'} \) and the rule in Figure 1, which is the only rule for deriving this judgment, we obtain that there exists a local event \( \alpha \in E v \) such that \( (\text{com}, \text{mem}, \tau) \xrightarrow{\alpha,\gamma}_v (\text{com}', \text{mem}', \tau') \) is derivable, \( lmon \rightarrow^{\delta,\alpha}_{\text{perm}} lmon' \) is derivable, and \( \beta = \chi(\alpha) \) holds. We perform a case distinction on \( \alpha \) and show for each of the five cases that, for each \( v \in Val \), the judgment \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) or the judgment \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) is derivable. Let \( v \in Val \) be arbitrary.

**Case 2bi.** If \( \alpha = \text{a}(y,e) \) for some \( e \in \text{Exp} \) with \( y \notin \text{vars}(e) \) then, according to Appendix B, \( \text{mem}' = \text{mem}[y \mapsto v'] \) must hold for some value \( v' \in Val \) for which \( e, \text{mem} \downarrow v' \) is derivable (argument is by induction on the number of applications of the rule for sequential composition, using the rule for assignments in the base case). Since \( y \notin \text{vars}(e) \) and \( e, \text{mem} \downarrow v' \) is derivable, \( e, \text{mem}[y \mapsto v] \downarrow v' \) must be derivable, \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) is derivable, and \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) is derivable, which concludes this case.

**Case 2bii.** If \( \alpha = \text{input}(y,ch,v) \) for some \( ch \in Ch \) and \( v \in Val \) then \( \text{mem}' = \text{mem}[y \mapsto v'] \) must hold according to Appendix B (argument is by induction on the number of applications of the rule for sequential composition, using the rule for input commands in the base case). Since \( \alpha = \text{input}(x,ch,v') \) and \( \text{com}, \text{mem}, \tau \xrightarrow{\alpha,\gamma}_v (\text{com}', \text{mem}[y \mapsto v'], \tau') \) is derivable, \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) is derivable, which concludes this case.

**Case 2biv.** If \( \alpha = \{ \text{a}(x,e), \text{input}(x,ch,v) \mid x \in \text{Var}, x \neq y, e \in \text{Exp}, e \notin \text{vars}(e), ch \in Ch, v' \in Val \} \) then \( \text{mem}' = \text{mem}[x \mapsto v'] \) must hold for some \( v' \in Val \) according to Appendix B (argument is by induction on the number of applications of the rule for sequential composition).

Consequently, \( \langle \text{thread}, \text{mem}[y \mapsto v], \tau \rangle \xrightarrow{\beta,\gamma,\delta}_\sigma \langle \text{thread}', \text{mem}', \tau' \rangle \) is derivable, which concludes this case.
That is, \( lcnf \) provides the no-read guarantee for each variable \( y \in \text{mdst}(G\text{-NR}) \).
In this section, we prove Theorem 3, which states that the local monitors, combined with the global monitor, enforce security.

Figure 9 provides an overview of the proof. Starting from two identical global configurations \( gcnf_1^0 \) and \( gcnf_2^0 \), and two \( \ell \)-equivalent strategies \( \sigma_1 \) and \( \sigma_2 \), we show that for each step that the first configuration can take (using \( \sigma_1 \)), the second one can take zero or more steps (using \( \sigma_2 \)) to a \( \ell \)-equivalent global configuration. Theorem 11 says that if the first configuration takes a “low” step, then the second configuration can also take a low step. Theorem 12 says that if the first configuration takes a “high” step, then the second configuration does not need to take any step. Theorem 13 says that if the first configuration takes a barrier step, then the second configuration takes zero or more high steps to reach the barrier, whereupon it can take a barrier step. Theorems 7, 8, and 9 describe the \( \ell \)-equivalence of individual threads, and Theorem 10 states that \( \ell \)-equivalence of threads is preserved when the memory and trace changes due to other threads taking a step. Theorems 7, 8, 9, and 10 are used in the proofs of Theorems 11, 12, and 13 as indicated in Figure 9.

The proof of Theorem 13 is more complex than the others, and is contained in Section E-F. In it, we define a stronger notion of \( \ell \)-equivalence between thread states: time-insensitive-\( \ell \)-equivalence, which in essence requires the components of equivalent thread states to be identical if the pc stack, termination level, and blocking level are all below \( \ell \) (i.e., it ignores the timing level). This time-insensitive-\( \ell \)-equivalence allows us to show that if one thread exits a while loop or conditional command with the pc stack, termination level and blocking level all below \( \ell \), then the other thread will also exit the same while loop or conditional, even though the timing level may be above \( \ell \).
A. Static bounds

In Section C-A we introduced the idea that the local monitor must examine conditional commands and loops in order to approximate the behavior of the monitor on the code not executed.

More specifically, the local monitor invokes a static oracle, passing in some of the local monitor state, and the oracle returns a tuple: \( SB(com, \Gamma, lmdst, \overline{pc}, time, term, block) = (\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb}) \). Security level \( \ell_{sb} \) will be pushed on to the program counter stack, and will remain on the stack until the branch or loop command is exited. Thus, \( \ell_{sb} \) should be an upper bound on the information that determines which branch to take (for branch commands), or how many times to execute the loop body (for loop commands). The tuple \((time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\) is a static branching environment, and will be pushed onto the stack of static branching environments, to be incorporated into the local monitor’s timing level, termination level, blocking level, and typing context when the monitor exits the branch or loop command.

Intuitively, the static branching environment is meant to summarize the behavior of the local monitor during interesting possible executions of \( com \), other than the execution that actually occurs. Other executions of \( com \) are interesting only if they may reveal secret information, i.e., only if \( \ell_{sb} \neq \bot \). If \( \ell_{sb} = \bot \), then other possible executions of \( com \) are not interesting, and we do not have any restrictions on static branching environment \((time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\).

If, however, \( \ell_{sb} \neq \bot \) then there may be other executions of \( com \) where different control flow decisions occur based on secret information bounded by \( \ell_{sb} \). In that case, static branching environment \((time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\) summarizes these other possible executions. Assume that we have a monitored execution of command \( com \), and just before executing \( com \) the local monitor configuration is \((\Gamma, lmdst, \overline{pc}, \overline{br}, time, term, block)\), and the local monitor’s configuration after successfully executing \( com \) is \((\Gamma', lmdst', \overline{pc}', \overline{br}', time', term', block')\).

Then the static branching environment timing level \( time_{sb} \) is an upper bound on the local monitor’s timing level at the end of \( com \) (i.e., \( time' \sqsubseteq time_{sb} \)). (Moreover, for loops, \( time_{sb} \) is an upper bound of the local monitor’s timing level at the start of every loop iteration.)

Similarly, the static branching environment termination level \( term_{sb} \), and the blocking level \( block_{sb} \) are upper bounds on, respectively, the local monitor’s termination level and the local monitor’s blocking level at the end of \( com \) (i.e., \( term' \sqsubseteq term_{sb} \) and \( block' \sqsubseteq block_{sb} \)). (Moreover, for loops, \( term_{sb} \) and \( block_{sb} \) are upper bounds of the local monitor’s termination and blocking levels at the start of every loop iteration.)

Also, the static branching environment typing environment \( \Gamma_{sb} \) has the same domain as the local monitor’s typing environment at the end of \( com \) (i.e., \( pre(\Gamma') = pre(\Gamma_{sb}) \)), and for each variable \( x \), \( \Gamma_{sb} \) provides an upper bound of the local monitor’s level for variable \( x \) (i.e., \( \forall x \in Var. \Gamma'(x) \sqsubseteq \Gamma_{sb}(x) \)). (Moreover, for loops, this holds true for the local monitor’s typing environment at the start of every loop iteration.)

There is one additional constraint on the soundness of the static monitor. If the command \( com \) contains any barrier synchronization, then \( \ell_{sb} = \bot \). That is, the decision to execute a barrier synchronization must depend only on public (i.e., level \( \bot \)) information.

We can phrase the informal requirements on the static oracle more formally, as follows. If we have a thread pool where the \( i \)th thread is \( com \) and \( com \) is the branching or looping command, and \( \Gamma, lmdst, time, term, \) and \( block \) are the relevant parts of the current state of the local monitor of the \( i \)th thread, and the monitored execution of \( com \) terminates (i.e., the \( i \)th thread becomes stop) with monitor state \( time', term', block', \Gamma' \) and \( SB(com, \Gamma, lmdst, \overline{pc}, time, term, block) = (\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb}) \), and \( \ell_{sb} \neq \bot \) then \( time_{sb}, term_{sb}, block_{sb} \) and \( \Gamma_{sb} \) are upper bounds of \( time', term', block' \), and \( \Gamma' \) respectively. Moreover, for loops, these are upper bounds on the monitor state every time the loop guard is executed.

Note that this holds true regardless of the current memory, trace, or other threads in the thread pool.

We define soundness of the static oracle by describing the conditions under which the tuple \((\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\) is a conservative approximation of the branching, timing, termination, blocking, and floating behavior of a local monitor for command \( com \), with local monitor configuration \((\Gamma, lmdst, \overline{pc}, \overline{br}, time, term, block)\).

**Definition 2** (Conservative approximation of branching, timing, termination, blocking, and floating behavior). Let \( com \in Com, \Gamma \) be an environment, \( lmdst \in MdSt \), and \( time, term, \) and \( block \) be security levels, such that

- For all variables \( x \), \( lmdst \triangleright others might read(x) \implies \Gamma'(x) \sqsubseteq \mathcal{L}(x) \)
- For all variables \( x \), \( lmdst \triangleright others might write(x) \implies \mathcal{L}(x) \sqsubseteq \Gamma'(x) \)

Let \( com_{start} \) and \( com_{end} \) be defined as either \( com_{start} = com \) and \( com_{end} = stop \) or \( com_{start} = com; com' \) and \( com_{end} = stop; com' \) for some arbitrary command \( com' \).

We say that the tuple \((\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\) is a conservative approximation of the branching, timing, termination, blocking, and floating behavior for \( com, \Gamma, lmdst, \overline{pc}, \overline{br}, time, term, block \) if all of the following hold.

1) \( term_{sb} \sqsubseteq time_{sb} \)
2) For all variables \( x \), \( lmdst \triangleright others might read(x) \implies \Gamma_{sb}(x) \sqsubseteq \mathcal{L}(x) \)
3) For all variables \( x \), \( lmdst \triangleright others might write(x) \implies \mathcal{L}(x) \sqsubseteq \Gamma_{sb}(x) \)
4) For all pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon, and pool’ ∈ PST, mem’ ∈ Mem, τ’ ∈ Tr, gmon’ ∈ GMon and Γ’, lmdst’, pre’, br’, time’, term’, and block’ such that
   - pool(i) = [com\textsubscript{start}, mc, lmdst] where mc = (Γ, lmdst, pre, br, time, term, block)
   - ⟨pool, mem, τ, gmon⟩ →\textsubscript{σ} ⟨pool’, mem’, τ’, gmon’⟩
   - pool'(i) = [com\textsubscript{end}, (Γ', lmdst', pre', br', time', term', block'), lmdst']

we have:
   - Either ℓ\textsubscript{sb} = ⊥ or (time' ⊆ time\textsubscript{sb} and term' ⊆ term\textsubscript{sb} and block' ⊆ block\textsubscript{sb} and pre(Γ') = pre(Γ\textsubscript{sb}) and ∀x ∈ Var. Γ'(x) ⊆ Γ\textsubscript{sb}(x))

5) For all pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon such that
   - pool(i) = [com\textsubscript{start}, mc, lmdst] where mc = (Γ, lmdst, pre, br, time, term, block)
   - execution of pool(i) may fail to terminate normally because either pool(i) diverges before reaching command com\textsubscript{end} or the monitor for pool(i) blocks before reaching command com\textsubscript{end}

we have:
   - ℓ\textsubscript{sb} ⊆ term\textsubscript{sb} or ℓ\textsubscript{sb} ⊆ block\textsubscript{sb}

6) If ℓ\textsubscript{sb} ≠ ⊥ then sync-free(com).

7) If com = if e then com\textsubscript{1} else com\textsubscript{2} fi then Γ(⟨e⟩) ∪ time ⊆ ℓ\textsubscript{sb}.

8) If com = while e do com\textsubscript{1} od then for all pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon, and pool’ ∈ PST, mem’ ∈ Mem, τ’ ∈ Tr, gmon’ ∈ GMon, and Γ’, lmdst’, pre’, br’, time’, term’, and block’ such that
   - pool(i) = [com\textsubscript{start}, mc, lmdst] where mc = (Γ, lmdst, pre, br, time, term, block)
   - ⟨pool, mem, τ, gmon⟩ →\textsubscript{σ} ⟨pool’, mem’, τ’, gmon’⟩
   - pool'(i) = [com\textsubscript{more}, (Γ', lmdst', pre', br', time', term', block'), lmdst'] (where com\textsubscript{more} = more e do com\textsubscript{1} od if com\textsubscript{start} = com and com\textsubscript{more} = (more e do com\textsubscript{1} od); com' if com\textsubscript{start} = com; com').

we have all of the following:
   - Γ'(⟨e⟩) ∪ time' ⊆ ℓ\textsubscript{sb}.
   - Either ℓ\textsubscript{sb} = ⊥ or (time' ⊆ time\textsubscript{sb} and term' ⊆ term\textsubscript{sb} and block' ⊆ block\textsubscript{sb} and pre(Γ') = pre(Γ\textsubscript{sb}) and ∀x ∈ Var. Γ'(x) ⊆ Γ\textsubscript{sb}(x))

Note that in the clauses that require that ℓ\textsubscript{sb} is an upper bound of the branching or looping decision, we are more precise when the current thread has exclusive write access to all variables in e, i.e., lmdst ⊢ exclusivewrite(e). Specifically, if lmdst ⊢ exclusivewrite(e) then other threads cannot concurrently change the value that e will evaluate to, and thus Γ(e) is an upper bound on the information that may be learned by evaluating e here (i.e., the timing of the evaluation of e relative to other threads—bounded above by the timing level time—is not relevant). Therefore, in that case, ℓ\textsubscript{sb} must be an upper bound only of Γ(e). Otherwise, if lmdst ⊢ othersmightwrite(e), then ℓ\textsubscript{sb} must be an upper bound of Γ(⟨e⟩)∪time, since the timing of when the expression is evaluated (relative to concurrent updates to variables in e) may influence that value of the expression.

The static bounds oracle is defined in Appendix F and a sketch of the proof of soundness.

B. Local configuration results

We first define some properties about single executions of programs. These properties describe invariants and formalize the intended meaning of monitor state.

1) Properties of local monitors: Each time execution enters a conditional or a loop, a special “control state” term is added to the program’s continuation, either a join command (for conditionals) or a more e do com od command (for loops). We define set Continuation\textsubscript{n} to be the subset of commands that have exactly n occurrences of more e do com od or join as subcommands, and (for n > 0), start with a more e do com od or join command. This definition will be useful, both to specify that the depth of a program counter level stack is equal to the number of more e do com od and join commands in the continuation, and also to identify the continuation that corresponds to the continuation after a particular loop or conditional.

Continuation\textsubscript{0} = \{com | com does not contain a subcommand of the form join or more e do com od\}

Continuation\textsubscript{n+1} = \{join; com; com' | com ∈ Continuation\textsubscript{0} and com' ∈ Continuation\textsubscript{n}\}
   \cup \{join; com' | com' ∈ Continuation\textsubscript{n}\}
   \cup \{more e do com od; com'' | com' ∈ Continuation\textsubscript{0} and com'' ∈ Continuation\textsubscript{n}\}
   \cup \{more e do com od; com' | com' ∈ Continuation\textsubscript{n}\}
Property 1 (Invariants of local monitors). Let \( pool_0 \in PSr, \sigma \in \Sigma, gmon \in GMon \) and \( \langle pool, mem, \tau, gmon \rangle \in reach_\sigma(pool_0) \). If \( pool(i) = [com, (\Gamma, lmdst, \overline{pr}, \overline{br}, time, term, block), mdst] \) for some \( i \in pre(pool) \) then all of the following hold.

1) \( |\overline{br}| = |\overline{pr}| \)
2) Let \( n = |\overline{br}| \). Either \( com = com'; com'' \) where \( com' \in \text{Continuation}_0 \) and \( com'' \in \text{Continuation}_n \) or \( com \in \text{Continuation}_n \).
3) \( \text{term} \subseteq \text{time} \)
4) \( \text{pre}(\Gamma) = \{ x \mid x \in \text{FloatVar} \land (lmdst \triangleright \text{exclusiveread}(x) \lor lmdst \triangleright \text{exclusivewrite}(x)) \} \)
5) For all variables \( x, lmdst \triangleright \text{othersmighthread}(x) \implies \Gamma(x) \not\subseteq \Sigma(x) \)
6) For all variables \( x, lmdst \triangleright \text{othersmighthwrite}(x) \implies \Sigma(x) \not\subseteq \Gamma(x) \)
7) \( lmdst = mdst \)
8) If \( com = \text{term} \) then \( \overline{pr} = e, \overline{br} = e, \text{term} = \perp, \) and \( \text{block} = \perp \).
9) For all \( k \in \overline{br} \), the tuple \( \langle \overline{pr}(k), \text{time}(k), \text{term}(k), \text{block}(k), \Gamma(k) \rangle \) (where \( \overline{br}(k) = (\text{time}(k), \text{term}(k), \text{block}(k), \Gamma(k)) \)) is a conservative approximation (Definition 2) of the branching, timing, termination, and floating behavior for some \( com_k, \Gamma_k, lmdst, \overline{pr}, \overline{br}, \text{time}, \text{term}, \text{block} \) such that there is a global configuration \( \langle pool_k', \text{mem}_k', \tau_k', gmon_k' \rangle \) with

\[
\text{pool}_k'(i) = [com'_k, \langle \Gamma_k, lmdst_k, \overline{pr}_k, \overline{br}_k, \text{time}_k, \text{term}_k, \text{block}_k \rangle, mdst_k]
\]

and \( com_k \) is the redex of \( com'_k \) and \( \langle pool_k', \text{mem}_k', \tau_k', gmon_k' \rangle \in \text{reach}_\sigma(pool_0) \) and \( \langle pool_k', \text{mem}_k', \tau_k', gmon_k' \rangle \rightarrow_\sigma \langle pool, \text{mem}, \tau, gmon \rangle \).

Proof: We proceed by induction on the steps of the global configuration. In the base case, every local configuration for the global configuration \( pool_0 \) is of the form

\[
[com, \langle \Gamma, lmdst, \overline{pr}, \overline{br}, \text{time}, \text{term}, \text{block} \rangle, mdst]
\]

where

- \( \text{time} = \perp \)
- \( \text{term} = \perp \)
- \( \text{block} = \perp \)
- \( \overline{pr} \) is empty
- \( \overline{br} \) is empty
- \( com \in \text{Continuation}_0 \)
- \( \text{pre}(\Gamma) = \{ x \mid x \in \text{FloatVar} \land (lmdst \triangleright \text{exclusiveread}(x) \lor lmdst \triangleright \text{exclusivewrite}(x)) \} \)
- \( \forall x \in \text{pre}(\Gamma).\Gamma(x) = \Sigma(x) \)
- \( \text{lmdst} = \text{mdst} \)

All of the invariants hold.

For the inductive case, assume the invariants hold true of all local configurations in a global configuration

\[
gcnf = \langle pool, \text{mem}, \tau, gmon \rangle,
\]

and consider

\[
gcnf_1 \rightarrow_\sigma \langle pool', \text{mem}', \tau', gmon' \rangle
\]

There are two possible inference rules that allow a global configuration to take a step. In one of them, all local configurations take a synchronization step.

\[
\langle pool(i), \text{mem}, \tau \rangle \xrightarrow{\text{sync,} \Delta(i)} \langle pool'(i), \text{mem}, \tau \rangle
\]

In the other inference rule that allows a global configuration to take a step, one local configuration takes a step:

\[
\langle pool(i), \text{mem}, \tau \rangle \xrightarrow{\text{other}} \langle pool'(i), \text{mem}', \tau' \rangle
\]

We cover both of these cases at the same time by performing an induction on the local monitor step associated with the local configuration step. Consider

\[
\langle \Gamma, lmdst, \overline{pr}, \overline{br}, \text{time}, \text{term}, \text{block} \rangle \rightarrow_{\text{perm}} \langle \Gamma', lmdst', \overline{pr}', \overline{br}', \text{time}', \text{term}', \text{block}' \rangle
\]
Assume that the invariants hold for pool(i), and we need to show they hold for pool'(i).

- **M-Skip, M-Output, M-Term, M-More.** Here δ = ε, Γ′ = Γ and lmdst′ = lmdst and \( \overline{pc} = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = term and block′ = block.
  All invariants except for Condition 8 hold trivially, since they held for pool(i). For Condition 8 if the monitor rule was M-Term, then from the premises of M-Term, we have \( \overline{pc} = \epsilon \), \( \overline{br} = \epsilon \), term = ⊥, and block = ⊥, as required.

- **M-Assign1** Here δ = ε, Γ′ = Γ(x ↦ \( t_{lmdst} \)) where
  \[
  \ell = Γ(ε)∪time∪(\overline{pc})∪term∪block
  \]
  and
  \[
  lmdst > [\text{maywrite}(x),\text{mayread}(e),\text{othersmightread}(x)]
  \]
  and \( \ell \subseteq L(x) \) and lmdst′ = lmdst and \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = term and block′ = block.
  All invariants hold either by simple inspection or because they held for pool(i).

- **M-Assign2** Here δ = ε, Γ′ = Γ(x ↦ \( t_{lmdst} \)) where
  \[
  \ell = Γ(ε)∪time∪(\overline{pc})∪term∪block
  \]
  and
  \[
  lmdst > [\text{maywrite}(x),\text{mayread}(e),\text{exclusiveread}(x)]
  \]
  and lmdst′ = lmdst and \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = term and block′ = block.
  All invariants hold either by simple inspection or because they held for pool(i).

- **M-Input1** Here δ = ε, Γ′ = Γ(x ↦ \( t_{lmdst} \)) where
  \[
  \ell = ch∪time∪(\overline{pc})∪term∪block
  \]
  and
  \[
  lmdst > [\text{maywrite}(x),\text{othersmightread}(x)]
  \]
  and \( \ell \subseteq L(x) \) and lmdst′ = lmdst and \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = term and block′ = block.
  All invariants hold either by simple inspection or because they held for pool(i).

- **M-Input2** Here δ = ε, Γ′ = Γ(x ↦ \( t_{lmdst} \)) where
  \[
  \ell = ch∪time∪(\overline{pc})∪term∪block
  \]
  and
  \[
  lmdst > [\text{maywrite}(x),\text{exclusiveread}(x)]
  \]
  and lmdst′ = lmdst and \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = term and block′ = block.
  All invariants hold either by simple inspection or because they held for pool(i).

- **M-Barrier-Local** Here \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and time′ = time and term′ = time and block′ = block.
  All invariants except for Condition 4 hold trivially, since they held for pool(i). For Condition 4 if the monitor rule was M-Barrier-Local, we have \( \overline{pc} = \epsilon \), \( \overline{br} = \epsilon \), term = ⊥, and block = ⊥, as required.

- **M-Branch** Here δ = ε, Γ′ = Γ and lmdst′ = lmdst and \( \overline{pc}' = \overline{pc} \) and \( \overline{br}' = \overline{br} \) and \((t_{ab}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})\) where
  \[
  (t_{ab}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb}) = \text{SB}(\text{if } e \text{ then } \text{com}_1\text{ else } \text{com}_2\text{ fi }, Γ, lmdst, \overline{pc}, time, term, block)
  \]
  and time′ = time and term′ = term and block′ = block.
Except for conditions 2 and 9, all invariants hold trivially, since they held for pool(i).

For condition 3 by inversion on the command, we see that a conditional statement if $e$ then $\text{com}_1$ else $\text{com}_2$ was reduced to either $\text{com}_1$; join or $\text{com}_2$; join, and thus the new command for the local configuration has one more join statement than the previous command, and the size of the program counter stack has also increased by one.

For condition 9 by Lemma 14, $(\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$ is a conservative approximation of the timing, termination, blocking, and floating behavior for $\text{com}$, $\Gamma$, $\text{lmdst}$, $\text{pc}$, $\text{br}$, $\text{time}$, $\text{term}$, and $\text{block}$ where $\text{com}$ is the command of the local configuration before the step, as required.

- **M-Join** Here $\delta = e$, $\text{lmdst}' = \text{lmdst}$ and $\text{pc}' - \ell_{sb} = \text{pc}$ and $\text{br}' - (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{br}$ and $\text{time}' = \text{time} \cup \text{time}_{sb} \cup \ell_{sb}$ and $\text{term}' = \text{term} \cup \text{term}_{sb}$ and $\text{block}' = \text{block} \cup \text{block}_{sb}$ and

$$\Gamma' = \lambda x. \begin{cases} \Gamma(x) \cup \Gamma_{sb}(x) & \text{if } x \in \text{pre}(\Gamma) \cap \text{pre}(\Gamma_{sb}) \\ \Gamma(x) & \text{if } x \in \text{pre}(\Gamma) \setminus \text{pre}(\Gamma_{sb}) \\ \text{undef} & \text{otherwise} \end{cases}$$

Since both the pc stack and the branch information stack decrease in size by one element, condition 1 is satisfied.

By inversion on the command step, we see that the command has one less join in it than previously, and the size of the pc stack has reduced by one, so condition 2 is satisfied.

Since $\text{term} \subseteq \text{time}$ and $\text{term}_{sb} \subseteq \text{time}_{sb}$ (by Definition 2), we have $\text{term}' \subseteq \text{time}'$, satisfying condition 3.

Condition 4 is satisfied since $\text{pre}(\Gamma) = \text{pre}(\Gamma')$.

Conditions 5 and 6 hold due to the definition of conservative approximation (Definition 2).

Conditions 7 and 8 are trivially satisfied, and condition 9 follows immediately since it held for pool(i).

- **M-Enter** Here $\delta = e$, $\text{com}' = \text{com}$ and $\text{pc}' - \ell_{sb} = \text{pc}$ and $\text{br}' - (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{br}$ and $\text{time}' = \text{time} \cup \text{time}_{sb} \cup \ell_{sb}$ and $\text{term}' = \text{term} \cup \text{term}_{sb}$ and $\text{block}' = \text{block} \cup \text{block}_{sb}$ and

$$\Gamma' = \lambda x. \begin{cases} \Gamma(x) \cup \Gamma_{sb}(x) & \text{if } x \in \text{pre}(\Gamma) \cap \text{pre}(\Gamma_{sb}) \\ \Gamma(x) & \text{if } x \in \text{pre}(\Gamma) \setminus \text{pre}(\Gamma_{sb}) \\ \text{undef} & \text{otherwise} \end{cases}$$

Since both the pc stack and the branch information stack decrease in size by one element, condition 1 is satisfied.

By inversion on the command step, we see that the command has one less join in it than previously, and the size of the pc stack has reduced by one, so condition 2 is satisfied.

Since $\text{term} \subseteq \text{time}$ and $\text{term}_{sb} \subseteq \text{time}_{sb}$ (by Definition 2), we have $\text{term}' \subseteq \text{time}'$, satisfying condition 3.

Condition 4 is satisfied since $\text{pre}(\Gamma) = \text{pre}(\Gamma')$.

Conditions 5 and 6 hold due to the definition of conservative approximation (Definition 2).

Conditions 7 and 8 are trivially satisfied, and condition 9 follows immediately since it held for pool(i).

- **M-Leave** Here $\delta = e$, $\text{com}' = \text{com}$ and $\text{pc}' - \ell_{sb} = \text{pc}$ and $\text{br}' - (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{br}$ and $\text{time}' = \text{time} \cup \text{time}_{sb} \cup \ell_{sb}$ and $\text{term}' = \text{term} \cup \text{term}_{sb}$ and $\text{block}' = \text{block} \cup \text{block}_{sb}$ and

$$\Gamma' = \lambda x. \begin{cases} \Gamma(x) \cup \Gamma_{sb}(x) & \text{if } x \in \text{pre}(\Gamma) \cap \text{pre}(\Gamma_{sb}) \\ \Gamma(x) & \text{if } x \in \text{pre}(\Gamma) \setminus \text{pre}(\Gamma_{sb}) \\ \text{undef} & \text{otherwise} \end{cases}$$

Since both the pc stack and the branch information stack decrease in size by one element, condition 1 is satisfied.

By inversion on the command step, we see that the command has one less join in it than previously, and the size of the pc stack has reduced by one, so condition 2 is satisfied.

Since $\text{term} \subseteq \text{time}$ and $\text{term}_{sb} \subseteq \text{time}_{sb}$ (by Definition 2), we have $\text{term}' \subseteq \text{time}'$, satisfying condition 3.

Condition 4 is satisfied since $\text{pre}(\Gamma) = \text{pre}(\Gamma')$.

Conditions 5 and 6 hold due to the definition of conservative approximation (Definition 2).

Conditions 7 and 8 are trivially satisfied, and condition 9 follows immediately since it held for pool(i).

\[ \]
2) For all channels \( ch \) if \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\subseteq ch \) then \( \tau_1 \models \tau_2 \models \text{ch} \) (i.e., \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \) is a lower bound on the \( \text{I/O} \) effects.)

3) \( \text{term}_1 \subseteq \text{term}_2 \) (i.e., termination level increases monotonically)

4) \( \text{block}_1 \subseteq \text{block}_2 \) (i.e., blocking level increases monotonically)

5) If \( \beta \neq \text{sync} \) then \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \subseteq \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \) (i.e., the “time-inclusive pc” can go down only at synchronization steps)

6) If \( \beta \neq \text{sync} \) then \( \text{time}_1 \subseteq \text{time}_2 \) (i.e., timing level increases monotonically, except at synchronizations)

7) If \( \beta \neq \text{sync} \) then \( \text{lmdst}_1 = \text{lmdst}_2 \) (i.e., mode state changes only at synchronizations)

8) If \( (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\subseteq \) then \( \beta \neq \text{sync} \) and \( \text{com}_2 \neq \text{term} \) (i.e., non-bottom termination, blocking, and pc prevents synchronization or thread termination.)

9) If \( \text{pc}_1 \neq \text{term} \) then \( \text{com}_2 \neq \text{term} \) (i.e., non-empty pc stack prevents thread termination.)

Proof: We consider the local monitor step associated with the local configuration step \( ([\text{com}_1, \text{mc}_1, \text{mdst}_1, \text{mem}_1, \tau_1]) \overset{\beta, \gamma, \delta}{\longrightarrow} ([\text{com}_2, \text{mc}_2, \text{mdst}_2, \text{mem}_2, \tau_2]) \) . That is, we consider the step

\[
(\Gamma_1, \text{lmdst}_1, \text{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1) \rightarrow^c_\text{perm} (\Gamma_2, \text{lmdst}_2, \text{pc}_2, \text{time}_2, \text{term}_2, \text{block}_2)
\]

- **M-Skip** Here \( \Gamma_2 = \Gamma_1 \) and \( \text{lmdst}_2 = \text{lmdst}_1 \) and \( \text{pc}_2 = \text{pc}_1 \) and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

  Note that by inversion on the command step, we also have that the memory is not changed and \( \text{com}_2 \neq \text{term} \). All of the conditions are satisfied trivially.

- **M-Assign1** Here \( \Gamma_2 = \Gamma_1(x \rightarrow \text{lmdst}_1, \ell) \) where

\[
\ell = \Gamma(e) \sqcup \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1
\]

and

\[
\text{lmdst}_1 > [\text{maywrite}(x), \text{mayread}(e), \text{othersmightread}(x)]
\]

and \( \ell \subseteq \mathcal{L}(x) \) and \( \text{lmdst}_2 = \text{lmdst}_1 \) and \( \text{pc}_2 = \text{pc}_1 \) and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

Note that by inversion on the command step, we have that for all variables \( y \) if \( y \neq x \) then \( \text{mem}_1(y) = \text{mem}_2(y) \) and \( \Gamma_1(y) = \Gamma_2(y) \), and \( \text{com}_2 \neq \text{term} \).

All of the conditions except condition \( \square \) are satisfied trivially.

Consider \( \Gamma_2(x) \). If \( x \notin \text{pre}(\Gamma_2) \) then \( \Gamma_2(x) = \mathcal{L}(x) \), and, since \( \ell \subseteq \mathcal{L}(x) \) we have \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \subseteq \Gamma_2(x). \)

If \( x \in \text{pre}(\Gamma_2) \) then \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\subseteq \ell \subseteq \Gamma_2(x) \). Thus, condition \( \square \) is satisfied.

- **M-Assign2** Here \( \Gamma_2 = \Gamma_1(x \rightarrow \text{lmdst}_1, \ell) \) where

\[
\ell = \Gamma(e) \sqcup \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1
\]

and

\[
\text{lmdst}_1 > [\text{maywrite}(x), \text{mayread}(e), \text{exclusiveread}(x)]
\]

and \( \text{lmdst}_2 = \text{lmdst}_1 \) and \( \text{pc}_2 = \text{pc}_1 \) and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

Note that by inversion on the command step, we have that for all variables \( y \) if \( y \neq x \) then \( \text{mem}_1(y) = \text{mem}_2(y) \) and \( \Gamma_1(y) = \Gamma_2(y) \), and \( \text{com}_2 \neq \text{term} \).

All of the conditions except condition \( \square \) are satisfied trivially.

Consider \( \Gamma_2(x) \). Since \( \text{lmdst}_2 > [\text{exclusiveread}(x)] \), we must have \( x \in \text{pre}(\Gamma_2) \). Thus, \( \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\subseteq \ell \subseteq \Gamma_2(x) \), and so condition \( \square \) is satisfied.

- **M-Input1** Here \( \Gamma_2 = \Gamma_1(x \rightarrow \text{lmdst}_1, \ell) \) where

\[
\ell = \text{ch} \sqcup \text{time}_1 \sqcup (\sqcup \text{pc}_1) \sqcup \text{term}_1 \sqcup \text{block}_1
\]

and

\[
\text{lmdst}_1 > [\text{maywrite}(x), \text{othersmightread}(x)]
\]

and \( \ell \subseteq \mathcal{L}(x) \) and \( \text{lmdst}_2 = \text{lmdst}_1 \) and \( \text{pc}_2 = \text{pc}_1 \) and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

Note that by inversion on the command step, we have that for all variables \( y \) if \( y \neq x \) then \( \text{mem}_1(y) = \text{mem}_2(y) \) and \( \Gamma_1(y) = \Gamma_2(y) \), and \( \text{com}_2 \neq \text{term} \), and the trace had an event for channel \text{ch} added.

All of the conditions except conditions \( \square \) and \( \Box \) are satisfied trivially.

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Consider $G_2(x)$. If $x \not\in \text{pre}(G_2)$ then $G_2(x) = \mathcal{L}(x)$, and, since $\ell \subseteq \mathcal{L}(x)$ we have $\text{time}_1 \cup (\cup \bar{pc}_2) \cup \text{term}_1 \cup \text{block}_1 \subseteq G_2(x)$. If $x \in \text{pre}(G_2)$ then $\text{time}_1 \cup (\cup \bar{pc}_2) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell \subseteq G_2(x)$. Thus, condition 1 is satisfied. Also, $\text{time} \cup (\cup \bar{pc}_2) \cup \text{term} \cup \text{block} \subseteq \text{ch}$, and so condition 2 is satisfied.

**M-Input** Here $G_2 = \Gamma_1(x \mapsto \text{lmdest}_1 \ell)$ where

$\ell = \text{ch} \cup \text{time}_1 \cup (\cup \bar{pc}_1) \cup \text{term}_1 \cup \text{block}_1$

and

$\text{lmdest}_1 \triangleright [\text{maywrite}(x), \text{exclusiveRead}(x)]$

and $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 = \bar{pc}_1$ and $\bar{br}_2 = \bar{br}_1$ and $\text{time}_2 = \text{time}_1$ and $\text{term}_2 = \text{term}_1$ and $\text{block}_2 = \text{block}_1$.

Note that by inversion on the command step, we have that for all variables $y$ if $y \neq x$ then $\text{mem}_1(y) = \text{mem}_2(y)$ and $G_1(y) = G_2(y)$, and $\text{com}_2 \neq \text{term}$, and the trace had an event for channel $\text{ch}$ added.

All of the conditions except conditions 1 and 2 are satisfied trivially.

Consider $G_2(x)$. Since $\text{lmdest} \triangleright [\text{exclusiveRead}(x)]$, we must have $x \in \text{pre}(G_2)$. Thus, $\text{time}_1 \cup (\cup \bar{pc}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell \subseteq G_2(x)$, and so condition 1 is satisfied.

Also, $\text{time} \cup (\cup \bar{pc}_2) \cup \text{term} \cup \text{block} \subseteq \text{ch}$, and so condition 2 is satisfied.

**M-Output** Here $G_2 = \Gamma_1$ and $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 = \bar{pc}_1$ and $\bar{br}_2 = \bar{br}_1$ and $\text{time}_2 = \text{time}_1$ and $\text{term}_2 = \text{term}_1$ and $\text{block}_2 = \text{block}_1$.

Note that by inversion on the command step, we have that the memory is unchanged, and $\text{com}_2 \neq \text{term}$, and the trace had an event for channel $\text{ch}$ added.

All of the conditions except condition 2 are satisfied trivially, and since $\text{time} \cup (\cup \bar{pc}_2) \cup \text{term} \cup \text{block} \subseteq \text{ch}$, condition 2 is also satisfied.

**M-Term** Here $G_2 = \Gamma_1$ and $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 = \bar{pc}_1$ and $\bar{br}_2 = \bar{br}_1$ and $\text{time}_2 = \text{time}_1$ and $\text{term}_2 = \text{term}_1 = \perp$ and $\text{block}_2 = \text{block}_1 = \perp$ and, by inversion on the command step, $\text{com}_2 = \text{term}$.

All conditions are trivially satisfied.

**M-Barrier-Local** Here $\bar{pc}_2 = \bar{pc}_1$ and $\bar{br}_2 = \bar{br}_1$ and $\text{time}_2 = \text{time}_1$ and $\text{term}_2 = \text{term}_1 = \perp$ and $\text{block}_2 = \text{block}_1 = \perp$.

By inversion on the command step, we see that the memory is unchanged and so is the event trace. Moreover, $\beta = \text{sync}$ and $\text{com}_2 \neq \text{term}$. Thus, all conditions are satisfied.

**M-Branch** Here $G_2 = \Gamma_1$ and $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 = \bar{pc}_1, \ell_{sb}$ and $\bar{br}_2 = \bar{br}_1 \cdot (\text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$ where

$\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{SB}(\text{if } e \text{ then } \text{com}_1 \text{ else } \text{com}_2 \text{ fi, } \Gamma_1, \text{lmdest}_1, \bar{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1)$

and $\text{time}_2 = \text{time}_1$ and $\text{term}_2 = \text{term}_1$ and $\text{block}_2 = \text{block}_1$.

By inversion on the command step, we see that the memory and event trace are unchanged, and that $\beta \neq \text{sync}$ and $\text{com}_2 \neq \text{term}$.

All conditions are thus trivially satisfied.

**M-Join** Here $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 \cdot \ell_{sb} = \bar{pc}_1$ and $\bar{br}_2 \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \bar{br}_1 \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$ where

$\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{SB}(\text{while } e \text{ do } \text{com}_2 \text{ od, } \Gamma_1, \text{lmdest}_1, \bar{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1)$

and $\text{time}_2 = \text{time}_1 \cup \text{term}_2 = \text{term}_1 \cup \text{block}_2 = \text{block}_1 \cup \text{block}_2$ and

$\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{SB}(\text{while } e \text{ do } \text{com}_2 \text{ od, } \Gamma_1, \text{lmdest}_1, \bar{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1)$

Thus all conditions are satisfied.

**M-Enter** $G_2 = \Gamma_1$ and $\text{lmdest}_2 = \text{lmdest}_1$ and $\bar{pc}_2 = \bar{pc}_1, \ell_{sb}$ and $\bar{br}_2 = \bar{br}_1 \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$ where

$\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \text{SB}(\text{while } e \text{ do } \text{com}_2 \text{ od, } \Gamma_1, \text{lmdest}_1, \bar{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1)$
and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

By inversion on the command step, we see that the memory and event trace are unchanged, and that \( \beta \neq \text{sync} \) and \( \text{com}_2 \neq \text{term} \).

All conditions are thus trivially satisfied.

- **M-More** Here \( \Gamma_2 = \Gamma_1 \) and \( \text{ldst}_2 = \text{ldst}_1 \) and \( \text{mc}_2 = \text{mc}_1 \) and \( \text{br}_2 = \text{br}_1 \) and \( \text{time}_2 = \text{time}_1 \) and \( \text{term}_2 = \text{term}_1 \) and \( \text{block}_2 = \text{block}_1 \).

By inversion on the command step, we see that the memory and event trace are unchanged, and that \( \beta \neq \text{sync} \) and \( \text{com}_2 \neq \text{term} \).

All conditions are thus trivially satisfied.

- **M-Leave** Here \( \text{ldst}_2 = \text{ldst}_1 \) and \( \text{mc}_2 \) and \( \text{sbl}_2 \) and \( \text{br}_2 \) and \( \text{time}_2 \) and \( \text{term}_2 \) and \( \text{block}_2 \) and \( \text{block}_2 \) and

\[
\Gamma_2 = \lambda x. \begin{cases} 
\Gamma_1(x) & \text{if } x \in \text{pre}(\Gamma_1) \cap \text{pre}(\Gamma_{sb}) \\
\text{undef} & \text{otherwise}
\end{cases}
\]

By inversion on the command step, we see that the memory and event trace are unchanged, and that \( \beta \neq \text{sync} \) and \( \text{com}_2 \neq \text{term} \).

All conditions are thus trivially satisfied.

\[\blacksquare\]

### C. \( \ell \)-equivalence relations

We define \( \ell \)-equivalence relations over local configurations and over global configurations. Intuitively, two (local, global) configurations are \( \ell \)-equivalent if they agree on information that is at level \( \ell \) or below.

**Definition 3** (\( \ell \)-equivalence of local configurations). Two local configurations

\[
\langle \{\text{com}_1, \text{mc}_1, \text{ldst}_1\}, \text{mem}_1, \tau_1 \rangle \text{ and } \langle \{\text{com}_2, \text{mc}_2, \text{ldst}_2\}, \text{mem}_2, \tau_2 \rangle
\]

are \( \ell \)-equivalent if and only if all the following conditions hold. Assume that for \( i = 1, 2 \) we have

\[
\text{mc}_i = \langle \Gamma_i, \text{ldst}_i, \text{mc}_i, \text{br}_i, \text{time}_i, \text{term}_i, \text{block}_i \rangle
\]

1. \( \tau_1 \downarrow \ell = \tau_2 \downarrow \ell \).
2. \( \text{ldst}_1 = \text{ldst}_2 \).
3. \( \text{ldst}_1 = \text{ldst}_2 \).
4. \( \text{pre}(\Gamma_1) = \text{pre}(\Gamma_2) \).
5. For all variables \( x \), if \( x \in \text{pre}(\Gamma_1) \) and \( \text{ldst}_1 \uparrow \text{mayread}(x) \) and \( \Gamma_1(x) \cup \text{time}_1 \cup (\cup \text{mc}_1) \subseteq \ell \) and \( \Gamma_2(x) \cup \text{time}_2 \cup (\cup \text{mc}_2) \subseteq \ell \) then \( \text{mem}_1(x) = \text{mem}_2(x) \) and \( \Gamma_1(x) = \Gamma_2(x) \).
6. For all variables \( x \), if \( x \not\in \text{pre}(\Gamma_1) \) and \( \text{ldst}_1 \uparrow \text{mayread}(x) \) and \( \mathcal{L}(x) \cup \text{time}_1 \cup (\cup \text{mc}_1) \subseteq \ell \) and \( \mathcal{L}(x) \cup \text{time}_2 \cup (\cup \text{mc}_2) \subseteq \ell \) then \( \text{mem}_1(x) = \text{mem}_2(x) \).
7. If

\[
\text{time}_1 \cup (\cup \text{mc}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell
\]

or

\[
\text{time}_2 \cup (\cup \text{mc}_2) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell
\]

then \( \text{time}_1 = \text{time}_2 \) and \( \text{term}_1 = \text{term}_2 \) and \( \text{block}_1 = \text{block}_2 \) and \( \text{mc}_1 = \text{mc}_2 \) and \( \text{com}_1 = \text{com}_2 \) and for all \( x \), \( \Gamma_1(x) = \Gamma_2(x) \).

8. For all \( j \in 1..\max(\lceil \text{mc}_1 \rceil, \lceil \text{mc}_2 \rceil) \), if

\[
\text{time}_1 \cup (\cup \text{mc}_1(0) \cup \ldots \cup \text{mc}_1(j-1)) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell
\]

or

\[
\text{time}_2 \cup (\cup \text{mc}_2(0) \cup \ldots \cup \text{mc}_2(j-1)) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell
\]

then all of the following holds
- \( \lceil \text{mc}_1 \rceil \geq j \).
- \( \lceil \text{mc}_2 \rceil \geq j \).
- For all \( k \in 0..j \) we have \( \text{mc}_1(k) = \text{mc}_2(k) \) and \( \text{br}_1(k) = \text{br}_2(k) \).
There exists $con_{cnt} \in \text{Continuation}_1$ such that:

- Either $con_1 = con_{cnt}$ or $con_1 = con_1'$; $con_{cnt}$ for some $con_1'$; and
- Either $con_2 = con_{cnt}$ or $con_2 = con_2'$; $con_{cnt}$ for some $con_2'$.

(This says that the pc level stack and the branch environments are identical up to and including the first high branch, and the two configurations agree on the low continuation.)

Note that the definition of $\ell$-equivalence of local configurations is symmetric. That is, if $lcnf_1$ and $lcnf_2$ are $\ell$-equivalent, then $lcnf_2$ and $lcnf_1$ are $\ell$-equivalent.

We now define $\ell$-equivalence for global configurations, using $\ell$-equivalence for local configurations as part of the definition.

**Definition 4 ($\ell$-equivalence of global configurations).** Two global configurations $(\langle pool_1, mem_1, \tau_1, gmon_1 \rangle)$ and $(\langle pool_2, mem_2, \tau_2, gmon_2 \rangle)$ are $\ell$-equivalent if and only if all the following conditions hold.

- $pool_1 = pool_2$.
- $\forall i \in \text{pre}(pool_1), (pool_1(i), mem_1, \tau_1)$ is $\ell$-equivalent to $(pool_2(i), mem_2, \tau_2)$.
- $\tau_1 \downarrow \ell = \tau_2 \downarrow \ell$.
- $gmon_1 = gmon_2$

**D. $\ell$-equivalent local configuration results**

We define lemmas and theorems that describe the behavior of $\ell$-equivalent local configurations when one configuration takes a step.

**Lemma 1 (Low-equivalent expressions).** Let

\[
lcnf_1 = \langle \langle \text{com}_1, \langle \text{I}_1, \text{lmdst}_1, \overline{\text{pc}}_1, \overline{\text{br}}_1, \text{time}_1, \text{term}_1, \text{block}_1 \rangle, \text{mdst}_1 \rangle, \text{mem}_1, \tau_1 \rangle
\]

and

\[
lcnf_2 = \langle \langle \text{com}_2, \langle \text{I}_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{\text{br}}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle, \text{mdst}_2 \rangle, \text{mem}_2, \tau_2 \rangle
\]

be $\ell$-equivalent local configurations.

For any expression $e$ such that $\text{lmdst}_1 \triangleright \text{mayread}(e)$, if $\Gamma_1(e) \sqcup \text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_1) \subseteq \ell$ and $\Gamma_2(e) \sqcup \text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_2) \subseteq \ell$ and $e, \text{mem}_1 \downarrow \ell_1$ and $e, \text{mem}_2 \downarrow \ell_2$ then $\ell_1 = \ell_2$.

**Proof:** By induction on the structure of $e$. The interesting case is for a variable $x$. Since $\text{lmdst}_1 \triangleright \text{mayread}(e)$ we have $\text{lmdst}_1 \triangleright \text{mayread}(x)$. Since $\Gamma_1(e) \sqcup \text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_1) \subseteq \ell$ we have $\Gamma_1(x) \sqcup \text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_1) \subseteq \ell$ and from $\Gamma_2(e) \subseteq \ell$ we have $\Gamma_2(x) \sqcup \text{time}_2 \sqcup (\sqcup \overline{\text{pc}}_2) \subseteq \ell$. So by conditions 5 and 6 we have $\text{mem}_1(x) = \text{mem}_2(x)$ as required.

**Theorem 7 (Thread Low Step).** Let $\sigma_1$ and $\sigma_2$ be $\ell$-equivalent strategies and let

\[
lcnf_1 = \langle \langle \text{com}_1, \langle \text{I}_1, \text{lmdst}_1, \overline{\text{pc}}_1, \overline{\text{br}}_1, \text{time}_1, \text{term}_1, \text{block}_1 \rangle, \text{mdst}_1 \rangle, \text{mem}_1, \tau_1 \rangle
\]

and

\[
lcnf_2 = \langle \langle \text{com}_2, \langle \text{I}_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{\text{br}}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle, \text{mdst}_2 \rangle, \text{mem}_2, \tau_2 \rangle
\]

be $\ell$-equivalent local configurations such that

\[
\text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \subseteq \ell
\]

and

\[
\text{time}_1 \sqcup (\sqcup \overline{\text{pc}}_2) \sqcup \text{term}_2 \sqcup \text{block}_2 \subseteq \ell
\]

and $lcnf_1 \xrightarrow{\ell, \sigma_1} lcnf_1'$.

Then $lcnf_2 \xrightarrow{\ell, \sigma_2} lcnf_2'$ and $lcnf_1'$ and $lcnf_2'$ are $\ell$-equivalent.

**Proof:** From $\ell$-equivalence of the two configurations (condition 7) we have that $\text{term}_1 = \text{term}_2$ and $\text{block}_1 = \text{block}_2$ and $\overline{\text{pc}}_1 = \overline{\text{pc}}_2$ and $\overline{\text{br}}_1 = \overline{\text{br}}_2$ and $\text{com}_1 = \text{com}_2$ and for all $x$, $\Gamma_1(x) = \Gamma_2(x)$.

Since $lcnf_1 \xrightarrow{\ell, \sigma_1} lcnf_1'$, we have

\[
\langle \text{com}_1, \text{mem}_1, \tau_1 \rangle \xrightarrow{\alpha, e, \sigma_1} (\text{com}_1', \text{mem}_1', \tau_1')
\]

and

\[
\langle \text{com}_1, \text{mem}_1, \tau_1 \rangle \xrightarrow{\alpha, e, \sigma_1} (\text{com}_1', \text{mem}_1', \tau_1')
\]

We proceed by induction on the judgment $(\text{com}_1, \text{mem}_1, \tau_1) \xrightarrow{\alpha, e, \sigma_1} (\text{com}_1', \text{mem}_1', \tau_1')$.  

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\textbullet{} \quad (x := e, \text{mem, } \tau) \xrightarrow{s_{\sigma},e} (\text{stop, mem}[x \mapsto v], \tau)

Here, \text{com}_1 \equiv x := e and \text{com}'_1 \equiv \text{stop and mem}'_1 = \text{mem}_1[x \mapsto v_1] and \tau'_1 = \tau_1 and e, \text{mem}_1 \downarrow v_1.

By inversion on the local monitor rules, there are two possible rules that may apply: M-Assign1 and M-Assign2. In both of these cases, we have \text{lmdst}_1 = \text{lmdst}'_1 and \text{per}_1 = \text{per}'_1 and \text{br}_1 = \text{br}'_1 and \text{time}_1 = \text{time}'_1 and \text{term}_1 = \text{term}'_1 and \text{block}_1 = \text{block}'_1 and \Gamma'_1 = \Gamma_1(x \mapsto \text{lmdst}_1, \ell') where \ell' = \Gamma(e)\cup\text{time}_1\cup\text{term}_1\cup\text{block}_1.

We can construct

\[(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{s_{\sigma},e} (\text{com}'_2, \text{mem}'_2, \tau'_2)\]

and

\[(\Gamma_2, \text{lmdst}_2, \text{per}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2) \xrightarrow{\text{e}, \text{per}_2}(\Gamma'_2, \text{lmdst}'_2, \text{per}'_2, \text{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2)\]

where \text{com}_2 \equiv x := e and \text{com}'_2 \equiv \text{stop and mem}_2 = \text{mem}_2[x \mapsto v_2] and \tau'_2 = \tau_2 and e, \text{mem}_2 \downarrow v_2 and \text{lmdst}_2 = \text{lmdst}'_2 and \text{per}_2 = \text{per}'_2 and \text{br}_2 = \text{br}'_2 and \text{time}_2 = \text{time}'_2 and \text{term}_2 = \text{term}'_2 and \text{block}_2 = \text{block}'_2 and \Gamma'_2 = \Gamma_2(x \mapsto \text{lmdst}_2, \ell')

where \ell' = \Gamma_2(e)\cup\text{time}_2\cup\text{term}_2\cup\text{block}_2.

All of the conditions for \ell\text{-equivalence of lcnf}_1 and lcnf_2 follow trivially from \ell\text{-equivalence of lenf}_1 and lenf_2, except for conditions 5 and 6.

If \Gamma'_1(e) \subseteq \ell and \text{time}_1 \subseteq \ell, then \Gamma'_1(x) \subseteq \ell and \Gamma'_1(x) = \Gamma'_2(x) and by Lemma 1 we have \text{v}_1 = \text{v}_2, so conditions 5 and 6 are satisfied.

If \Gamma'_1(e) \nsubseteq \ell or \text{time}_1 \nsubseteq \ell, then \Gamma'_1(x) \nsubseteq \ell and so conditions 5 and 6 are satisfied.

\textbullet{} \quad (\text{skip, mem, } \tau) \xrightarrow{s_{\sigma}} (\text{com}, \text{mem, } \tau)

Here, \text{com}_1 \equiv \text{skip and com}'_1 \equiv \text{stop and mem}'_1 = \text{mem}_1 and \tau'_1 = \tau_1.

By inversion on the local monitor step, rule M-Skip must have been used, and we have \Gamma_1 = \Gamma'_1 and \text{lmdst}_1 = \text{lmdst}'_1 and \text{per}_1 = \text{per}'_1 and \text{br}_1 = \text{br}'_1 and \text{time}_1 = \text{time}'_1 and \text{term}_1 = \text{term}'_1 and \text{block}_1 = \text{block}'_1.

We can construct

\[(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{s_{\sigma},e} (\text{com}'_2, \text{mem}'_2, \tau'_2)\]

and

\[(\Gamma_2, \text{lmdst}_2, \text{per}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2) \xrightarrow{\text{e}, \text{per}_2}(\Gamma'_2, \text{lmdst}'_2, \text{per}'_2, \text{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2)\]

where \text{com}_2 \equiv \text{skip and com}'_2 \equiv \text{stop and mem}_2 = \text{mem}_2 and \tau'_2 = \tau_2 and \text{lmdst}_2 = \text{lmdst}'_2 and \text{per}_2 = \text{per}'_2 and \text{br}_2 = \text{br}'_2 and \text{time}_2 = \text{time}'_2 and \text{term}_2 = \text{term}'_2 and \text{block}_2 = \text{block}'_2 and \Gamma'_2 = \Gamma_2.

The result follows trivially from the \ell\text{-equivalence of lenf}_1 and lenf_2.

\textbullet{} \quad (\text{stop, com, mem, } \tau) \xrightarrow{s_{\sigma}} (\text{com}, \text{mem, } \tau)

Here, \text{com}_1 \equiv \text{stop; com}'_1 and \text{mem}'_1 = \text{mem}_1 and \tau'_1 = \tau_1.

By inversion on the local monitor step, rule M-Skip must have been used, and we have \Gamma_1 = \Gamma'_1 and \text{lmdst}_1 = \text{lmdst}'_1 and \text{per}_1 = \text{per}'_1 and \text{br}_1 = \text{br}'_1 and \text{time}_1 = \text{time}'_1 and \text{term}_1 = \text{term}'_1 and \text{block}_1 = \text{block}'_1.

We can construct

\[(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{s_{\sigma},e} (\text{com}'_2, \text{mem}'_2, \tau'_2)\]

and

\[(\Gamma_2, \text{lmdst}_2, \text{per}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2) \xrightarrow{\text{e}, \text{per}_2}(\Gamma'_2, \text{lmdst}'_2, \text{per}'_2, \text{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2)\]

where \text{com}_2 \equiv \text{stop; com}'_2 and \text{mem}_2 = \text{mem}_1 and \tau'_2 = \tau_1 and \text{lmdst}_2 = \text{lmdst}'_2 and \text{per}_2 = \text{per}'_2 and \text{br}_2 = \text{br}'_2 and \text{time}_2 = \text{time}'_2 and \text{term}_2 = \text{term}'_2 and \text{block}_2 = \text{block}'_2 and \Gamma'_2 = \Gamma_2.

The result follows trivially from the \ell\text{-equivalence of lenf}_1 and lenf_2.

\textbullet{} \quad (\text{com}_2; \text{com}_3, \text{mem, } \tau) \xrightarrow{s_{\sigma}} (\text{com}'_2; \text{com}_3, \text{mem}', \tau')

Here we have (\text{com}_2, \text{mem, } \tau) \xrightarrow{s_{\sigma}} (\text{com}'_2, \text{mem}', \tau') and the result holds by the inductive hypothesis.

\textbullet{} \quad (\text{if } e \text{ then com}_4 \text{ else com}_5 \text{ fi, mem, } \tau) \xrightarrow{s_{\sigma}} (\text{com}_4; \text{join, mem, } \tau)

Here, \text{com}_1 \equiv \text{if } e \text{ then com}_4 \text{ else com}_5 \text{ fi and com}'_1 \equiv \text{com}_4; \text{join and mem}'_1 = \text{mem}_1 and \tau'_1 = \tau_1.

By inversion on the local monitor step, rule M-Branch must have been used, and we have \Gamma_1 = \Gamma'_1 and \text{lmdst}_1 = \text{lmdst}'_1 and \text{time}_1 = \text{time}'_1 and \text{term}_1 = \text{term}'_1 and \text{block}_1 = \text{block}'_1. Also \text{per}_1 = \text{per}_1, \text{block}_1 = \text{block}'_1, \text{time}_1 = \text{time}'_1 and \text{term}_1 = \text{term}'_1 and \text{block}_1 = \text{block}'_1.

We can construct

\[(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{s_{\sigma},e} (\text{com}'_2, \text{mem}'_2, \tau'_2)\]

\[(\ell_\text{sb}, \text{time}_\text{sb}, \text{term}_\text{sb}, \text{block}_\text{sb}, \Gamma'_\text{sb}) = \text{SB}(\text{if } e \text{ then com}_4 \text{ else com}_5 \text{ fi, } \Gamma_1, \text{lmdst}_1, \text{per}_1, \text{time}_1, \text{term}_1, \text{block}_1).\]

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and
\[
\langle I_2, \text{lmdst}_2, \overline{pc}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle \rightarrow^{e, a}_{\text{perm}} \langle I'_2, \text{lmdst}_2, \overline{pc}'_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2' \rangle
\]
where \( \text{com}_2 \equiv e \) then \( \text{com}_a \) else \( \text{com}_b \) fi and either \( \text{com}'_2 \equiv \text{com}_a; \) join or \( \text{com}'_2 \equiv \text{com}_b; \) join (depending on the result of evaluation \( e \)) and \( \text{mem}'_2 = \text{mem}_2 \) and \( \tau'_2 = \tau_2 \) and \( I_2 = I'_2 \) and \( \text{lmdst}_2 = \text{lmdst}_2' \) and \( \text{time}_2 = \text{time}_2' \) and \( \text{term}_2 = \text{term}_2' \) and \( \text{block}_2 = \text{block}_2' \). Also \( \overline{pc}_2 = \overline{pc}_2' \) and \( \overline{br}_2 = \overline{br}_2' \) (\( \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb} \)) since
\[
\text{SB}(e \text{ then } \text{com}_a \text{ else } \text{com}_b, \text{fi}, I_1, \text{lmdst}_1, \overline{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1) =
\text{SB}(e \text{ then } \text{com}_a \text{ else } \text{com}_b, \text{fi}, I_2, \text{lmdst}_2, \overline{pc}_2, \text{time}_2, \text{term}_2, \text{block}_2).\n\]

We consider two (mutually exclusive and exhaustive) cases, based on whether \( \ell_{sb} \subseteq \ell \).
- \( \ell_{sb} \subseteq \ell \). Since \( \ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb} \) is a conservative approximation to if \( e \) then \( \text{com}_a \) else \( \text{com}_b \) fi, etc., (Definition 2 and Lemma 14, we have that \( I_1(e) \cup \text{time}_1 \cup (\overline{\text{pc}}_1) = I_2(e) \cup \text{time}_2 \subseteq \ell_{sb} \subseteq \ell \), so by Lemma 11 we have \( e, \text{mem}_1 \downarrow v \) and \( e, \text{mem}_2 \downarrow v \) for some \( v \). That is, both executions take the same branch of the conditional, i.e., \( \text{com}_1 = \text{com}'_2 = \text{com}_a; \) join.

The result follows from this fact plus the \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).
- \( \ell_{sb} \not\subseteq \ell \). Here, we may not have \( \text{com}'_2 = \text{com}'_2 \), since the two executions may differ on which branch of the conditional they will take. However, since the pc of the configuration is now “high” (i.e., \( (\overline{\text{pc}}_1) \not\subseteq \ell \) and \( (\overline{\text{pc}}_2) \not\subseteq \ell \). The result follows easily from this and the \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \), including Condition 8.

• (if \( e \) then \( \text{com}_a \) else \( \text{com}_b \) fi, mem, \( \tau \), \( b(e, \text{com}_a, \text{com}_b), e \) (\( \text{com}_b; \) join, mem, \( \tau \))
This case is symmetric to the case above.

• (\( \text{join}, \text{mem}, \tau \) \text{ join} \( e \) \( \text{stop}, \text{mem}, \tau \)) Here, \( \text{com}_1 \equiv \text{join} \) and \( \text{com}'_1 \equiv \text{stop} \) and \( \text{mem}'_1 = \text{mem}_1 \) and \( \tau'_1 = \tau_1 \).
By inversion on the local monitor step, rule M-Join must have been used, and we have \( \text{lmdst}'_1 = \text{lmdst}_1 \) and \( \overline{pc}'_1 = \overline{pc}_1' \ell_{sb} \) and \( \overline{br}_1 = \overline{br}_1'(\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \) and time' \( \equiv \text{time}_1 \cup \text{time}_1 \ell_{sb} \) \text{ and } \text{term}'_1 = \text{term}_1 \ell_{sb} \) \text{ and } \text{block}'_1 = \text{block}_1 \ell_{sb} \text{ and and }
\[ I_1 = \lambda x. \begin{cases} I_1(x) \cup I_{sb}(x) & \text{if } x \in \text{pre}(I_1) \cup \text{pre}(I_{sb}) \\ I_1(x) & \text{if } x \not\in \text{pre}(I_1) \cup \text{pre}(I_{sb}) \\ \text{undef} & \text{otherwise} \end{cases} \]
Note that for all variables \( x \) we have \( I_1(x) \subseteq I_1'(x) \).

We can construct
\[
\text{com}_2, \text{mem}_2, \tau_2 \rightarrow^{a, e}_{\sigma_2} \text{com}'_2, \text{mem}'_2, \tau'_2 \]
and
\[
\langle I_2, \text{lmdst}_2, \overline{pc}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle \rightarrow^{e, a}_{\text{perm}} \langle I'_2, \text{lmdst}_2, \overline{pc}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2' \rangle
\]
where \( \text{com}_2 = \text{com}_1 \equiv \text{join} \) and \( \text{com}'_2 = \text{com}_1 \equiv \text{stop} \) and \( \text{mem}'_2 = \text{mem}_2 \) and \( \tau'_2 = \tau_2 \) and \( \text{lmdst}_2 = \text{lmdst}_2' \) and \( \overline{pc}_2 = \overline{pc}_2' \ell_{sb} \) and \( \overline{br}_2 = \overline{br}_2' \) and \( \overline{br}_2 = \overline{br}_2'(\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \overline{br}_2'(\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) = \overline{br}_2'(\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \) and time' \( \equiv \text{time}_2 \text{ time}_2 \ell_{sb} \) \text{ and } \text{term}'_2 = \text{term}_2 \ell_{sb} \text{ and } \text{block}'_2 = \text{block}_2 \ell_{sb} \text{ and and and and }
\[ I_2 = \lambda x. \begin{cases} I_2(x) \cup I_{sb}(x) & \text{if } x \in \text{pre}(I_2) \cup \text{pre}(I_{sb}) \\ I_2(x) & \text{if } x \not\in \text{pre}(I_2) \cup \text{pre}(I_{sb}) \\ \text{undef} & \text{otherwise} \end{cases} \]
All of the conditions for \( \ell \)-equivalence of \( \text{lcnf}_1' \) and \( \text{lcnf}_2' \) follow easily.

• (\( \text{while } e \text{ do com od}, \text{mem}, \tau \)) \text{ enter} e, (\text{com od}, \text{mem}, \tau) \text{ Here, } \text{com}_1 \equiv \text{while } e \text{ do od and com}'_1 \equiv \text{more } e \text{ do com od and mem}'_1 = \text{mem}_1 \) and \( \tau'_1 = \tau_1 \).
By inversion on the local monitor step, rule M-Enter must have been used, and we have \( I_1 = I'_1 \) and \( \text{lmdst}_1 = \text{lmdst}_1' \) and time' \( \equiv \text{time}_1 \) and \text{term}'_1 = \text{term}_1 \) \text{ and } \text{block}'_1 = \text{block}_1'. Also \( \overline{pc}_1 = \overline{pc}_1' \ell_{sb} \) and \( \overline{br}_1 = \overline{br}_1'(\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \) where
\[
\text{SB}(e \text{ do com od, } I_1, \text{lmdst}_1, \overline{pc}_1, \text{time}_1, \text{term}_1, \text{block}_1) =
\text{SB}(e \text{ do com od, } I_2, \text{lmdst}_2, \overline{pc}_2, \text{time}_2, \text{term}_2, \text{block}_2).
\]
We can construct
\[
\text{com}_2, \text{mem}_2, \tau_2 \rightarrow^{a, e}_{\sigma_2} \text{com}'_2, \text{mem}'_2, \tau'_2 \]
and
\[
\langle I_2, \text{lmdst}_2, \overline{pc}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle \rightarrow^{e, a}_{\text{perm}} \langle I'_2, \text{lmdst}_2, \overline{pc}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2' \rangle
\]
where \( \text{com}_2 \equiv \text{com}_1 \equiv \text{while} \ e \ \text{do} \ \text{com}_2 \ \text{od} \) and \( \text{com}'_2 \equiv \text{com}'_1 \equiv \text{more} \ e \ \text{do} \ \text{com}_2 \ \text{od} \) and \( \text{mem}'_2 = \text{mem}_2 \) and \( \tau'_2 = \tau_2 \) and \( \Gamma_2 = \Gamma'_2 \) and \( \text{lmdst}_2 = \text{lmdst}'_2 \) and \( \text{time}_2 = \text{time}'_2 \) and \( \text{term}_2 = \text{term}'_2 \) and \( \text{block}_2 = \text{block}'_2 \). Also \( \overline{\text{pc}}_2 = \overline{\text{pc}}'_2 \cdot \text{sb} \) and 

\[
\overline{\text{br}}_2 = \overline{\text{br}}'_2 (\text{time}_{\text{ab}}, \text{term}_{\text{ab}}, \text{block}_{\text{ab}}, \Gamma_{\text{sb}})
\]

\[
\text{SB}(\text{while} \ e \ \text{do} \ \text{com}_2 \ \text{od}, \Gamma_1, \text{lmdst}_1, \overline{\text{pc}}_1, \text{time}_1, \text{term}_1, \text{block}_1) = 
\]

\[
\text{SB}(\text{while} \ e \ \text{do} \ \text{com}_2 \ \text{od}, \Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \text{time}_2, \text{term}_2, \text{block}_2).
\]

The result follows easily from the \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).

- \( \text{(more} \ e \ \text{do} \ \text{com}_2 \ \text{od}, \ \text{mem}, \tau) = \text{more} \ (e, \text{com}) \) \( \xrightarrow{e} \) \( \text{mem} ; \text{com} \) \( \text{more} \ e \ \text{do} \ \text{com}_2 \ \text{od}, \ \text{mem}, \tau \)

Here, \( \text{com}_1 \equiv \text{more} \ e \ \text{do} \ \text{com}_2 \ \text{od} \) and \( \text{com}'_1 \equiv \text{mem} \) and \( \text{mem}'_1 \equiv \text{mem}_1 \) and \( \tau'_1 = \tau_1 \).

By inversion on the local monitor step, rule M-More must have been used, and we have \( \Gamma_1 = \Gamma'_1 \) and \( \text{lmdst}_1 = \text{lmdst}'_1 \) and 

\[
\overline{\text{pc}}_1 = \overline{\text{pc}}'_1 \quad \text{and} \quad \overline{\text{br}}_1 = \overline{\text{br}}'_1 \quad \text{and} \quad \text{time}_1 = \text{time}'_1 \quad \text{and} \quad \text{term}_1 = \text{term}'_1 \quad \text{and} \quad \text{block}_1 = \text{block}'_1.
\]

From Property [1], we have that for \( k = [\overline{\text{br}}_1] \), the tuple \( (\overline{\text{pc}}_1(k), \text{time}(k), \text{term}(k), \text{block}(k), \Gamma(k)) \) (where \( \overline{\text{br}}(k) = (\text{time}(k), \text{term}(k), \text{block}(k), \Gamma(k))) \) is a conservative approximation (Definition [2]) of the branching, timing, termination, and floating behavior for while \( e \ \text{do} \ \text{com}_2 \ \text{od} \) (and the local monitor state as at the time the while loop was entered).

Thus, by Definition [2], we have that \( \Gamma_1(e) \cup \text{time}_1 \subseteq \overline{\text{pc}}_1(k) \subseteq \ell \). So, from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \), we have that \( \Gamma_2(e) \cup \text{time}_2 \subseteq \overline{\text{pc}}_2(k) \subseteq \ell \). Thus by Lemma [1], we have \( e, \text{mem}_1 \upharpoonright v \) and \( e, \text{mem}_2 \upharpoonright v \) for some \( v \). That is, in both executions, the loop condition evaluates to true.

So we can construct

\[
(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{e} (\text{com}'_2, \text{mem}'_2, \tau'_2)
\]

and

\[
(\Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{\text{br}}_2, \text{time}_2, \text{term}_2, \text{block}_2) \xrightarrow{e} (\Gamma'_2, \text{lmdst}_2, \overline{\text{pc}}'_2, \overline{\text{br}}'_2, \text{time}_2, \text{term}_2, \text{block}_2)
\]

where \( \text{com}_2 \equiv \text{more} \ e \ \text{do} \ \text{com}_2 \ \text{od} \) and \( \text{com}'_2 \equiv \text{com}'_1 \equiv \text{mem} \) and \( \text{mem}'_2 = \text{mem}_2 \) and \( \tau'_2 = \tau_2 \) and \( \Gamma_2 = \Gamma'_2 \) and \( \text{lmdst}_2 = \text{lmdst}'_2 \) and \( \text{time}_2 = \text{time}'_2 \) and \( \text{term}_2 = \text{term}'_2 \) and \( \text{block}_2 = \text{block}'_2 \) and \( \overline{\text{pc}}_2 = \overline{\text{pc}}'_2 \cdot \text{sb} \) and 

\[
\overline{\text{br}}_2 = \overline{\text{br}}'_2 (\text{time}_{\text{ab}}, \text{term}_{\text{ab}}, \text{block}_{\text{ab}}, \Gamma_{\text{sb}})
\]

\[
\text{result follows easily from the } \ell \text{-equivalence of } \text{lcnf}_1 \text{ and } \text{lcnf}_2.
\]
• (input $ch$ to $x$, mem, $\tau$) $\leadsto_{\sigma}^{\text{input}(x, ch, v), e}$ (stop, mem$[x \mapsto v]$, $\tau$·inp$(ch, v)$) Here, $com_1 \equiv$ input $ch$ to $x$ and $com'_1 \equiv$ stop and mem$'_1 = \text{mem}_1[x \mapsto v_1]$ and $\tau'_1 = \tau_1$·inp$(ch, v_1)$ where $v_1 = \tau_1(ch)$.

By inversion on the local monitor rules, there are two possible rules that may apply: M-Input1 and M-Input2, (which differ on whether $\text{lmdst} \triangleright \text{exclusiveread}(x)$ or $\text{lmdst} \triangleright \text{othershightread}(x)$). Regardless of which of the two monitor rules is used, we have $\text{lmdst} = \text{lmdst}'_1$ and $\overline{\text{pc}} = \overline{\text{pc}}'_1$ and $\overline{v} = \overline{v}'_1$ and $\text{time} = \text{time}'_1$ and $\text{term} = \text{term}'_1$ and block$'_1 = \text{block}'_1$ and $I'' \equiv \Gamma(x \mapsto \text{lmdst}', \ell'' \equiv \chi \sqcup \Gamma'(e) \sqcup \text{time}_1 \sqcup ((\sqcup \overline{\text{pc}}) \sqcup \text{term}_1 \sqcup \text{block}_1)$.

We can construct

$$(\text{com}_2, \text{mem}_2, \tau_2) \overset{\alpha, e}{\to}_{\sigma_2} (\text{com}'_2, \text{mem}'_2, \tau'_2)$$

and

$$(\Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{v}_2, \text{time}_2, \text{term}_2, \text{block}_2) \overset{\epsilon, \text{perm}}{\to} (\Gamma'_2, \text{lmdst}'_2, \overline{\text{pc}}'_2, \overline{v}'_2, \text{time}'_2, \text{term}_2, \text{block}_2')$$

where $\text{com}_2 \equiv$ input $ch$ to $x$ and $\text{com}'_2 \equiv$ stop and mem$'_2 = \text{mem}_2[x \mapsto v_2]$ and $\tau'_2 = \tau_2$·inp$(ch, v_2)$ where $v_2 = \sigma_2(ch)$. Also, we have $\text{lmdst} = \text{lmdst}'_2$ and $\overline{\text{pc}} = \overline{\text{pc}}'_2$ and $\overline{v} = \overline{v}'_2$ and $\text{time} = \text{time}'_2$ and $\text{term} = \text{term}'_2$ and block$'_2 = \text{block}'_2$ and $\Gamma''_2 = \Gamma_2(x \mapsto \text{lmdst}_1, \ell'')$ where $\ell'' = \chi \sqcup \Gamma'(e) \sqcup \text{time}_1 \sqcup ((\sqcup \overline{\text{pc}}) \sqcup \text{term}_1 \sqcup \text{block}_1)$.

All of the conditions for $\ell$-equivalence of lenf$'_1$ and lenf$'_2$ follow easily from $\ell$-equivalence of lenf$_1$ and lenf$_2$, except for conditions 1] and 5] and 6] are satisfied.

• If $\chi \subseteq \ell$ then $v_1 = v_2$ since $\sigma_1$ and $\sigma_2$ are $\ell$-equivalent strategies so $v_1 = \sigma_1(\tau_1, ch) = \sigma_2(\tau_2, ch) = v_2$. So conditions 1] and 5] and 6] are satisfied.

• (output $e$ to $ch$, mem, $\tau$) $\overset{\text{output}(ch, e), e}{\to}_{\sigma}$ (stop, mem, $\tau$-out$(ch, v)$)

Here, $com_1 \equiv$ output $e$ to $ch$ and $com'_1 \equiv$ stop and mem$'_1 = \text{mem}_1$ and $\tau'_1 = \tau_1$·out$(ch, v_1)$ where $e, \text{mem}_1 \nvdash v_1$.

By inversion on the local monitor step, we have $\Gamma_1 = \Gamma'_1$ and $\text{lmdst}_1 = \text{lmdst}'_1$ and $\overline{\text{pc}}_1 = \overline{\text{pc}}'_1$ and $\overline{v}_1 = \overline{v}'_1$ and $\text{time}_1 = \text{time}'_1$ and $\text{term}_1 = \text{term}'_1$ and $\text{block}_1 = \text{block}'_1$. We also know that $\text{lmdst} \triangleright \text{mayread}(e)$ and $\Gamma_1(e) \sqcup \text{time}_1 \sqcup ((\sqcup \overline{\text{pc}}) \sqcup \text{term}_1 \sqcup \text{block}_1 \subseteq \chi$).

We can construct

$$(\text{com}_2, \text{mem}_2, \tau_2) \overset{\alpha, e}{\to}_{\sigma_2} (\text{com}'_2, \text{mem}'_2, \tau'_2)$$

and

$$(\Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{v}_2, \text{time}_2, \text{term}_2, \text{block}_2) \overset{\epsilon, \text{perm}}{\to} (\Gamma'_2, \text{lmdst}'_2, \overline{\text{pc}}'_2, \overline{v}'_2, \text{time}'_2, \text{term}_2, \text{block}'_2)$$

where $\tau'_2 = \tau_2$·out$(ch, v_2)$ where $e, \text{mem}_2 \nvdash v_2$ and $\Gamma_2 = \Gamma'_2$ and $\text{lmdst}_2 = \text{lmdst}'_2$ and $\overline{\text{pc}}_2 = \overline{\text{pc}}'_2$ and $\overline{v}_2 = \overline{v}'_2$ and $\text{time}_2 = \text{time}'_2$ and $\text{term}_2 = \text{term}'_2$ and $\text{block}_2 = \text{block}'_2$. All of the conditions for $\ell$-equivalence of lenf$'_1$ and lenf$'_2$ follow easily from $\ell$-equivalence of lenf$_1$ and lenf$_2$, except for condition 1]. There are two (mutually exclusive and exhaustive) cases to consider.

• If $\chi \subseteq \ell$ then $v_1 = v_2$. Thus $\tau'_2 \downarrow \ell = \tau_2 \downarrow \ell$·out$(ch, v_2) \tau'_2 \downarrow \ell$, and condition 1] holds.

• (//[$\gamma$]// barrier, mem, $\tau$) $\overset{\text{sync}, \gamma}{\to}_{\sigma}$ (stop, mem, $\tau$)

This case is impossible, since rule (M-BARRIER-Local) is the only rule that could be used to allow the local monitor to take a step (due to the event sync), but this is not a synchronization event: sync $\neq \epsilon$.

• (stop, mem, $\tau$) $\overset{\tau}{\to}_{\sigma}$ (term, mem, $\tau$)

Here, $com_1 \equiv$ stop and $com'_1 \equiv$ term and mem$'_1 = \text{mem}_1$ and $\tau'_1 = \tau_1$.

By inversion on the local monitor step, rule M-Term must have been used, and we have $\Gamma_1 = \Gamma'_1$ and $\text{lmdst}_1 = \text{lmdst}'_1$ and $\overline{\text{pc}}_1 = \overline{\text{pc}}'_1 = \epsilon$ and $\overline{v}_1 = \overline{v}'_1 = \epsilon$ and $\text{time}_1 = \text{time}'_1$ and $\text{term}_1 = \text{term}'_1 = \bot$ and block$'_1 = \text{block}'_1 = \bot$.

We can construct

$$(\text{com}_2, \text{mem}_2, \tau_2) \overset{\alpha, e}{\to}_{\sigma_2} (\text{com}'_2, \text{mem}'_2, \tau'_2)$$

and

$$(\Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{v}_2, \text{time}_2, \text{term}_2, \text{block}_2) \overset{\epsilon, \text{perm}}{\to} (\Gamma'_2, \text{lmdst}'_2, \overline{\text{pc}}'_2, \overline{v}'_2, \text{time}'_2, \text{term}_2, \text{block}'_2)$$

where $\text{com}_2 \equiv$ stop and $\text{com}'_2 \equiv$ term and mem$'_2 = \text{mem}_2$ and $\tau'_2 = \tau_2$ and $\text{lmdst}_2 = \text{lmdst}'_2$ and $\overline{\text{pc}}_2 = \overline{\text{pc}}'_2 = \epsilon$ and $\overline{v}_2 = \overline{v}'_2 = \epsilon$ and $\text{time}_2 = \text{time}'_2$ and $\text{term}_2 = \text{term}'_2 = \bot$ and block$'_2 = \text{block}'_2 = \bot$ and $I'_2 = \Gamma_2$.

The result follows trivially from the $\ell$-equivalence of lenf$'_1$ and lenf$'_2$. $\blacksquare$
Theorem 8 (Thread High Step). Let $\sigma_1$ and $\sigma_2$ be $\ell$-equivalent strategies and let

$$\text{lcnf}_1 = \{[\text{com}_1, (I_1', \text{lndef}_1, p, r_1, \text{time}_1, \text{term}_1, \text{block}_1), \text{mdst}_1], \text{mem}_1, \tau_1)$$

and

$$\text{lcnf}_2 = \{[\text{com}_2, (I_2', \text{lndef}_2, p, r_2, \text{time}_2, \text{term}_2, \text{block}_2), \text{mdst}_2], \text{mem}_2, \tau_2)$$

be $\ell$-equivalent local configurations such that

$$\text{time}_1 \sqcup (\text{time}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\sqsubseteq \ell$$

and $\text{lcnf}_1 \rightarrow^c_{\ell, \sigma_1} \text{lcnf}'_1$, where

$$\text{lcnf}'_1 = \{[\text{com}'_1, (I_1', \text{lndef}_1, p, r_1, \text{time}'_1, \text{term}'_1, \text{block}'_1), \text{mdst}_1], \text{mem}'_1, \tau'_1)$$

and

$$\text{time}'_1 \sqcup (\text{time}'_1) \sqcup \text{term}'_1 \sqcup \text{block}'_1 \not\sqsubseteq \ell.$$ 

Then $\text{lcnf}'_1$ and $\text{lcnf}_2$ are $\ell$-equivalent.

Proof: Since $\text{lcnf}_1 \rightarrow^c_{\ell, \sigma_1} \text{lcnf}'_1$, we have

$$(\text{com}_1, \text{mem}_1, \tau_1) \rightarrow^c_{\ell, \sigma_1} (\text{com}'_1, \text{mem}'_1, \tau'_1)$$

and

$$(I_1', \text{lndef}_1, p, r_1, \text{time}_1, \text{term}_1, \text{block}_1) \rightarrow^c_{\ell, \text{perm}} (I_1', \text{lndef}_1, p, r_1, \text{time}'_1, \text{term}'_1, \text{block}'_1)$$

We proceed by induction on the judgment $(\text{com}_1, \text{mem}_1, \tau_1) \rightarrow^c_{\ell, \sigma_1} (\text{com}'_1, \text{mem}'_1, \tau'_1)$. 

- \((x := e, \text{mem}, \tau) \rightarrow^c_{\ell, \sigma} \text{(stop, mem}[x \rightarrow v], \tau)\) Here, $\text{com}_1 \equiv x := e$ and $\text{com}'_1 \equiv \text{stop}$ and $\text{mem}'_1 = \text{mem}_1[x \rightarrow v]$ and $\tau'_1 = \tau_1$ and $e, \text{mem} \downarrow v$. 

  By inversion on the local monitor rules, there are two possible rules that may apply: M-Assign1 and M-Assign2. In both of these cases, we have $\text{lndef}_1 = \text{lndef}_1$ and $p = p_1$ and $r_1 = r_1$ and and $\text{time}_1 = \text{time}_1$ and $\text{term}_1 = \text{term}_1$ and $\text{block}_1 = \text{block}_1$, and $I'_1 = I_1'(x \rightarrow \text{lndef}_1)$ where $\ell' = \ell_1'(\text{time}_1 \sqcup \text{time}_1 \sqcup \text{term}_1 \sqcup \text{block}_1)$. Note that $\ell' \not\sqsubseteq \ell$, since by assumption we have $\text{time}_1 \sqcup (\text{time}_1) \sqcup \text{term}_1 \sqcup \text{block}_1 \not\sqsubseteq \ell$.

  All of the conditions for $\ell$-equivalence of $\text{lcnf}_1'$ and $\text{lcnf}_2$ follow trivially from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$, except for conditions [5] and [6].

  We consider three (mutually exclusive and exhaustive) cases, based on variable $x$.

  - $x \not\in \text{pre}(I'_1)$. Then we have $I'_1 = I_1'$, and conditions [5] and [6] follow trivially from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$.

  - $x \in \text{pre}(I_1') \land \text{lndef}_1 \triangleright \text{exclusiveWrite}(x) \land x \in \text{FloatVar}$ then $I'_1(x) = \ell'$. Since $x$ is a floating variable, condition [6] is satisfied for $x$, and trivially satisfied for all other variables from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$. Since $\ell' \not\sqsubseteq \ell$, $I'_1(x) \not\sqsubseteq \ell$, condition [5] is satisfied for $x$, and trivially satisfied for all other variables from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$.

  - $x \in \text{pre}(I_1') \land \text{lndef}_1 \triangleright \text{othermsightWrite}(x) \land x \in \text{FloatVar}$ then $I'_1(x) = \ell' \sqcup \mathcal{L}(x)$. Since $x$ is a floating variable, condition [6] is satisfied for $x$, and trivially satisfied for all other variables from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$. Since $\ell' \not\sqsubseteq \ell$, $I'_1(x) \not\sqsubseteq \ell$, condition [5] is satisfied for $x$, and trivially satisfied for all other variables from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$.

- \((\text{skip}, \text{mem}, \tau) \rightarrow^c_{\ell, \sigma} \text{(stop, mem}, \tau)\) Here, $\text{com}_1 \equiv \text{skip}$ and $\text{com}'_1 \equiv \text{stop}$ and $\text{mem}'_1 = \text{mem}_1$ and $\tau'_1 = \tau_1$.

  By inversion on the local monitor step, we have $I'_1 = I_1'$ and $\text{lndef}_1 = \text{lndef}_1$ and $p = p_1$ and $r_1 = r_1$ and $\text{time}_1 = \text{time}_1$ and $\text{term}_1 = \text{term}_1$ and $\text{block}_1 = \text{block}_1$.

  The result follows trivially from the $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$.

- \((\text{stop}, \text{com}, \text{mem}, \tau) \rightarrow^c_{\ell, \sigma} \text{(com, mem, \tau)}\) Here, $\text{com}_1 \equiv \text{stop}$ and $\text{com}'_1 \equiv \text{com}_1$ and $\tau'_1 = \tau_1$.

  By inversion on the local monitor step, we have $I'_1 = I_1'$ and $\text{lndef}_1 = \text{lndef}_1$ and $p = p_1$ and $r_1 = r_1$ and $\text{time}_1 = \text{time}_1$ and $\text{term}_1 = \text{term}_1$ and $\text{block}_1 = \text{block}_1$.

  The result follows trivially from the $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$.

- \((\text{com}_a, \text{com}_b, \text{mem}, \tau) \rightarrow^c_{\ell, \sigma} (\text{com}_a, \text{com}_b, \text{mem}', \tau')\) Here we have $\text{com}_a \equiv \text{com}_a$ and $\text{com}_b \equiv \text{com}_b$ and $\text{mem}' \equiv \text{mem}_1$ and $\tau'_1 = \tau_1$.

- \((\text{if } e \text{ then } \text{com}_a \text{ else } \text{com}_b, \text{mem}, \tau) \rightarrow^c_{\ell, \sigma} (\text{com}_a, \text{join}, \text{mem}, \tau)\) Here, $\text{com}_1 \equiv \text{if } e \text{ then } \text{com}_a \text{ else } \text{com}_b$ and $\text{com}'_1 \equiv \text{com}_a \text{; join and } \text{mem}' \equiv \text{mem}_1$ and $\tau'_1 = \tau_1$. 

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By inversion on the local monitor step, rule M-Branch must have been used, and we have $\Gamma_1 = \Gamma'_1$ and $lmdst_1 = lmdst'_1$ and $time_1 = time'_1$ and $term_1 = term'_1$ and $block_1 = block'_1$. Also $\overline{pc}_1 = \overline{pc}_1', lmdst_1 = lmdst'_1$ and $\overline{br}_1 = \overline{br}_1' \cdot time_1 \cdot mem \cdot block_1$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.

- $s = \Gamma_1 \equiv \Gamma_1'$ if $e$ then $com_a$ else $com_b$ fi, $\Gamma_1$, $lmdst_1$, $\overline{pc}_1$, $time_1$, $term_1$, $block_1$.

This case is symmetric to the case above.

- $(\ell, time_1, term_1, block_1, \Gamma_1) = SB(\ell, time_1, term_1, block_1, \Gamma_1)$

Note that for all variables $x$ we have $\Gamma_1(x) \subseteq \Gamma_1'(x)$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.

- $(\ell, time_1, term_1, block_1, \Gamma_1) = SB(\ell, time_1, term_1, block_1, \Gamma_1)$

Note that for all variables $x$ we have $\Gamma_1(x) \subseteq \Gamma_1'(x)$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.

- $(\ell, time_1, term_1, block_1, \Gamma_1) = SB(\ell, time_1, term_1, block_1, \Gamma_1)$

Note that for all variables $x$ we have $\Gamma_1(x) \subseteq \Gamma_1'(x)$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.

- $(\ell, time_1, term_1, block_1, \Gamma_1) = SB(\ell, time_1, term_1, block_1, \Gamma_1)$

Note that for all variables $x$ we have $\Gamma_1(x) \subseteq \Gamma_1'(x)$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.

- $(\ell, time_1, term_1, block_1, \Gamma_1) = SB(\ell, time_1, term_1, block_1, \Gamma_1)$

Note that for all variables $x$ we have $\Gamma_1(x) \subseteq \Gamma_1'(x)$.

All of the conditions for $\ell$-equivalence of $lcnf_1'$ and $lcnf_2$ follow trivially from $\ell$-equivalence of $lcnf_1$ and $lcnf_2$.
\[ x \in \text{pre}(\Gamma)_1 \land \text{lmdst}_1 \triangleright \text{exclusivewrite}(x) \land x \in \text{FloatVar} \text{ then } \Gamma'_1(x) = \ell'. \] Since \( x \) is a floating variable, condition 6 is satisfied for \( x \), and trivially satisfied for all other variables from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \). Since \( \ell' \not\sqsubseteq \ell, \Gamma'_1(x) \not\sqsubseteq \ell \), so condition 5 is satisfied for \( x \), and trivially satisfied for all other variables from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).

- \[ x \in \text{pre}(\Gamma)_1 \land \text{lmdst}_1 \triangleright \text{othersmighthashwrite}(x) \land x \in \text{FloatVar} \text{ then } \Gamma'_1(x) = \ell' \sqcup \text{lmdst}(x). \] Since \( x \) is a floating variable, condition 6 is satisfied for \( x \), and trivially satisfied for all other variables from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \). Since \( \ell' \not\sqsubseteq \ell, \Gamma'_1(x) \not\sqsubseteq \ell \), so condition 5 is satisfied for \( x \), and trivially satisfied for all other variables from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).

- \( \text{output} e \text{ to } ch, \text{mem}, \tau \)
  \[ \begin{array}{c}
  \text{output}(e, ch, v), \ell \\
  \text{stop}(\text{mem}, \tau, \text{out}(ch, v))
  \end{array} \]
  Here, \( \text{com}_1 \equiv \text{output} e \text{ to } ch \) and \( \text{com}_1' \equiv \text{stop and mem}' = \text{mem}_1 \) and \( \tau'_1 = \tau_1 \text{ out}(ch, v) \) where \( e, \text{mem}_1 \not\sqsubseteq v \).

  By inversion on the local monitor step, we have \( \Gamma_1 = \Gamma_1' \) and \( \text{lmdst}_1 = \text{lmdst}'_1 \) and \( \text{pc}_1 = \text{pc}'_1 \) and \( \text{br}_1 = \overline{\text{br}}_1 \) and \( \text{time}_1 = \text{time}'_1 \) and \( \text{term}_1 = \text{term}'_1 \) and \( \text{block}_1 = \text{block}'_1 \).

  We also have (from the premises of (M-Output)) that \( \text{time} \sqcup (\overline{\text{pc}} \sqcup \text{term} \sqcup \text{block} \sqsubseteq ch \) and thus \( ch \not\sqsubseteq \ell \) and so \( \tau_1 \downarrow \ell = \tau'_1 \downarrow \ell \) and all other conditions for \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \) follow trivially from \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).

- \( \text{//}\gamma// \text{ barrier}, \text{mem}, \tau \)
  \[ \begin{array}{c}
  \text{sync,} \gamma \rightarrow \sigma \\
  \text{stop}(\text{mem}, \tau)
  \end{array} \]
  This case is impossible, since rule (M-Barrier-Local) is the only rule that could be used to allow the local monitor to take a step (due to the event sync), but this is not a synchronization event: \( \text{sync} \neq \epsilon \).

- \( \text{stop, } \text{mem}, \tau \)
  \[ \begin{array}{c}
  \text{term,} \text{mem,} \tau
  \end{array} \]
  Here, \( \text{com}_1 \equiv \text{stop and com}'_1 \equiv \text{term and mem}' = \text{mem}_1 \) and \( \tau'_1 = \tau_1 \).

  By inversion on the local monitor step, we have \( \Gamma_1 = \Gamma_1' \) and \( \text{lmdst}_1 = \text{lmdst}'_1 \) and \( \text{pc}_1 = \text{pc}'_1 \) and \( \text{br}_1 = \overline{\text{br}}_1 \) and \( \text{time}_1 = \text{time}'_1 \) and \( \text{term}_1 = \text{term}'_1 \) and \( \text{block}_1 = \text{block}'_1 \).

  The result follows trivially from the \( \ell \)-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \).

Note that by the properties of local monitor steps (Property 3 condition 5), we have that the time inclusive PC (\( \text{time} \sqcup (\overline{\text{pc}}) \sqcup \text{term} \sqcup \text{block} \)) increases monotonically except at synchronization steps. This means that given a sequence of local configuration steps (i.e., the execution of a thread), once we apply the high-step theorem (Theorem 8) we cannot apply the low-step theorem (Theorem 7) until the thread reaches a barrier.

**Theorem 9 (Thread barrier step).** Let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies and let

\[
\text{lcnf}_1 = \langle \langle \text{com}_1, (\Gamma_1, \text{lmdst}_1, \text{pc}_1, \overline{\text{br}}_1, \text{time}_1, \text{term}_1, \text{block}_1), \text{lmdst}_1, \text{mem}_1, \tau_1 \rangle
\]

and

\[
\text{lcnf}_2 = \langle \langle \text{com}_2, (\Gamma_2, \text{lmdst}_2, \overline{\text{pc}}_2, \overline{\text{br}}_2, \text{time}_2, \text{term}_2, \text{block}_2), \text{lmdst}_2, \text{mem}_2, \tau_2 \rangle
\]

be \( \ell \)-equivalent local configurations such that \( \text{lcnf}_1 \overset{\text{sync}, \gamma}{\longrightarrow}_{\sigma_1} \text{lcnf}'_1 \) and \( \text{lcnf}_2 \) is at a barrier (i.e., the next command to reduce is a barrier command), and moreover, the following two conditions hold.

1) If \( \overline{\text{pc}}_1 \sqcup \text{term}_1 \sqcup \text{block}_1 \sqsubseteq \ell \) or \( \overline{\text{pc}}_2 \sqcup \text{term}_2 \sqcup \text{block}_2 \sqsubseteq \ell \) then \( \text{time}_1 = \text{time}_2 \) and \( \text{term}_1 = \text{term}_2 \) and \( \text{block}_1 = \text{block}_2 \) and \( \overline{\text{pc}}_1 = \overline{\text{pc}}_2 \) and \( \overline{\text{br}}_1 = \overline{\text{br}}_2 \) and \( \text{com}_1 = \text{com}_2 \) and for all \( x, \Gamma_1(x) = \Gamma_2(x) \).

2) For all \( j \in 1..\max(\|\text{pc}_1\|, \|\text{pc}_2\|) \), if

\[
\overline{\text{pc}}_1(0) \sqcup ... \sqcup \overline{\text{pc}}_1(j - 1) \sqcup \text{term}_1 \sqcup \text{block}_1 \sqsubseteq \ell
\]

or

\[
\overline{\text{pc}}_2(0) \sqcup ... \sqcup \overline{\text{pc}}_2(j - 1) \sqcup \text{term}_2 \sqcup \text{block}_2 \sqsubseteq \ell
\]

then all of the following holds:

- \( \|\overline{\text{pc}}_1\| \geq j \).
- \( \|\overline{\text{pc}}_2\| \geq j \).
- For all \( k \in 0..j \) we have \( \overline{\text{pc}}_1(k) = \overline{\text{pc}}_2(k) \) and \( \overline{\text{br}}_1(k) = \overline{\text{br}}_2(k) \).
- There exists \( \text{com}_{\text{cnt}} \in \text{Continuation}_1 \) such that:
  - Either \( \text{com}_1 = \text{com}_{\text{cnt}} \) or \( \text{com}_1 = \text{com}_1' \); \( \text{com}_{\text{cnt}} \) for some \( \text{com}_1' \); and
  - Either \( \text{com}_2 = \text{com}_{\text{cnt}} \) or \( \text{com}_2 = \text{com}_2' \); \( \text{com}_{\text{cnt}} \) for some \( \text{com}_2' \).

Then

\[
\text{lcnf}_2 \overset{\text{sync}, \gamma}{\longrightarrow}_{\sigma_2} \text{lcnf}'_2
\]

and \( \text{lcnf}'_1 \) and \( \text{lcnf}'_2 \) are \( \ell \)-equivalent.

**Proof:**
Since $\text{lcnf}_1 \xrightarrow{\text{sync}^\gamma_\tau_\delta} \text{lcnf}'_1$, the monitor rule (M-Barrier-Local) must have been used, so we have $(\bigcup \text{mem}) \cup \text{term}_1 \cup \text{block}_1 = \bot$ and $\text{lmst}'_1 = \text{update}(\text{lmst}_1, \delta)$ and $\text{pre}(\Gamma_1) = \{ x \mid x \in \text{FloatVar} \land (\text{lmst}'_1 \triangleright \text{exclusiveRead}(x) \lor \text{lmst}'_1 \triangleright \text{exclusiveWrite}(x)) \}$ and $(\text{lmst}_1 \triangleright \text{exclusiveRead}(x) \land \text{lmst}_1 \triangleright \text{exclusiveWrite}(x)) \implies \Gamma'_1(x) \subseteq \mathcal{L}(x)$ and $\text{lmst}'_1 \triangleright \text{exclusiveWrite}(x) \implies \Gamma'_1(x) = \Gamma'_1(x)$ and $(\text{lmst}_1 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_1 \triangleright \text{exclusiveRead}(x), \text{othersmightwrite}(x)) \implies \Gamma'_1(x) = \Gamma'_1(x)$ and $(\text{lmst}_1 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_1 \triangleright \text{exclusiveRead}(x), \text{othersmightwrite}(x)) \implies \Gamma'_1(x) = \Gamma'_1(x) \cup \mathcal{L}(x)$.

Thus, we have $\text{time}_1 = \text{time}_2$ and $\text{term}_1 = \text{term}_2$ and $\text{block}_1 = \text{block}_2$ and $\text{mem} = \text{mem}_2$ and $\overline{br}_1 = \overline{br}_2$ and $\overline{com}_1 = \overline{com}_2$ and for all $x$, $\Gamma'_1(x) = \Gamma'_2(x)$.

We can therefore construct

$$(\text{com}_2, \text{mem}_2, \tau_2) \xrightarrow{\text{sync}_\tau} (\text{com}'_2, \text{mem}'_2, \tau'_2)$$

and

$$(\Gamma_2, \text{lmst}_2, \text{pre}_2, \overline{br}_2, \text{time}_2, \text{term}_2, \text{block}_2) \xrightarrow{\text{perm}_\text{term}_2} (\Gamma'_2, \text{lmst}'_2, \text{pre}'_2, \overline{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2)$$

where $\text{com}_1 \equiv \text{com}_2$ and $\text{com}'_1 \equiv \text{com}'_2$ and $\text{mem}_1 = \text{mem}_2$ and $\tau_1 = \tau_2$ and $\text{pre}_2 = \text{pre}'_2$ and $\overline{br}_2 = \overline{br}'_2$ and $\text{time}_2 = \text{time}'_2$ and $\text{term}_2 = \text{term}'_2 = \bot$ and $\text{block}_2 = \text{block}'_2 = \bot$ and $\text{lmst}_2 = \text{update}(\text{lmst}_2, \delta)$ and $\Gamma'_2 = \Gamma_1'$.

By setting $\Gamma'_2 = \Gamma'_1$, we ensure that the preconditions for (M-Barrier-Local) are satisfied, namely: $\text{pre}(\Gamma'_2) = \{ x \mid x \in \text{FloatVar} \land (\text{lmst}'_2 \triangleright \text{exclusiveRead}(x) \lor \text{lmst}'_2 \triangleright \text{exclusiveWrite}(x)) \}$ and $(\text{lmst}_2 \triangleright \text{exclusiveRead}(x) \land \text{lmst}_2 \triangleright \text{exclusiveWrite}(x)) \implies \Gamma'_2(x) \subseteq \mathcal{L}(x)$ and $\text{lmst}'_2 \triangleright \text{exclusiveWrite}(x) \implies \Gamma'_2(x) = \Gamma'_2(x)$ and $(\text{lmst}_2 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_2 \triangleright \text{exclusiveRead}(x)) \implies \Gamma'_2(x) = \Gamma'_2(x)$ and $(\text{lmst}_2 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_2 \triangleright \text{exclusiveRead}(x)) \implies \Gamma'_2(x) = \Gamma'_2(x) \cup \mathcal{L}(x)$.

We consider each condition of $\ell$-equivalence of local configurations (Definition 3) to see that they all hold.

- **Condition 1** requires that for all variables $x$, if $x \in \text{pre}(\Gamma'_1)$ and $\text{lmst}'_1 \triangleright \text{mayRead}(x)$ and $\Gamma'_1(x) \cup \text{time}'_1 \cup (\bigcup \text{mem}'_1) \subseteq \ell$ and $\Gamma'_2(x) \cup \text{time}'_2 \cup (\bigcup \text{mem}'_2) \subseteq \ell$ then $\text{mem}_1(x) = \text{mem}_2(x)$ and $\Gamma'_1(x) = \Gamma'_2(x)$.

- **Condition 2** requires that for all variables $x$, if $x \in \text{pre}(\Gamma'_1)$ and $\text{lmst}'_1 \triangleright \text{mayRead}(x)$ and $\Gamma'_1(x) \cup \text{time}'_1 \cup (\bigcup \text{mem}'_1) \subseteq \ell$ and $\text{lmst}_1 \triangleright \text{ExclusiveWrite}(x) \implies \Gamma'_2(x) \subseteq \mathcal{L}(x)$ and $\text{lmst}'_2 \triangleright \text{exclusiveWrite}(x) \implies \Gamma'_2(x) = \Gamma'_2(x)$ and $(\text{lmst}_2 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_2 \triangleright \text{exclusiveRead}(x)) \implies \Gamma'_2(x) = \Gamma'_2(x) \cup \mathcal{L}(x)$.

- **Condition 3** requires that for all variables $x$, if $x \notin \text{pre}(\Gamma_1)$ and $\text{lmst}_1 \triangleright \text{mayRead}(x)$ and $\mathcal{L}(x) \cup \text{time}_1 \cup (\bigcup \text{mem}_1) \subseteq \ell$ and $\mathcal{L}(x) \cup \text{time}_2 \cup (\bigcup \text{mem}_2) \subseteq \ell$ then $\text{mem}_1(x) = \text{mem}_2(x)$.

- **Condition 4** requires that for all variables $x$, if $x \notin \text{pre}(\Gamma_1)$ and $\text{lmst}_1 \triangleright \text{mayRead}(x)$ and $\mathcal{L}(x) \cup \text{time}_1 \cup (\bigcup \text{mem}_1) \subseteq \ell$ and $\text{lmst}_1 \triangleright \text{ExclusiveWrite}(x) \implies \Gamma'_1(x) \subseteq \mathcal{L}(x)$ and $\text{lmst}'_1 \triangleright \text{exclusiveWrite}(x) \implies \Gamma'_1(x) = \Gamma'_1(x)$ and $(\text{lmst}_1 \triangleright \text{exclusiveWrite}(x) \land \text{lmst}'_1 \triangleright \text{exclusiveRead}(x), \text{othersmightwrite}(x)) \implies \Gamma'_1(x) = \Gamma'_1(x) \cup \mathcal{L}(x)$.

- **Condition 5** requires that for all variables $x$, if $x \notin \text{pre}(\Gamma_1)$ and $\text{lmst}_1 \triangleright \text{mayRead}(x)$ and $\mathcal{L}(x) \cup \text{time}_1 \cup (\bigcup \text{mem}_1) \subseteq \ell$ and $\mathcal{L}(x) \cup \text{time}_2 \cup (\bigcup \text{mem}_2) \subseteq \ell$ then $\text{mem}_1(x) = \text{mem}_2(x)$.

- **Condition 6** requires that for all variables $x$, if $x \notin \text{pre}(\Gamma_1)$ and $\text{lmst}_1 \triangleright \text{mayRead}(x)$ and $\mathcal{L}(x) \cup \text{time}_1 \cup (\bigcup \text{mem}_1) \subseteq \ell$ and $\mathcal{L}(x) \cup \text{time}_2 \cup (\bigcup \text{mem}_2) \subseteq \ell$ then $\text{mem}_1(x) = \text{mem}_2(x)$.

- **Condition 7** requires that if $\text{time}_1 \cup (\bigcup \text{mem}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell$

  or $\text{time}_2 \cup (\bigcup \text{mem}_2) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell$

  then $\text{time}_1 = \text{time}_2$ and $\text{term}_1 = \text{term}_2$ and $\text{block}_1 = \text{block}_2$ and $\text{mem}_1 = \text{mem}_2$ and $\overline{br}_1 = \overline{br}_2$ and $\text{com}_1 = \text{com}_2$ and for all $x$, $\Gamma'_1(x) = \Gamma'_2(x)$.

This follows immediately from $\ell$-equivalence of $\text{lcnf}_1$ and $\text{lcnf}_2$ and since $\Gamma'_1 = \Gamma'_2$. 

- **Condition 8** follows immediately by assumption.
We define a useful lemma that says that if we have two ℓ-equivalent local configurations, and one of them takes a step, and modifies variable x, which it thinks is level ℓ or below, and the other local configuration takes zero or one steps to a ℓ-equivalent configuration, then the value of x will be the same in the two final configurations.

**Lemma 2.** Let σ₁ and σ₂ be ℓ-equivalent strategies and let

\[ \text{lcnf}_1 = \langle \langle \text{com}_1, \langle \text{lndst}_1, \text{term}_1, \text{block}_1 \rangle, \text{mem}_1, \tau_1 \rangle \]

and

\[ \text{lcnf}_2 = \langle \langle \text{com}_2, \langle \text{lndst}_2, \text{term}_2, \text{block}_2 \rangle, \text{mem}_2, \tau_2 \rangle \]

be ℓ-equivalent local configurations such that \( \text{lcnf}_1 \xrightarrow{e,e,e_{\sigma_1}} \text{lcnf}'_1 \) where

\[ \text{lcnf}'_1 = \langle \langle \text{com}_1', \langle \text{lndst}_1, \text{term}_1', \text{block}_1' \rangle, \text{mem}_1, \tau'_1 \rangle \]

and either

\[ \text{lcnf}_2 = \text{lcnf}'_1 \]

or

\[ \text{lcnf}_2 \xrightarrow{e,e,e_{\sigma_2}} \text{lcnf}'_2 \]

and

\[ \text{lcnf}'_2 = \langle \langle \text{com}_2', \langle \text{lndst}_2, \text{term}_2', \text{block}_2' \rangle, \text{mem}_2', \tau'_2 \rangle \]

and \( \text{lcnf}'_1 \) and \( \text{lcnf}'_2 \) are ℓ-equivalent (That is, \( \text{lcnf}_2 \) takes zero or one steps to reach local configuration \( \text{lcnf}'_2 \) that is ℓ-equivalent to \( \text{lcnf}'_1 \).)

For any variable x, if \( \text{mem}_1(x) \neq \text{mem}_1'(x) \) and \( \Gamma_1(x) \subseteq \ell \) then

\[ \text{lcnf}_2 \xrightarrow{e,e,e_{\sigma_2}} \text{lcnf}'_2 \]

and \( \text{mem}_1'(x) = \text{mem}_2'(x) \).

**Proof:** Let x be a variable such that \( \text{mem}_1(x) \neq \text{mem}_1'(x) \) and \( \Gamma_1(x) \subseteq \ell \). By Property 2, we have

\[ \text{time}_1 \cup (\cup \text{pre}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \Gamma_1'(x) \subseteq \ell \]

By ℓ-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \) (Definition 3), we have

\[ \text{time}_2 \cup (\cup \text{pre}_2) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell \]

By inspection of the operational semantics for local configurations, we see that the only way the value of variable x can change is by an assignment to x or an input to x from some channel. These correspond to local monitor rules (M-Assign1), (M-Assign2), (M-Input1), and (M-Input2). In all cases, we have that \( \text{time}_1 = \text{time}_1', \text{pre}_1 = \text{pre}_1', \text{term}_1 = \text{term}_1', \) and \( \text{block}_1 = \text{block}_1' \), and so

\[ \text{time}_1' \cup (\cup \text{pre}_1) \cup \text{term}_1' \cup \text{block}_1' \subseteq \ell. \]

By ℓ-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \) (Definition 3, condition 7), we have \( \text{com}_1 = \text{com}_2 \) and \( \text{com}_1' = \text{com}_2' \), and so

\[ \text{lcnf}_2 \xrightarrow{e,e,e_{\sigma_2}} \text{lcnf}'_2. \]

Also, by inspection of rules (M-Assign1), (M-Assign2), (M-Input1), and (M-Input2), we see that \( \Gamma_1(e) \subseteq \Gamma_1'(x) \subseteq \ell \) and \( \text{mayread}(e) \). Moreover, by condition 7 of Definition 3, applied to the variables in e, we have \( \Gamma_2(e) \subseteq \ell \). Therefore by ℓ-equivalence of \( \text{lcnf}_1 \) and \( \text{lcnf}_2 \) and Lemma 1, we have \( e, \text{mem}_1, v \) and \( e, \text{mem}_2, v \) where \( v = \text{mem}_1'(x) \), and thus \( \text{mem}_1'(x) = \text{mem}_2'(x) \) as required.

**Theorem 10** (Preservation of local equivalence). Let \( \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon} \rangle \) and \( \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon} \rangle \) be ℓ-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be ℓ-equivalent strategies. Assume

\[ \langle \text{pool}_1(i), \text{mem}_1, \tau_1 \rangle \xrightarrow{e,e,e_{\sigma_1}} \langle \text{thread}_{1,i}, \text{mem}_1, \tau'_1 \rangle \]

and we have \( \text{thread}_{1,i} \), \( \text{mem}_1' \), and \( \tau'_1 \) such that \( \langle \text{thread}_{1,i}, \text{mem}_1', \tau'_1 \rangle \) and \( \langle \text{thread}_{2,i}, \text{mem}_2', \tau'_2 \rangle \) are ℓ-equivalent. and either \( \text{thread}_{1,i} = \text{pool}_1(i) \), \( \text{mem}_2 = \text{mem}_2 \), and \( \tau'_2 = \tau_2 \) or

\[ \langle \text{pool}_2(i), \text{mem}_2, \tau_2 \rangle \xrightarrow{e,e,e_{\sigma_2}} \langle \text{thread}_{2,i}, \text{mem}_2, \tau'_{2,i} \rangle. \]

Then for all \( j \in \text{pre}(\text{pool}_1) \setminus \{i\} \) we have \( \langle \text{pool}_1(j), \text{mem}_1, \tau'_1 \rangle \) and \( \langle \text{pool}_2(j), \text{mem}_2, \tau'_2 \rangle \) are ℓ-equivalent.

**Proof:** First note that \( \tau'_1 \downarrow \ell = \tau'_2 \downarrow \ell \), since \( \langle \text{thread}_{1,i}, \text{mem}_1', \tau'_1 \rangle \) and \( \langle \text{thread}_{2,i}, \text{mem}_2', \tau'_2 \rangle \) are ℓ-equivalent.
Let \( j \in \text{pre}(\text{pool}_1) \setminus \{i\} \). By \( \ell \)-equivalence of \( \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle \) and \( \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle \) we have that local configurations \( \langle \text{pool}_1(j), \text{mem}_1, \tau_1 \rangle \) and \( \langle \text{pool}_2(j), \text{mem}_2, \tau_2 \rangle \) are \( \ell \)-equivalent, and we need to show that \( \langle \text{pool}_1(j), \text{mem}_1', \tau_1' \rangle \) and \( \langle \text{pool}_2(j), \text{mem}_2', \tau_2' \rangle \) are \( \ell \)-equivalent (Definition 5).

Let
\[
\text{pool}_1(j) = [\text{com}_1, \langle \Gamma_1, \text{lmdst}_1, \text{pr}_1, \overline{\text{br}}_1, \text{time}_1, \text{term}_1, \text{block}_1 \rangle, \text{mdst}_1]
\]
and
\[
\text{pool}_2(j) = [\text{com}_2, \langle \Gamma_2, \text{lmdst}_2, \text{pr}_2, \overline{\text{br}}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle, \text{mdst}_2]
\]
and
\[
\text{pool}_1'(i) = [\text{com}_1', \langle \Gamma_1', \text{lmdst}_1', \text{pr}_1', \overline{\text{br}}_1', \text{time}_1', \text{term}_1', \text{block}_1' \rangle, \text{mdst}_1']
\]
and
\[
\text{pool}_2'(i) = [\text{com}_2', \langle \Gamma_2', \text{lmdst}_2', \text{pr}_2', \overline{\text{br}}_2', \text{time}_2', \text{term}_2', \text{block}_2' \rangle, \text{mdst}_2']
\]

Since \( \tau_1' \) and \( \tau_2' \) are \( \ell \)-equivalent, and \( \langle \text{pool}_1(j), \text{mem}_1, \tau_1 \rangle \) and \( \langle \text{pool}_2(j), \text{mem}_2, \tau_2 \rangle \) are \( \ell \)-equivalent, we only need to check that conditions of local-configuration-\( \ell \)-equivalence that involve the memory are still satisfied. Specifically, we need to show conditions 5 and 6 hold.

- Condition 5 requires that if \( x \in \text{pre}(\Gamma_1) \) and \( \text{lmdst}_1 \triangleright \text{mayread}(x) \) and \( \Gamma_1(x) \cup \text{time}_1 \cup (\bigcup \text{pr}_1) \subseteq \ell \) and \( \Gamma_2(x) \cup \text{time}_2 \cup (\bigcup \text{pr}_2) \subseteq \ell \) then \( \text{mem}_1(x) = \text{mem}_2(x) \) and \( \Gamma_1(x) = \Gamma_2(x) \).

- Condition 6 requires that for all variables \( x \), if \( x \not\in \text{pre}(\Gamma_1) \) and \( \text{lmdst}_1 \triangleright \text{mayread}(x) \) and \( \text{L}(x) \cup \text{time}_1 \cup (\bigcup \text{pr}_1) \subseteq \ell \) and \( \text{L}(x) \cup \text{time}_2 \cup (\bigcup \text{pr}_2) \subseteq \ell \) then \( \text{mem}_1(x) = \text{mem}_2(x) \).

We will address both of these cases at once. Let \( x \) be a variable such that \( \text{lmdst}_1 \triangleright \text{mayread}(x) \) and \( \Gamma_1(x) \cup \text{time}_1 \cup (\bigcup \text{pr}_1) \subseteq \ell \) and \( \Gamma_2(x) \cup \text{time}_2 \cup (\bigcup \text{pr}_2) \subseteq \ell \).

If it is not the case that \( \text{lmdst}_1 \triangleright \text{maywrite}(x) \) (and thus, not the case that \( \text{lmdst}_1 \triangleright \text{maywrite}(x) \)), then the \( i \)th threads could not have written to \( x \), and so, \( \text{mem}_1(x) = \text{mem}_1'(x) \) and \( \text{mem}_2(x) = \text{mem}_2'(x) \). Moreover, since \( \langle \text{pool}_1(j), \text{mem}_1, \tau_1 \rangle \) and \( \langle \text{pool}_2(j), \text{mem}_2, \tau_2 \rangle \) are \( \ell \)-equivalent, we have \( \text{mem}_1(x) = \text{mem}_2(x) \), and thus \( \text{mem}_1'(x) = \text{mem}_2'(x) \) as required.

Otherwise, it is the case that \( \text{lmdst}_1 \triangleright \text{maywrite}(x) \) (and thus, \( \text{lmdst}_1 \triangleright \text{maywrite}(x) \)). From the sound use of mode states, this means that \( \text{lmdst}_1 \triangleright \text{othersmoothwrite}(x) \) and \( \text{lmdst}_1 \triangleright \text{othersmoothread}(x) \). By Property 1 we have \( \text{L}(x) \subseteq \Gamma_1(x) \) and \( \Gamma_1(x) \subseteq \ell \). Thus, we have \( \Gamma_1(x) \subseteq \ell \). If thread \( i \) did not write \( x \), then by a similar argument above, we have \( \text{mem}_1'(x) = \text{mem}_2'(x) \) as required. If thread \( i \) did write \( x \), then by Lemma 2 we have \( \text{mem}_1'(x) = \text{mem}_2'(x) \) as required. ■

E. \( \ell \)-equivalent global configuration results

We present two of the theorems related to showing that given two \( \ell \)-equivalent global configurations, if one of them takes a step, then the other can take zero or more steps to a \( \ell \)-equivalent global configuration. We actually require more than just two \( \ell \)-equivalent global configurations: we require that those two global configurations were produced by executions that started from the same initial pool state, and that these two executions are “well aligned”, i.e., the global configurations in the executions can be lined up into \( \ell \)-equivalent pairs.

Definition 5. Let \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) be two \( \ell \)-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be two \( \ell \)-equivalent strategies. We say that \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned for \( \sigma_1 \) and \( \sigma_2 \) (or, simply, well aligned if the strategies are clear from context) if there exists an initial pool state \( \text{pool}_0 \), \( k_1 \) and \( k_2 \) such that
\[
\text{gcnf}_1^0 = \text{gcnf}_2^0 = \langle \text{pool}_0, \text{mem}_\text{init}, \tau_\text{init}, \text{gmon}_\text{init}, \text{pre} (\text{pool}_0) \rangle
\]
and
\[
\text{for all } i \in 0..(k_1 - 1). \text{ gcnf}_1^i \rightarrow_{\sigma_1} \text{ gcnf}_1^{i+1}
\]
and
\[
\text{gcnf}_1 = \text{gcnf}_1^{k_1}
\]
and
\[
\text{for all } j \in 0..(k_2 - 1). \text{ gcnf}_2^j \rightarrow_{\sigma_2} \text{ gcnf}_2^{j+1}
\]
and
\[
\text{gcnf}_2 = \text{gcnf}_2^{k_2}
\]
and there is a relation \( R \subseteq \{0, \ldots, k_1\} \times \{0, \ldots, k_2\} \) such that all of the following hold.

- if \( (i, j) \in R \) then \( \text{gcnf}_1^i \) is \( \ell \)-equivalent to \( \text{gcnf}_2^j \).
- \( (0, 0) \in R \).
Then there exists \( \text{gcnf}_1 \in \text{reach}_{\sigma_1}(\text{pool}_0) \) and \( \text{gcnf}_2 \in \text{reach}_{\sigma_2}(\text{pool}_0) \) but also requires that the executions to reach these global configurations can be lined up appropriately into \( \ell \)-equivalent configurations. The relation \( R \) is a correspondence \([37]\), which shows how the configurations in the two executions line up.

The proof of the soundness of our monitor relies on being given an execution using strategy \( \sigma_1 \) and constructing a well-aligned execution from \( \sigma_2 \), where \( \sigma_1 \) and \( \sigma_2 \) are \( \ell \)-equivalent strategies. This essentially implies that an observer of the channel \( \ell \) cannot distinguish the two executions.

**Theorem 11** (Non-barrier global low step). Let \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) be \( \ell \)-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies. Suppose that \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned. Suppose \( \text{gcnf}_1 \dashv_{\sigma_1} \text{gcnf}'_1 \) by the \( i \)th thread taking a step using the first rule of Figure 2, where

\[
\text{time}_i \cup (\cup \overline{\text{pc}}_i) \cup \text{term}_i \cup \text{block}_i \subseteq \ell
\]

and

\[
\text{pool}_1(i) = [\text{com}_i, \langle \Gamma_i, \text{lmdst}_i, \overline{\text{br}}_i, \text{time}_i, \text{term}_i, \text{block}_i \rangle, \text{mdst}_i]
\]

and

\[
\text{gcnf}_1 = \langle \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle \rangle.
\]

Then there exists \( \text{gcnf}'_2 \) such that \( \text{gcnf}_2 \vdash_{\sigma_2} \text{gcnf}'_2 \) and \( \text{gcnf}'_1 \) and \( \text{gcnf}'_2 \) are \( \ell \)-equivalent and well aligned.

**Proof:** Since \( \text{gcnf}_1 \) and \( \text{gcnf}_2 = \langle \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle \rangle \) are \( \ell \)-equivalent, we have that local configurations \( \langle \text{pool}_1(i), \text{mem}_1, \tau_1 \rangle \) and \( \langle \text{pool}_2(i), \text{mem}_2, \tau_2 \rangle \) are \( \ell \)-equivalent. The result follows immediately by Theorem 7 and Theorem 10.

**Theorem 12** (Non-barrier global high step). Let \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) be \( \ell \)-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies. Suppose that \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned. Suppose \( \text{gcnf}_1 \dashv_{\sigma_1} \text{gcnf}'_1 \) by the \( i \)th thread taking a step using the first rule of Figure 2, where

\[
\text{time}_i \cup (\cup \overline{\text{pc}}_i) \cup \text{term}_i \cup \text{block}_i \subseteq \ell
\]

and

\[
\text{pool}_1(i) = [\text{com}_i, \langle \Gamma_i, \text{lmdst}_i, \overline{\text{br}}_i, \text{time}_i, \text{term}_i, \text{block}_i \rangle, \text{mdst}_i]
\]

and

\[
\text{gcnf}_1 = \langle \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle \rangle.
\]

Then \( \text{gcnf}'_1 \) and \( \text{gcnf}_2 \) are \( \ell \)-equivalent.

**Proof:** Since \( \text{gcnf}_1 \) and \( \text{gcnf}_2 = \langle \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle \rangle \) are \( \ell \)-equivalent, we have that local configurations \( \langle \text{pool}_1(i), \text{mem}_1, \tau_1 \rangle \) and \( \langle \text{pool}_2(i), \text{mem}_2, \tau_2 \rangle \) are \( \ell \)-equivalent.

Let

\[
\text{pool}'_1(i) = [\text{com}'_i, \langle \Gamma'_i, \text{lmdst}'_i, \overline{\text{br}}'_i, \text{time}'_i, \text{term}'_i, \text{block}'_i \rangle, \text{mdst}'_i]
\]

where

\[
\text{gcnf}'_1 = \langle \langle \text{pool}'_1, \text{mem}'_1, \tau'_1, \text{gmon}'_1 \rangle \rangle.
\]

By Property 2 since this is not a synchronization step, we have that

\[
\text{time}_i \cup (\cup \overline{\text{pc}}_i) \cup \text{term}_i \cup \text{block}_i \subseteq \text{time}'_i \cup (\cup \overline{\text{pc}}'_i) \cup \text{term}'_i \cup \text{block}'_i
\]

and so

\[
\text{time}'_i \cup (\cup \overline{\text{pc}}'_i) \cup \text{term}'_i \cup \text{block}'_i \subseteq \ell
\]

The result follows immediately by Theorem 8 and Theorem 10.
F. Global barrier results

The global barrier theorem (Theorem 13) states, essentially, that if we have two \( \ell \)-equivalent global configurations, and one of them performs a barrier synchronization, then the other global configuration can also reach the global barrier, and successfully perform the synchronization.

In order to prove this, we first define a notion of low equivalence, very similar to \( \ell \)-equivalence of local configurations, but ignoring the timing level. We call this time-insensitive-\( \ell \)-equivalence. Some key differences from \( \ell \)-equivalence include: the strong guarantees apply when the pc excluding the timing is low; we don’t care about equality of memory; we require that the time-inclusive pc is high (simplifies some of the cases).

**Definition 6** (Time-insensitive-\( \ell \)-equivalence of thread states). Two thread states

\[
[\text{com}_1, \text{mc}_1, \text{mdst}_1] \text{ and } [\text{com}_2, \text{mc}_2, \text{mdst}_2]
\]

are time-insensitive-\( \ell \)-equivalent if and only if all the following conditions hold. Assume that for \( i = 1, 2 \) we have

\[
\text{mc}_i = (\Gamma_i, \text{lmdst}_i, \text{pc}_i, \overline{\text{br}}_i, \text{time}_i, \text{term}_i, \text{block}_i)
\]

1) \( \text{time}_1 \uplus (\uplus \text{pc}_1) \uplus \text{term}_1 \uplus \text{block}_1 \not\subseteq \ell \) and \( \text{time}_2 \uplus (\uplus \text{pc}_2) \uplus \text{term}_2 \uplus \text{block}_2 \not\subseteq \ell \).

2) If

\[
(\uplus \text{pc}_1) \uplus \text{term}_1 \uplus \text{block}_1 \subseteq \ell
\]

or

\[
(\uplus \text{pc}_2) \uplus \text{term}_2 \uplus \text{block}_2 \subseteq \ell
\]

then \( \text{time}_1 = \text{time}_2 \) and \( \text{term}_1 = \text{term}_2 \) and \( \text{block}_1 = \text{block}_2 \) and \( \overline{\text{pc}}_1 = \overline{\text{pc}}_2 \) and \( \overline{\text{br}}_1 = \overline{\text{br}}_2 \) and \( \text{com}_1 = \text{com}_2 \) and for all \( x \), \( \Gamma_1(x) = \Gamma_2(x) \).

3) For all \( j \in 1..\max(|\text{pc}_1|, |\text{pc}_2|) \), if

\[
(\text{pc}_1(0) \cup \ldots \cup \text{pc}_1(j-1)) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell
\]

or

\[
(\text{pc}_2(0) \cup \ldots \cup \text{pc}_2(j-1)) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell
\]

then all of the following holds

- \( |\text{pc}_1| \geq j \).
- \( |\text{pc}_2| \geq j \).
- For all \( k \in 0..j \) we have \( \overline{\text{pc}}_1(k) = \overline{\text{pc}}_2(k) \) and \( \overline{\text{br}}_1(k) = \overline{\text{br}}_2(k) \).
- There exists \( \text{com}_{\text{cnt}} \in \text{Continuation}_i \) such that:
  - Either \( \text{com}_1 = \text{com}_{\text{cnt}} \) or \( \text{com}_1 = \text{com}_1' \); \( \text{com}_{\text{cnt}} \) for some \( \text{com}_1' \); and
  - Either \( \text{com}_2 = \text{com}_{\text{cnt}} \) or \( \text{com}_2 = \text{com}_2' \); \( \text{com}_{\text{cnt}} \) for some \( \text{com}_2' \).

(This says that the pc level stack and the branch environments are identical up to and including the first high branch, and the two configurations agree on the low continuation.)

We use this definition of time-insensitive-\( \ell \)-equivalence to define the following key lemma for proving the global barrier theorem.

**Lemma 3** (Time-insensitive-\( \ell \)-equivalent execution). Let \( \text{gcnf}_1 = \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle \) and \( \text{gcnf}_2 = \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle \) be \( \ell \)-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies. Suppose that \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned.

Let \( \text{gcnf}_1' = \langle \text{pool}'_1, \text{mem}'_1, \tau'_1, \text{gmon}'_1 \rangle \).

Let \( \text{gcnf}_1 \xrightarrow[\sigma_1]{*} \text{gcnf}_1' \) without executing a synchronization step.

Suppose \( i \in \text{pre} \text{(pool}_1) \) and \( \text{pool}_1(i) \) and \( \text{pool}_2(i) \) are time-insensitive-\( \ell \)-equivalent, and

\[
\text{pool}_1'(i) = [\text{com}'_i, (\Gamma'_i, \text{lmdst}'_i, \text{pc}'_i, \overline{\text{br}}'_i, \text{time}'_i, \text{term}'_i, \text{block}'_i), \text{mdst}'_i]
\]

where

\[
(\uplus \text{pc}'_i) \uplus \text{term}'_i \uplus \text{block}'_i \subseteq \ell.
\]

Then for all (fair) executions from \( \text{gcnf}_2 \), there exists a \( \text{gcnf}_2' = \langle \text{pool}'_2, \text{mem}'_2, \tau'_2, \text{gmon}'_2 \rangle \) in the execution (reachable without going through a synchronization step) such that \( \text{pool}'_2(i) \) and \( \text{pool}_2(i) \) are time-insensitive-\( \ell \)-equivalent.
In order to prove Lemma 3, we first state and prove several supporting lemmas (Lemma 4, Lemma 5, and Lemma 6), which are analogous to, respectively, a high-step lemma, a low-step lemma, and a high-to-low-step lemma, but for time-insensitive-ℓ-equivalence instead of ℓ-equivalence. Another key difference is that while the ℓ-equivalence theorems simply needed to find some steps that satisfied the requirements, these lemmas must hold for all fair executions. This is because at this point in the proof, the second execution has already been chosen (by the ℓ-equivalence theorems) and we need to show that these executions satisfy some additional properties.

**Lemma 4** (Time-insensitive-ℓ-equivalence high step). Let

\[ gcnf_1 = \langle \{\text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1\} \rangle \]

and

\[ gcnf_2 = \langle \{\text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2\} \rangle \]

be ℓ-equivalent global configurations, and let σ₁ and σ₂ be ℓ-equivalent strategies. Suppose that gcnf₁ and gcnf₂ are well aligned. \( i \in \text{pre}(\text{pool}_1) \) and assume \( \text{pool}_1(i) \) and \( \text{pool}_2(i) \) are time-insensitive-ℓ-equivalent.

Let \( gcnf_1 \rightarrow_\sigma \ gcnf'_1 \) be a non-synchronization step. Let \( gcnf'_1 = \langle \{\text{pool}'_1, \text{mem}'_1, \tau'_1, \text{gmon}'_1\} \rangle \) and

\[
\begin{align*}
\text{pool}_1(i) &= [\text{com}_{1,i}, \{\Gamma_{1,i}, \text{lmdst}_{1,i}, \text{pc}_{1,i}, \text{br}_{1,i}, \text{time}_{1,i}, \text{term}_{1,i}, \text{block}_{1,i}, \text{mdst}_{1,i}\}] \\
\text{pool}'_1(i) &= [\text{com}'_{1,i}, \{\Gamma'_{1,i}, \text{lmdst}'_{1,i}, \text{pc}'_{1,i}, \text{br}'_{1,i}, \text{time}'_{1,i}, \text{term}'_{1,i}, \text{block}'_{1,i}, \text{mdst}'_{1,i}\}]
\end{align*}
\]

If \( \text{pool}_1(i) \neq \text{pool}'_1(i) \) and

\[
(\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell
\]

and

\[
(\bigcup \text{pc}_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \not\subseteq \ell
\]

then \( \text{pool}'_1(i) \) is time-insensitive-ℓ-equivalent to \( \text{pool}_2(i) \).

**Proof:** First note that \( (\bigcup \text{pc}_{1,i}) \cup \text{term}_{2,i} \cup \text{block}_{2,i} \not\subseteq \ell \), since \( (\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \) and \( \text{pool}_1(i) \) is time-insensitive-ℓ-equivalent to \( \text{pool}_2(i) \).

We show that each condition holds.

- **Condition 1** Since \( (\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \) we have \( \text{time}_{1,i} \cup (\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \). Also, \( \text{time}_{2,i} \cup (\bigcup \text{pc}_{2,i}) \cup \text{term}_{2,i} \cup \text{block}_{2,i} \not\subseteq \ell \) since \( \text{pool}_1(i) \) is time-insensitive-ℓ-equivalent to \( \text{pool}_2(i) \).

- **Condition 2** We have \( (\bigcup \text{pc}_{2,i}) \cup \text{term}_{2,i} \cup \text{block}_{2,i} \not\subseteq \ell \). Also, by assumption we have \( (\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \). Thus the precondition is false, and this condition is trivially satisfied.

- **Condition 3** By cases on the monitor rule used in the derivation of judgment

\[
\langle \text{pool}_1(i), \text{mem}_1, \tau_1 \rangle \xrightarrow{\beta_1 \gamma_1 \delta_1} \langle \text{pool}(i), \text{mem}'_1, \tau'_1 \rangle
\]

- (M-Skip), (M-Assign1), (M-Assign2), (M-Input1), (M-Input2), (M-Output), and (M-Term). In all of these rules, most of the monitor state remains the same \( \langle \text{pc}_1 = \text{pc}'_1, \text{br}_1 = \text{br}'_1, \text{time}_1 = \text{term}'_1, \text{block}_1 = \text{block}'_1 \rangle \), which ensures that most of the requirements of Condition 4 are met. Note also that none of these commands reduce a join or a more, and so if there previously existed a \text{com}_\text{cnt} that satisfied the requirements, there will continue to exist such a \text{com}_\text{cnt} that satisfies the requirements.

- (M-Branch), (M-Enter). These commands increase the pc and branch environment stack by one. But since \( (\bigcup \text{pc}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \), the precondition is not satisfied for this new branch.

- (M-Join), (M-Leave). These commands pop an element off the pc and branch environment stack, and increase the termination and blocking levels. Thus, the required conditions follow from the time-insensitive-ℓ-equivalence of \( \text{pool}_1(i) \) and \( \text{pool}_2(i) \).

- (M-Barrier-Local) is impossible, since it was a non-synchronization step.

**Lemma 5** (Time-insensitive-ℓ-equivalence low step). Let \( gcnf_1 = \langle \{\text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1\} \rangle \) be a global configuration and let \( gcnf_0^1, gcnf^1_2, gcnf^2_3, \ldots \) be a (finite or infinite) sequence of global configurations such that for all \( j < n \) we have \( gcnf^j_2 \rightarrow_\tau_j gcnf^{j+1}_2 \) (without taking a synchronization step), and the execution is fair (i.e., for any thread that can take a non-synchronization step, it eventually does) and \( gcnf_1 \) is ℓ-equivalent to \( gcnf^j_2 \) for all \( gcnf^j_2 \) in the sequence. Moreover, suppose that \( gcnf_1 \) and \( gcnf^0_2 \) are well aligned.

Let \( gcnf^j_2 = \langle \{\text{pool}^j_1, \text{mem}^j_2, \tau^j_1, \text{gmon}^j_2\} \rangle \).

Let \( i \in \text{pre}(\text{pool}_1) \) and assume \( \text{pool}_1(i) \) and \( \text{pool}_2(i) \) are time-insensitive-ℓ-equivalent.
Let $\text{gcnf}_1 \rightarrow_{\sigma_1} \text{gcnf}'_1$ be a non-synchronization step. Let $\text{gcnf}'_1 = \langle \langle \text{pool}'_1, \text{mem}'_1, \tau'_1, gmn'_1 \rangle \rangle$ and

$$\text{pool}'_1(i) = [\text{com}'_{1,i}, \langle \Gamma_1, \text{lmdst}'_1, \overline{\text{pc}}_1, \overline{\text{br}}_1, \text{time}'_1, \text{term}'_1, \text{block}'_1, \text{mdst}'_1 \rangle]$$

and

$$\text{pool}'_1(i) = [\text{com}'_{1,i}, \langle \Gamma_1, \text{lmdst}'_1, \overline{\text{pc}}_1, \overline{\text{br}}_1, \text{time}'_1, \text{term}'_1, \text{block}'_1, \text{mdst}'_1 \rangle]$$

If $\text{pool}'_1(i) \neq \text{pool}'_1(i)$ and

$$((\cup \overline{\text{pc}}_1) \cup \text{term}'_1 \cup \text{block}'_1 \subseteq \ell)$$

then there exists a $k$ such that for all $j \in 1..k$, $\text{pool}'_2(i) = \text{pool}'_2(i)$ and $\text{pool}'_2(i) \neq \text{pool}'_2(i)$ and $\text{pool}'_1(i)$ is time-insensitive-$\ell$-equivalent to $\text{pool}'_2(i)$ (i.e., the next step that thread $i$ takes is to an equivalent thread state).

Proof: Assume $\text{pool}'_1(i) \neq \text{pool}'_1(i)$ and $((\cup \overline{\text{pc}}_1) \cup \text{term}'_1 \cup \text{block}'_1 \subseteq \ell)$. Since $\text{pool}'_1(i)$ and $\text{pool}'_2(i)$ are time-insensitive-$\ell$-equivalent, by Condition 2 of Definition 6 we have $\text{time}'_1, i = \text{time}'_2, i$ and $\text{term}'_1, i = \text{term}'_2, i$ and $\text{block}'_1, i = \text{block}'_2, i$ and $\text{pc}_1, i = \text{pc}'_2, i$ and $\overline{\text{br}}_1, i = \overline{\text{br}}_2, i$ and $\text{mem}'_1, i = \text{mem}'_2, i$ and for all $x$, $\Gamma_1, i(x) = \Gamma_2, i(x)$.

Moreover, since the step $\text{gcnf}_1 \rightarrow_{\sigma_1} \text{gcnf}'_1$ was not a synchronization step, we have

$$\langle \text{pool}'_1(i), \text{mem}_1, \tau_1 \rangle \xrightarrow{\epsilon, \epsilon} \langle \text{pool}'_1(i), \text{mem}'_1, \tau'_1 \rangle$$

and thus, by inversion on the inference rule in Figure 1

$$(\text{com}'_{1,i}, \text{mem}_1, \tau_1) \xrightarrow{\alpha, \epsilon} (\text{com}'_{1,i}, \text{mem}'_1, \tau'_1)$$

and

$$\langle \text{com}'_{1,i}, \text{mem}_1, \tau_1 \rangle \xrightarrow{\epsilon, \epsilon} \langle \text{com}'_{1,i}, \text{mem}'_1, \tau'_1 \rangle$$

Since the execution from $\text{gcnf}'_0$ is fair, the $t$th thread must be scheduled eventually. Assume that this occurs after $k$ steps, i.e., $\text{pool}'_2(i) \neq \text{pool}'_2(i)$.

We proceed by induction on the command $\text{com}'_{2, i}$. We show that we can construct a derivation for

$$\langle \text{pool}'_2(i), \text{mem}'_2, \tau'_2 \rangle \xrightarrow{\epsilon, \epsilon} \langle \text{pool}'_2(i), \text{mem}'_2, \tau'_2 \rangle$$

such that $\text{pool}'_2(i)$ is time-insensitive-$\ell$-equivalent to $\text{pool}'_2(i)$

- **com'_{2,i} = skip.** Here, $\text{com}'_{2,i} = \text{com}'_{2,i}$, $\text{time}'_{2,i} = \text{time}'_{2,i}$, $\text{term}'_{2,i} = \text{term}'_{2,i}$, $\text{block}'_{2,i} = \text{block}'_{2,i}$ and $\text{mem}'_{2,i} = \text{mem}'_{2,i}$ and $\overline{\text{br}}_{2,i} = \overline{\text{br}}_{2,i}$ and for all $x$, $\Gamma_{1, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x)$.

  The result follows trivially.

- **com'_{2,i} = stop; com.** Here, $\text{com}'_{2,i} = \text{com}'_{2,i}$, $\text{time}'_{2,i} = \text{time}'_{2,i}$, $\text{term}'_{2,i} = \text{term}'_{2,i}$, $\text{block}'_{2,i} = \text{block}'_{2,i}$ and $\text{mem}'_{2,i} = \text{mem}'_{2,i}$ and $\overline{\text{br}}_{2,i} = \overline{\text{br}}_{2,i}$ and for all $x$, $\Gamma_{1, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x)$.

  The result follows trivially.

- **com'_{2,i} = stop.** Here, $\text{com}'_{2,i} = \text{com}'_{2,i}$, $\text{time}'_{2,i} = \text{time}'_{2,i}$, $\text{term}'_{2,i} = \text{term}'_{2,i}$, $\text{block}'_{2,i} = \text{block}'_{2,i}$ and $\text{mem}'_{2,i} = \text{mem}'_{2,i}$ and $\overline{\text{br}}_{2,i} = \overline{\text{br}}_{2,i}$ and $\overline{\text{br}}_{2,i} = \overline{\text{br}}_{2,i}$ and $\epsilon$ and for all $x$, $\Gamma_{1, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x) = \Gamma_{2, i}(x)$.

  The result follows trivially.

- **com'_{2,i} = input ch to x.** This case is nearly identical to the assignment case above.
• $\text{com}_{k,i}^i = \text{output } e \text{ to } ch$.

Here, $\text{com}_{k+1,i}^i = \text{com}_{1,i}^i = \text{stop and } time_{1,i} = time_{k,i} = time_{k+1,i}$ and $\text{term}_{1,i} = \text{term}_{k,i} = \text{term}_{k+1,i}$ and $\text{block}_{1,i} = \text{block}_{k+1,i} = \text{block}_{k+1,i}$ and $\overline{\text{pc}}_{1,i} = \overline{\text{pc}}_{k+1,i} = \overline{\text{pc}}_{k+1,i}$ and $\overline{\text{br}}_{1,i} = \overline{\text{br}}_{k+1,i} = \overline{\text{br}}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$.

Moreover, we have $\text{lmdst}_{1,i} \triangleright \text{mayread}(e)$ and $\Gamma_i(x) \cup time_{1,i} \cup (\overline{\text{pc}}_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \subseteq ch$. But since $\text{lmdst}_{1,i} = \text{lmdst}_{k,i}^i$, and from the other equalities, we have $\text{lmdst}_{2,i} \triangleright \text{mayread}(e)$ and $\Gamma^1_{k,i} \cup time_{2,i} \cup (\overline{\text{pc}}_{2,i}) \cup \text{term}_{2,i} \cup \text{block}_{2,i} \subseteq ch$.

We can thus construct an appropriate derivation, and the result follows easily.

• $\text{com}_{2,i}^i = \text{while } e \text{ do } \text{com } \text{od. Here, } \text{com}_{k+1,i}^i = \text{com}_{1,i}^i = \text{more } e \text{ do } \text{com } \text{od and } time_{1,i} = time_{k,i} = time_{k+1,i}$ and $\text{term}_{1,i} = \text{term}_{k,i} = \text{term}_{k+1,i}$ and $\text{block}_{1,i} = \text{block}_{k+1,i} = \text{block}_{k+1,i}$ and $\overline{\text{pc}}_{1,i} = \overline{\text{pc}}_{k+1,i} = \overline{\text{pc}}_{k+1,i}$ and $\overline{\text{br}}_{1,i} = \overline{\text{br}}_{k+1,i} = \overline{\text{br}}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$.

Also, we have $\text{lmdst}_{1,i} \triangleright \text{mayread}(e)$ and $\Gamma^1_{i,i}(x) = \Gamma^1_{k+1}(x)$.

Thus, we can easily construct an appropriate derivation, and the result follows easily.

• $\text{com}_{k,i}^i = \text{join. Here, } \text{com}_{k+1,i}^i = \text{com}_{1,i}^i = \text{stop and } time_{1,i} = time_{k,i} = time_{k+1,i}$ and $\text{term}_{1,i} = \text{term}_{k,i} = \text{term}_{k+1,i}$ and $\text{block}_{1,i} = \text{block}_{k+1,i} = \text{block}_{k+1,i}$ and $\overline{\text{pc}}_{1,i} = \overline{\text{pc}}_{k+1,i} = \overline{\text{pc}}_{k+1,i}$ and $\overline{\text{br}}_{1,i} = \overline{\text{br}}_{k+1,i} = \overline{\text{br}}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$.

Also, we have $\text{lmdst}_{1,i} \triangleright \text{mayread}(e)$ and $\Gamma^1_{i,i}(x) = \Gamma^1_{k+1}(x)$.

We can easily construct an appropriate derivation, such that $time_{1,i} = time_{k+1,i}^i$ and $\text{term}_{i} = \text{term}_{k+1,i}^i$ and $\text{block}_{1,i} = \text{block}_{k+1,i}^i$ and $\overline{\text{pc}}_{1,i} = \overline{\text{pc}}_{k+1,i}^i$ and $\overline{\text{br}}_{1,i} = \overline{\text{br}}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$, whereupon the result follows trivially.

• $\text{com}_{k,i}^i = \text{if } e \text{ then } \text{com } \text{e } \text{else } \text{com } \text{f } \text{fi. Here, either } \text{com}_{1,i} = \text{com } \text{e } \text{or } \text{com}_{1,i} = \text{com } \text{f } \text{and } time_{1,i} = time_{k,i} = time_{k+1,i}$ and $\text{term}_{1,i} = \text{term}_{k,i} = \text{term}_{k+1,i}$ and $\text{block}_{1,i} = \text{block}_{k+1,i} = \text{block}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$.

Also, we have $\text{lmdst}_{1,i} \triangleright \text{mayread}(e)$ and $\Gamma^1_{i,i}(x) = \Gamma^1_{k+1}(x)$.

We consider two possible cases, based on whether $\ell_{sb} \subseteq \ell$.

If $\ell_{sb} \subseteq \ell$ then by $\ell$-equivalence of $\text{genf}_{1,i}$ and $\text{genf}_{2,i}$, we have $\text{mem}_{1,i}(e) = \text{mem}_{2,i}(e)$. Thus, based on the inference rules for conditionals, $\text{com}_{1,i}^i = \text{com}_{k+1,i}^i$, and we can construct appropriate derivations such that $pool_{1,i}(i)$ is time-insensitive-$\ell$-equivalent to $pool_{k+1,i}$ because all of the thread state is identical.

Otherwise, $\ell_{sb} \not\subseteq \ell$, and so we don’t know whether $\text{com}_{1,i}^i = \text{com}_{k+1,i}^i$. However, in this case, $(\overline{\text{pc}}^i_{1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell$ and $(\overline{\text{pc}}^i_{k+1,i}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell$, and so Condition 3 of time-insensitive-$\ell$-equivalence is trivially satisfied, as is Condition 3 for $j = \overline{\text{pc}}^i_{k+1,i}$.

• $\text{com}_{k,i}^i = \text{more } e \text{ do } \text{com } \text{od. Here, either } \text{com}_{1,i}^i = \text{com } \text{more } e \text{ do } \text{com } \text{od or } \text{com}_{1,i}^i = \text{stop, based on the value of } \text{mem}_{1,i}(e)$.

We consider these cases separately.

- $\text{com}_{k,i}^i = \text{com } \text{more } e \text{ do } \text{com } \text{od.}$

Here $time_{1,i} = time_{k,i} = time_{k+1,i}$ and $\text{term}_{1,i} = \text{term}_{k,i} = \text{term}_{k+1,i}$ and $\text{block}_{1,i} = \text{block}_{k+1,i}$ and for all $x$, $\Gamma_i(x) = \Gamma^1_{k+1}(x)$.

Also, we have $\text{lmdst}_{1,i} \triangleright \text{mayread}(e)$ and $\overline{\text{pc}}_{1,i} = \overline{\text{pc}}_{k+1,i} = \overline{\text{pc}}_{k+1,i}$ and $\overline{\text{br}}_{1,i} = \overline{\text{br}}_{k+1,i} = \overline{\text{br}}_{k+1,i}$.

By Definition 3 we have $\Gamma^1_{i,i}(x) \subseteq \ell_{sb}$.

Since $\text{lmdst}_{1,i} = \text{lmdst}_{k,i}^i$, and from the other equalities, we have $\text{lmdst}_{2,i} \triangleright \text{mayread}(e)$ and $\overline{\text{br}}_{k+1,i} = \overline{\text{br}}_{k+1,i}$.

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We consider two possible cases, based on whether \( \ell_{sb} \subseteq \ell \).

If \( \ell_{sb} \subseteq \ell \) then by \( \ell \)-equivalence of \( gcnf_1 \) and \( gcnf_2 \), we have \( \text{mem}_1(e) = \text{mem}_2(e) \). Thus, based on the inference rules for loops, \( \text{com}_{1,i} = \text{com}_{2,i}^{k+1} \), and we can construct appropriate derivations such that \( \text{pool}^{1}_{1}(i) \) is time-insensitive-\( \ell \)-equivalent to \( \text{pool}^{k+1}_{2}(i) \) because all of the thread state is identical.

Otherwise, \( \ell_{sb} \not\subseteq \ell \), and so we don’t know whether \( \text{com}_{1,i} = \text{com}_{2,i}^{k+1} \). However, in this case, \( (\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}^{1}_{i} \cup \text{block}^{1}_{i} \not\subseteq \ell \) and \( (\cup \overline{\text{pool}^{k+1}_{2}}) \cup \text{term}^{k+1}_{2} \cup \text{block}^{k+1}_{2} \not\subseteq \ell \), and so Condition 2 of time-insensitive-\( \ell \)-equivalence is trivially satisfied, as is Condition 3 for \( j = \max((\overline{\text{pool}^{1}_{i}}, \overline{\text{pool}^{k+1}_{2}})) \).

- \( \text{com}_{1,i} = \text{stop} \).

Here, \( \text{stmt}_{1,i} \triangleright \text{mayread}(e) \) and \( \overline{\text{pool}^{1}_{i}} = \overline{\text{pool}^{1}_{i}} \cdot \ell_{sb} \) and \( \overline{b}^{1}_{1,i} = \overline{b}^{1}_{1,i} \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \) and \( \text{time}^{1}_{i} = \text{time}^{1}_{i} \cup \text{time}_{sb} \cup \ell_{sb} \) and \( \text{term}^{1}_{i} = \text{term}^{1}_{i} \cup \text{term}_{sb} \) and \( \text{block}^{1}_{i} = \text{block}^{1}_{i} \cup \text{block}_{sb} \) and

\[
\Gamma'_{1,i} = \lambda x. \begin{cases} 
\Gamma_{1,i}(x) & \text{if } x \in \text{pre}(\Gamma_{1,i}) \cap \text{pre}(\Gamma_{sb}) \\
\text{undef} & \text{if } x \in \text{pre}(\Gamma_{1,i}) \setminus \text{pre}(\Gamma_{sb}) \\
\text{otherwise} & \end{cases}
\]

Also, we have By Definition 2 we have \( \Gamma_{1,i}(e) \subseteq \ell_{sb} \).

Since \( \text{stmt}_{1,i} = \text{stmt}_{2,i}^{k+1} \), and from the other equalities, we have \( \text{stmt}^{k+1}_{2,i} \triangleright \text{mayread}(e) \) and \( \overline{b}^{k+1}_{2,i} = \overline{b}^{k+1}_{2,i} \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \).

We consider two possible cases, based on whether \( \ell_{sb} \subseteq \ell \).

If \( \ell_{sb} \subseteq \ell \) then by \( \ell \)-equivalence of \( gcnf_1 \) and \( gcnf_2 \), we have \( \text{mem}_1(e) = \text{mem}_2(e) \). Thus, based on the inference rules for loops, \( \text{com}_{1,i} = \text{com}_{2,i}^{k+1} \), and we can construct appropriate derivations such that \( \text{pool}^{1}_{1}(i) \) is time-insensitive-\( \ell \)-equivalent to \( \text{pool}^{k+1}_{2}(i) \) because all of the thread state is identical.

Otherwise, \( \ell_{sb} \not\subseteq \ell \), and so we don’t know whether \( \text{com}_{1,i} = \text{com}_{2,i}^{k+1} \). However, in this case, \( (\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}^{1}_{i} \cup \text{block}^{1}_{i} \not\subseteq \ell \) and \( (\cup \overline{\text{pool}^{k+1}_{2}}) \cup \text{term}^{k+1}_{2} \cup \text{block}^{k+1}_{2} \not\subseteq \ell \), and so Condition 2 of time-insensitive-\( \ell \)-equivalence is trivially satisfied, as is Condition 3 for \( j = \max((\overline{\text{pool}^{1}_{i}}, \overline{\text{pool}^{k+1}_{2}})) \).

- \( \text{com}_{1,i} = \text{term} \). This case is impossible, as \( \text{com}_{1,i} = \text{com}_{2,i}^{k+1} \), and there is no \( \text{com}_{1,i} \) such that \( (\text{com}_{1,i}, \text{mem}_1, \tau_1) \to^{0,\tau_1} (\text{com}_{1,i}, \text{mem}_1', \tau_1') \).

\[ \text{Lemma 6} \text{ (Time-insensitive-\( \ell \)-equivalence high-to-low step). Let } gcnf_1 = \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle \text{ be a global configuration and let } gcnf_2^0, gcnf_2^1, gcnf_2^2, gcnf_2^3, \ldots \text{ be a (finite or infinite) sequence of global configurations such that for all } j < n \text{ we have } gcnf_j \to \sigma_j, gcnf_{j+1}^j \text{ (without taking a synchronization step), and the execution is fair (i.e., for any thread that can take a non-synchronization step, it eventually does). Moreover, suppose that } gcnf_1 \text{ and } gcnf_2 \text{ are well aligned, and that } gcnf_1 \text{ is } \ell \text{-equivalent to } gcnf_2^0 \text{ for all } gcnf_2^j \text{ in the sequence.}
\]

Let \( gcnf_2^j = \langle \text{pool}_2^j, \text{mem}_2^j, \text{mem}_2^j, \text{gmon}_2^j \rangle \).

Let \( i \in \text{pre}(\text{pool}_1) \) and assume \( \text{pool}(i) \) and \( \text{pool}(i) \) are time-insensitive-\( \ell \)-equivalent.

Let \( gcnf_1 \to_{\sigma_1}, gcnf_1' \) be a non-synchronization step. Let \( gcnf_1' = \langle \text{pool}_1', \text{mem}_1', \tau_1', \text{gmon}_1' \rangle \) and

\[
\text{pool}(i) = \langle \text{com}_{1,i}, \langle \Gamma_{1,i}, \text{stmt}_{1,i}, \overline{\text{pool}^{1}_{i}}, \overline{b}^{1}_{1,i}, \text{time}_{1,i}, \text{term}_{1,i}, \text{block}_{1,i} \rangle, \text{mdst}_{1,i} \rangle
\]

and

\[
\text{pool}(i) = \langle \text{com}_{1,i}', \langle \Gamma_{1,i}', \text{stmt}_{1,i}, \overline{\text{pool}^{1}_{i}}, \overline{b}^{1}_{1,i}, \text{time}_{1,i}, \text{term}_{1,i}, \text{block}_{1,i} \rangle, \text{mdst}_{1,i} \rangle
\]

If \( \text{pool}(i) \neq \text{pool}(i) \) and

\[
(\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell
\]

and

\[
(\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \subseteq \ell
\]

then there exists a \( k \) such that \( \text{pool}(i) \) is time-insensitive-\( \ell \)-equivalent to \( \text{pool}(k+1)(i) \) (i.e., the second configuration will take some number of high steps, and eventually get to an equivalent thread state).

\[ \text{Proof: Assume } (\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \not\subseteq \ell \text{ and } (\cup \overline{\text{pool}^{1}_{i}}) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \subseteq \ell \text{. By examination of the monitor rules, we see that the only way this can be true is if the monitor rule used for the step is (M-Join) or (M-Leave), i.e., the thread is exiting the scope of an if command or while command. More specifically, we must have } \overline{\text{pool}^{1}_{i}} = \overline{\text{pool}^{1}_{i}} \cdot \ell_{sb} \text{ where } \ell_{sb} \not\subseteq \ell \text{ and } \overline{b}^{1}_{1,i} = \overline{b}^{1}_{1,i} \cdot (\text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \text{ where } \text{term}_{sb} \subseteq \ell \text{ and } \text{block}_{sb} \subseteq \ell \text{. Moreover, since an if command or while command}
\]

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command just finished, either $com'_{1,i} = \text{stop}; com$ for some $com$, or $com'_{1,i} = \text{stop}; com$, as the other case proceeds very similarly.

By Property 1 we have that $(\ell_{sb}, time_{sb}, \text{block}_{sb}, \Gamma_{sb})$ is a conservative approximation of the branching, timing, termination, and floating behavior for some thread state such that the execution of the $i$th thread had when it started the last command if command or while command that was just completed. Moreover, the join of the pc stack, termination, and blocking levels of the local monitor when it started the if command or while command was bounded above by $\ell$ (since termination and blocking levels increase monotonically, and the pc stack follows a stack discipline).

Let $j = |\mathcal{Pc}|_{1,i} - 1$. We have $\mathcal{Pc}_{1,i}(0) \cup \ldots \cup \mathcal{Pc}_{1,i}(j-1) \cup \text{term}_{1,i} \cup \text{block}_{1,i} \subseteq \ell$, since $\mathcal{Pc}_{1,i}(0) \cup \ldots \cup \mathcal{Pc}_{1,i}(j-1) = (\cup \mathcal{Pc}'_{1,i})$, and $\text{term}_{1,i} \subseteq \text{term}'_{1,i}$ and $\text{block}_{1,i} \subseteq \text{block}'_{1,i}$ and $(\cup \mathcal{Pc}'_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \subseteq \ell$.

So because $pool_{1}(i)$ and $pool_{2}(i)$ are time-insensitive-$\ell$-equivalent, we have that $pool_{1}(i).\text{com} = com_{cnt}$ for some $com_{cnt}$ and either $pool_{2}(i).\text{com} = com_{cnt}$ or $pool_{2}(i).\text{com} = com'$; $com_{cnt}$ for some $com'$. Moreover, let $pool_{2}(i).\text{mon} = (\ell_{2,1}, \text{mdst}_{2,1}, \mathcal{Pc}_{2,1}, \text{br}_{2,1}, time_{2,1}, \text{term}_{2,1}, \text{block}_{2,1})$; we have that $\text{br}_{2,1}(j) = \text{br}_{1,1}(j) = (\ell_{sb}, time_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$.

Thus $(\ell_{sb}, time_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb})$ is a conservative approximation for the command that $pool_{2}(i)$ is in the middle of executing.

Since $\ell_{sb} \nsubseteq \ell$ and $(\cup \mathcal{Pc}) \cup \text{term} \cup \text{block} \nsubseteq \ell$ and $\text{term}_{sb} \cup \text{block}_{sb} \nsubseteq \ell$, we have that for any execution through the command that $pool_{2}(i)$ is in the middle of executing, it cannot encounter a while loop or possibly reach a state where the monitor might block (since otherwise the termination or blocking level would be at least $\ell_{sb}$, which is impossible, by the definition of conservative approximation). Since it cannot loop indefinitely or block, it must terminate.

Thus, we have that there exists a $k$ such that $pool_{2}^{k+1}(i).\text{com} = pool_{1}(i).\text{com}$.

Consider the time-insensitive-$\ell$-equivalence of $pool_{1}(i)$ and $pool_{2}^{k+1}(i)$. Conditions 2 and 3 of Definition 6 follow immediately from the fact that $pool_{2}^{k+1}(i).\text{com} = pool_{1}(i).\text{com}$ by examining the (M-Join) and (M-Leave) monitor rules, we see that $\ell_{sb} \subseteq time'_{1,i}$, and since $\ell_{sb} \nsubseteq \ell$, we have $time'_{1,i} \nsubseteq \ell$, thus satisfying Condition 1.

We can now prove Lemma 7.

**Proof of Lemma 7.** Let $\text{gcnf}_{1} = \langle pool_{1}, \text{mem}_{1}, \tau_{1}, gmon_{1} \rangle$ and $\text{gcnf}_{2} = \langle pool_{2}, \text{mem}_{2}, \tau_{2}, gmon_{2} \rangle$ be $\ell$-equivalent global configurations, and let $\sigma_{1}$ and $\sigma_{2}$ be $\ell$-equivalent strategies. Assume that $\text{gcnf}_{1}$ and $\text{gcnf}_{2}$ are well aligned.

Let $\text{gcnf}_{1} \rightarrow_{\sigma_{1}} \text{gcnf}'_{1}$. Let $\text{gcnf}_{1} \rightarrow_{\sigma_{1}} \text{gcnf}''_{1}$ without executing a synchronization step.

Assume $i \in \text{pre}(pool_{1})$ and $pool_{1}(i)$ and $pool_{2}(i)$ are time-insensitive-$\ell$-equivalent, and

$$pool_{1}(i) = [\text{com}', (\ell_{1,i}, \text{mdst}_{1,i}, \mathcal{Pc}'_{1,i}, \text{br}'_{i}, time'_{i}, \text{term}'_{i}, \text{block}'_{i}, \text{mdst}'_{i})]$$

where

$$(\cup \mathcal{Pc}') \cup \text{term}' \cup \text{block}' \subseteq \ell.$$ 

Suppose we have a fair execution from $\text{gcnf}_{2}$ without executing a synchronization step (i.e., we do not get to choose how $\text{gcnf}_{2}$ evolves, but each thread eventually gets the opportunity to make progress).

We proceed by induction on the length of the execution $\text{gcnf}_{1} \rightarrow_{\sigma_{1}} \text{gcnf}'_{1}$.

Base case: Clearly if $\text{gcnf}_{1} = \text{gcnf}'_{1}$ then the result holds immediately.

Inductive case: Consider $\text{gcnf}_{1} \rightarrow_{\sigma_{1}} \text{gcnf}''_{1}$. For notational convenience, let $\text{gcnf}''_{1} = \langle pool_{1}, \text{mem}_{1}, \tau''_{1}, gmon''_{1} \rangle$, and

$$\text{gcnf}''_{1}(i) = [\text{com}'_{1,i}, (\ell''_{1,i}, \text{mdst}_{1,i}, \mathcal{Pc}'_{1,i}, \text{br}'_{i}, time'_{i}, \text{term}'_{i}, \text{block}'_{i}, \text{mdst}'_{i})].$$

There are 4 (mutually exclusive and exhaustive) cases.

1) $\text{pool}''_{1}(i) = \text{pool}_{1}(i)$.

Here, the step $\text{gcnf}_{1} \rightarrow_{\sigma_{1}} \text{gcnf}''_{1}$ did not advance thread $i$, and so $\text{pool}''_{1}(i)$ is time-insensitive-$\ell$-equivalent to $\text{pool}_{2}(i)$ as required.

2) $\text{pool}''_{1}(i) \neq \text{pool}_{1}(i)$ and $$(\cup \mathcal{Pc}'_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \nsubseteq \ell$$ and $$(\cup \mathcal{Pc}'_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \nsubseteq \ell$$. By Lemma 4 $\text{pool}''_{1}(i)$ is time-insensitive-$\ell$-equivalent to $\text{pool}_{2}(i)$ as required.

3) $\text{pool}''_{1}(i) \neq \text{pool}_{1}(i)$ and $$(\cup \mathcal{Pc}'_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \nsubseteq \ell$$.

Here, the $i$th thread took a step and had a high (time-exclusive) pc level before and after the step.

4) $\text{pool}''_{1}(i) \neq \text{pool}_{1}(i)$ and $$(\cup \mathcal{Pc}'_{1,i}) \cup \text{term}'_{1,i} \cup \text{block}'_{1,i} \nsubseteq \ell$$.

Here, the $i$th thread took a step and had a high (time-exclusive) pc level before the step, and a low (time-exclusive) pc level before the step.
By Lemma 6, thread \( i \) in the 2nd execution will eventually reach a thread configuration that is time-insensitive-\( \ell \)-equivalent, as required.

We state and prove one additional lemma, that shows that time-insensitive-\( \ell \)-equivalence is established when two \( \ell \)-equivalent local configurations go from a “low pc” to a “high pc”.

**Lemma 7** (Establishment of time-insensitive-\( \ell \)-equivalence). Let \( \ell \) be a security level. Let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies and let

\[
\text{lcnf}_1 = \langle \text{com}_1, \langle I_1, \text{lmdst}_1, \text{pc}_1, \text{br}_1, \text{time}_1, \text{term}_1, \text{block}_1 \rangle, \text{mdst}_1, \text{mem}_1, \tau_1 \rangle
\]

and

\[
\text{lcnf}_2 = \langle \text{com}_2, \langle I_2, \text{lmdst}_2, \text{pc}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2 \rangle, \text{mdst}_2, \text{mem}_2, \tau_2 \rangle
\]

be \( \ell \)-equivalent local configurations such that

\[
\text{time}_1 \cup (\cup \text{pc}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell
\]

let

\[
\text{lcnf}'_1 = \langle \text{com}'_1, \langle I'_1, \text{lmdst}'_1, \text{pc}'_1, \text{br}'_1, \text{time}'_1, \text{term}'_1, \text{block}'_1 \rangle, \text{mdst}'_1, \text{mem}'_1, \tau'_1 \rangle
\]

and

\[
\text{lcnf}'_2 = \langle \text{com}'_2, \langle I'_2, \text{lmdst}'_2, \text{pc}'_2, \text{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2 \rangle, \text{mdst}'_2, \text{mem}'_2, \tau'_2 \rangle
\]

be \( \ell \)-equivalent local configurations such that

\[
\text{time}'_1 \cup (\cup \text{pc}'_1) \cup \text{term}'_1 \cup \text{block}'_1 \not\subseteq \ell
\]

Let \( \text{lcnf}_1 \xrightarrow{\mathcal{E}}_{\sigma_1} \text{lcnf}'_1 \) and \( \text{lcnf}_2 \xrightarrow{\mathcal{E}}_{\sigma_2} \text{lcnf}'_2 \).

Then thread states

\[
[\text{com}'_1, \text{mc}'_1, \text{mdst}'_1] \text{ and } [\text{com}'_2, \text{mc}'_2, \text{mdst}'_2]
\]

are time-insensitive-\( \ell \)-equivalent, where

\[
\text{mc}'_1 = \langle I'_1, \text{lmdst}'_1, \text{pc}'_1, \text{br}'_1, \text{time}'_1, \text{term}'_1, \text{block}'_1 \rangle
\]

and

\[
\text{mc}'_2 = \langle I'_2, \text{lmdst}'_2, \text{pc}'_2, \text{br}'_2, \text{time}'_2, \text{term}'_2, \text{block}'_2 \rangle.
\]

**Proof:** By the definition of \( \ell \)-equivalence of local configurations (condition 7), since

\[
\text{time}_1 \cup (\cup \text{pc}_1) \cup \text{term}_1 \cup \text{block}_1 \subseteq \ell
\]

we have

\[
\text{time}_2 \cup (\cup \text{pc}_2) \cup \text{term}_2 \cup \text{block}_2 \subseteq \ell
\]

and since

\[
\text{time}'_1 \cup (\cup \text{pc}'_1) \cup \text{term}'_1 \cup \text{block}'_1 \not\subseteq \ell
\]

we have

\[
\text{time}'_2 \cup (\cup \text{pc}'_2) \cup \text{term}'_2 \cup \text{block}'_2 \not\subseteq \ell.
\]

Thus Condition 1 of time-insensitive-\( \ell \)-equivalence holds.

For Conditions 2 and 3 of time-insensitive-\( \ell \)-equivalence, consider how the pc level can be raised. It must be because one of the monitor rules M-Branch, M-Join, M-Enter, or M-Leave was used. In all of these cases, since the monitor states and commands of \(\text{lcnf}_1\) and \(\text{lcnf}_2\) were identical, the monitor states and commands of \(\text{lcnf}'_1\) and \(\text{lcnf}'_2\) are identical.

Having proven Lemma 6, we can now state and prove the global barrier theorem.

**Theorem 13** (Global barrier). Let \(\text{gcnf}_1\) and \(\text{gcnf}_2\) be \(\ell\)-equivalent global configurations, and let \(\sigma_1\) and \(\sigma_2\) be \(\ell\)-equivalent strategies. Assume that \(\text{gcnf}_1\) and \(\text{gcnf}_2\) are well aligned.

Suppose \(\text{gcnf}_1 \rightarrow_{\sigma_1} \text{gcnf}'_1\) by all live thread configurations taking a synchronization step (i.e., second rule of Figure 4). There exists a finite sequence of global configurations \(\text{gcnf}^0_1, \text{gcnf}^1_2, \ldots, \text{gcnf}^n_2, \text{gcnf}^{n+1}_2\), such that \(\text{gcnf}^0_2 = \text{gcnf}_2\) and for all \(i < n\) we have \(\text{gcnf}^i_2 \rightarrow_{\sigma_2} \text{gcnf}^{i+1}_2\) (without taking a synchronization step), and \(\text{gcnf}^n_2 \rightarrow_{\sigma_2} \text{gcnf}^{n+1}_2\) is a synchronization step. Moreover, for all \(0 \leq i \leq n\) we have that \(\text{gcnf}^i_2\) is \(\ell\)-equivalent to \(\text{gcnf}_1\) and \(\text{gcnf}^{n+1}_2\) is \(\ell\)-equivalent to \(\text{gcnf}'_1\).
Proof: Let $\text{gcnf}_1 = \langle \text{pool}_1, \text{mem}_1, \tau_1, \text{gmon}_1 \rangle$ and $\text{gcnf}_2 = \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle$.

First, construct a fair schedule for the second execution (i.e., that gives each thread in $\text{gcnf}_2$ infinitely many time steps). Execute $\text{gcnf}_2$ using that schedule, preventing barrier synchronization steps. Note that at this point, the execution may be finite (if all threads either get stuck, terminate, or reach a barrier), or infinite (if at least one thread diverges).

Now consider each thread $i \in \text{pre}(\text{pool}_1)$. Let

$$\text{pool}_1(i) = [\text{com}_i, \langle \Gamma_1, \text{ldmdst}_1, \text{pc}_1, \text{br}_1, \text{time}_1, \text{term}_1, \text{block}_1, \text{mdst}_1 \rangle]$$

and

$$\text{pool}_2(i) = [\text{com}_i, \langle \Gamma_2, \text{ldmdst}_2, \text{pc}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2, \text{mdst}_2 \rangle].$$

If $\text{time}_1 ⊔ (∪ \text{pc}_1) ⊔ \text{term}_1 ⊔ \text{block}_1 ⊈ \ell$ then, by $\ell$-equivalence of $\text{gcnf}_1$ and $\text{gcnf}_2$ we have $\text{time}_1 = \text{time}_2$ and $\text{term}_1 = \text{term}_2$ and $\text{block}_1 = \text{block}_2$ and $\text{pc}_1 = \text{pc}_2$ and $\text{br}_1 = \text{br}_2$ and $\text{com}_1 = \text{com}_2$ and for all $x$, $\Gamma_i(x) = \Gamma_2(x)$. This implies that (1) if thread $i \notin \text{alive}(\text{pool}_1)$, then $\text{com}_1(i) = \text{term}_1(i)$ and so $i \notin \text{alive}(\text{pool}_2)$; and (2) if $i \in \text{alive}(\text{pool}_1)$ then $\text{pool}_1(i)$ is ready to execute the barrier step, and all pre-conditions are satisfied, and thus $\text{pool}_2(i)$ is also ready to execute the barrier.

Otherwise, $\text{time}_1 ⊔ (∪ \text{pc}_1) ⊔ \text{term}_1 ⊔ \text{block}_1 ⊈ \ell$ but $(\cup \text{pc}_1) ⊔ \text{term}_1 ⊔ \text{block}_1 ⊈ \ell$. Consider the execution that lead up to $\text{gcnf}_1$. There was some step from one global configuration to some global configuration $\text{gcnf}_1^\text{init}$ such that the timing-inclusive program counter counter level (i.e., $\text{time} ⊔ (∪ \text{pc}) ⊔ \text{term} ⊔ \text{block}$) was $\ell$ before the step, and $\ell \notin \ell$ after the step, and remained $\ell$ until $\text{gcnf}_1$ was reached. Note that since that step, no synchronization step could have been taken during the execution. If we consider the execution that lead to $\text{gcnf}_1$, there was some global configuration $\text{gcnf}_2^\text{init}$ such that $\text{gcnf}_1^\text{init}$ and $\text{gcnf}_2^\text{init}$ are $\ell$-equivalent, and $\text{gcnf}_1^\text{init} \not\sim_\ell \text{gcnf}_2^\text{init}$. Note that the $i$th thread of $\text{gcnf}_1^\text{init}$ is time-insensitive-$\ell$-equivalent to the $i$th thread of $\text{gcnf}_2^\text{init}$, by Lemma 7. Then, by Lemma 5 the execution from $\text{gcnf}_1^\text{init}$ to $\text{gcnf}_2^\text{init}$ and beyond will eventually reach a global configuration $\text{gcnf}_2^* = \langle \text{pool}_2, \text{mem}_2, \tau_2, \text{gmon}_2 \rangle$ such that $\text{gcnf}_2^*(i) = [\text{com}_2, \langle \Gamma_2, \text{ldmdst}_2, \text{pc}_2, \text{br}_2, \text{time}_2, \text{term}_2, \text{block}_2, \text{mdst}_2 \rangle]$ is time-sensitive-$\ell$-equivalent to $\text{pool}_1(i)$. Since $(\cup \text{pc}_1) ⊔ \text{term}_1 ⊔ \text{block}_1 ⊈ \ell$, by Definition 4(Condition 4), we have that $\text{time}_1(i) = \text{time}_2(i)$ and $\text{term}_1(i) = \text{term}_2(i)$ and $\text{block}_1(i) = \text{block}_2(i)$ and $\text{pc}_1(i) = \text{pc}_2(i)$ and $\text{br}_1(i) = \text{br}_2(i)$ and $\text{com}_1(i) = \text{com}_2(i)$ and for all $x$, $\Gamma_i(x) = \Gamma_2(x)$. This implies that (1) if thread $i \notin \text{alive}(\text{pool}_1)$, then $\text{com}_1(i) = \text{term}_1(i)$ and so $i \notin \text{alive}(\text{pool}_2)$; and (2) if $i \in \text{alive}(\text{pool}_1)$ then $\text{pool}_1(i)$ is ready to execute the barrier step, and all pre-conditions are satisfied, and thus $\text{pool}_2(i)$ is also ready to execute the barrier.

Thus, since each thread eventually reaches the barrier or terminates, and no thread can take a step over the barrier until all non-terminated threads are ready to step over the barrier, we have that there is a finite sequence of global configurations $\text{gcnf}_2^0, \text{gcnf}_2^1, \ldots, \text{gcnf}_2^n$, such that $\text{gcnf}_2^n = \text{gcnf}_2$ and for all $i < n$ we have $\text{gcnf}_2^i \not\sim_\ell \text{gcnf}_2^{i+1}$ (without taking a synchronization step). By induction on this execution, we can show, by repeated application of Theorem 12 that for all $0 ≤ i ≤ n$ we have that $\text{gcnf}_1^i$ is $\ell$-equivalent to $\text{gcnf}_2^i$.

Thus, $\text{gcnf}_2^n$ can take a barrier step, since all preconditions for the barrier step are satisfied: $\text{gcnf}_2^n \not\sim_\ell \text{gcnf}_2^{n+1}$. We now need to show that $\text{gcnf}_2^{n+1}$ is $\ell$-equivalent to $\text{gcnf}_1^n$. To give names to the components of $\text{gcnf}_1^n$, $\text{gcnf}_2^n$, and $\text{gcnf}_2^{n+1}$ let

$$\text{gcnf}_1^n = \langle \text{pool}_1^n, \text{mem}_1^n, \tau_1^n, \text{gmon}_1^n \rangle$$

and

$$\text{gcnf}_2^n = \langle \text{pool}_2^n, \text{mem}_2^n, \tau_2^n, \text{gmon}_2^n \rangle$$

and

$$\text{gcnf}_2^{n+1} = \langle \text{pool}_2^{n+1}, \text{mem}_2^{n+1}, \tau_2^{n+1}, \text{gmon}_2^{n+1} \rangle$$

and

$$\text{pool}_1^n(i) = [\text{com}_1^n, \langle \Gamma_1^n, \text{ldmdst}_1^n, \text{pc}_1^n, \text{br}_1^n, \text{time}_1^n, \text{term}_1^n, \text{block}_1^n, \text{mdst}_1^n \rangle]$$

and

$$\text{pool}_2^n(i) = [\text{com}_2^n, \langle \Gamma_2^n, \text{ldmdst}_2^n, \text{pc}_2^n, \text{br}_2^n, \text{time}_2^n, \text{term}_2^n, \text{block}_2^n, \text{mdst}_2^n \rangle]$$

and

$$\text{pool}_2^{n+1}(i) = [\text{com}_2^{n+1}, \langle \Gamma_2^{n+1}, \text{ldmdst}_2^{n+1}, \text{pc}_2^{n+1}, \text{br}_2^{n+1}, \text{time}_2^{n+1}, \text{term}_2^{n+1}, \text{block}_2^{n+1}, \text{mdst}_2^{n+1} \rangle].$$

Note that we have $\text{alive}(\text{pool}_1^n) = \text{alive}(\text{pool}_2^{n+1})$, and that the annotation requests in both executions are identical (since annotation requests come from the commands, which are identical).

Note that since $\text{gcnf}_2^n$ is $\ell$-equivalent to $\text{gcnf}_1^n$ and the barrier step does not modify the trace or memory, or domain of the pool states, we have that $\tau_1^n(i) = \tau_2^{n+1}(i) \Downarrow \ell$ and $\text{pre}(\text{pool}_2^n) = \text{pre}(\text{pool}_1^n)$.

It just remains to show that $\forall i \in \text{pre}(\text{pool}_1^n) \cdot \langle \text{pool}_1^n(i), \text{mem}_1^n(i), \tau_1^n \rangle$ is $\ell$-equivalent to $\langle \text{pool}_2^{n+1}(i), \text{mem}_2^{n+1}, \tau_2^{n+1} \rangle$. Let $i \in \text{pre}(\text{pool}_1^n)$. We will apply Theorem 9. Note that if $\text{time}_2^{n+1} \cup (∪ \text{pc}_2^{n+1}) \cup \text{term}_2^{n+1} \cup \text{block}_2^{n+1} \not\sim_\ell \ell$ then from $\ell$-equivalence of
When one configuration takes a step, the other configuration can take zero or more steps to an
where Theorem 9 are satisfied. Either way, we can apply Theorem 9 and get the required result.

G. Finally, the soundness proof

Using the previously defined lemmas, we now define the key lemma, which says that given \( \ell \)-equivalent global configurations, when one configuration takes a step, the other configuration can take zero or more steps to an \( \ell \)-equivalent global configuration.

**Lemma 8.** Let \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) be \( \ell \)-equivalent global configurations, and let \( \sigma_1 \) and \( \sigma_2 \) be \( \ell \)-equivalent strategies. Assume that \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned.

If \( \text{gcnf}_1 \rightharpoonup_{\sigma_1} \text{gcnf}_1' \) then there exists \( \text{gcnf}_2' \) such that \( \text{gcnf}_2' \rightharpoonup_{\sigma_2} \text{gcnf}_2 \) and \( \text{gcnf}_1' \) and \( \text{gcnf}_2' \) are \( \ell \)-equivalent and \( \text{gcnf}_1 \) and \( \text{gcnf}_2 \) are well aligned.

**Proof:** Consider the possible derivations of \( \text{gcnf}_1 \rightharpoonup_{\sigma_1} \text{gcnf}_1' \). If the step was due to a barrier synchronization, then the result holds immediately by Theorem 13. Otherwise, if the step was an individual thread, then it was either a low step or a high step. If it was a low step, the result follows by Theorem 11, and if it was a high step, the result follows by Theorem 12.

We are now ready to prove Theorem 3.

**Proof of Theorem 3** Let \( \text{pool} \in \text{PSI} \), \( \sigma \in \Sigma \), and \( \langle \langle \text{pool}' , \text{mem}' , \tau' , \text{gmon}' \rangle \rangle \in \text{reach}_{\sigma}(\text{pool}) \). Let \( \ell \) be a security level.

We need to show that

\[
\kappa(\ell, \text{pool}, \tau') = \{ \sigma' \in \Sigma \mid \sigma =_{\ell} \sigma' \}
\]

where

\[
\kappa(\ell, \text{pool}, \tau') = \{ \sigma' \in \Sigma \mid \exists \langle \langle \text{pool}'' , \text{mem}'' , \tau'' , \text{gmon}'' \rangle \rangle \in \text{reach}_{\sigma'}(\text{pool}) : \tau'' \downarrow_{\ell} \tau = \tau' \downarrow_{\ell} \}
\]

Let \( \sigma' \) be a strategy such that \( \sigma =_{\ell} \sigma' \). We need to show that \( \sigma' \in \kappa(\ell, \text{pool}, \tau') \). That is, we need to show that there exists \( \langle \langle \text{pool}'' , \text{mem}'' , \tau'' , \text{gmon}'' \rangle \rangle \in \text{reach}_{\sigma'}(\text{pool}) \) such that \( \tau'' \downarrow_{\ell} \tau = \tau' \downarrow_{\ell} \).

Since \( \langle \langle \text{pool}' , \text{mem}' , \tau' , \text{gmon}' \rangle \rangle \in \text{reach}_{\sigma}(\text{pool}) \), there is an execution \( \langle \langle \text{pool}' , \text{mem}_{\text{init}} , \tau_{\text{init}} , \text{gmon}_{\text{init}} \rangle \rangle \rightharpoonup_{\sigma}^{\ast} \langle \langle \text{pool}' , \text{mem}' , \tau' , \text{gmon}' \rangle \rangle \).

First note that \( \langle \langle \text{pool}' , \text{mem}_{\text{init}} , \tau_{\text{init}} , \text{gmon}_{\text{init}} \rangle \rangle \) is \( \ell \)-equivalent to itself, and so by repeated applications of Lemma 8 we can construct a global configuration \( \langle \langle \text{pool}'' , \text{mem}'' , \tau'' , \text{gmon}'' \rangle \rangle \) such that

\[
\langle \langle \text{pool}' , \text{mem}_{\text{init}} , \tau_{\text{init}} , \text{gmon}_{\text{init}} \rangle \rangle \rightharpoonup_{\sigma}^{\ast} \langle \langle \text{pool}'' , \text{mem}'' , \tau'' , \text{gmon}'' \rangle \rangle
\]

and \( \langle \langle \text{pool}'' , \text{mem}'' , \tau'' , \text{gmon}'' \rangle \rangle \) is \( \ell \)-equivalent to \( \langle \langle \text{pool}' , \text{mem}' , \tau' , \text{gmon}' \rangle \rangle \). From the definition of \( \ell \)-equivalence for global configurations (Definition 4), we have \( \tau'' \downarrow_{\ell} \tau = \tau' \downarrow_{\ell} \) as required.
APPENDIX F
SOUNDNESS OF STATIC BOUNDS ORACLE

In this section we define the static bounds oracle, and sketch the proof of its soundness.

**Static-Skip**

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{skip} : \bot, \text{time}, \text{term, block}
\]

**Static-Assgin**

\[
\ell = \Gamma(e) \sqcup \text{time} \sqcup (\sqcup \overline{\text{pc}}) \sqcup \text{term} \sqcup \text{block} \quad \quad \ell'' = \Gamma(x \mapsto \text{time} \sqcup \ell)
\]

\[
\text{block}' = \begin{cases} 
\text{block} & \text{if } \ell \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{if } \ell'' \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{otherwise}
\end{cases}
\]

\[
\Gamma, \text{time}, \text{term, block} \vdash x \equiv e : \bot, \text{time}, \text{term, block}
\]

**Static-Input**

\[
\ell^* = \text{time} \sqcup (\sqcup \overline{\text{pc}}) \sqcup \text{term} \sqcup \text{block} \quad \quad \ell = \text{ch} \sqcup \ell^* \quad \quad \ell'' = \Gamma(x \mapsto \text{time} \sqcup \ell)
\]

\[
\text{block}' = \begin{cases} 
\text{block} & \text{if } \ell'' \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{if } \ell'' \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{otherwise}
\end{cases}
\]

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{input} \text{ch} \rightarrow x : \bot, \text{time}, \text{term, block}
\]

**Static-Output**

\[
\text{block}' = \begin{cases} 
\text{block} & \text{if } \ell'' \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{otherwise}
\end{cases}
\]

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{output} \text{ch} \rightarrow e : \bot, \text{time}, \text{term, block}
\]

**Static-Seq**

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{com}_1 : \ell_1, \overline{\text{pc}}_1, \text{br}_1, \ell_1', \Gamma_1, \text{time}, \text{term}, \text{block}
\]

\[
\Gamma_1, \text{time}, \text{term, block} \vdash \text{com}_2 : \ell_2, \overline{\text{pc}}_2, \text{br}_2, \ell_2', \Gamma_2, \text{time}, \text{term}, \text{block}
\]

**Static-Branch**

\[
\text{com}_i \in S 
\]

\[
\text{block}' = \begin{cases} 
\text{block} & \text{if } \ell'' \sqcup \overline{\text{pc}} \sqcup \text{term} \sqcup \text{time} \\
\text{block} & \text{otherwise}
\end{cases}
\]

\[
\ell = \Gamma(e) \sqcup \text{time} \sqcup \ell^* \quad \ell = \Gamma(e) \sqcup \text{time} \quad \ell \neq \bot
\]

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{if } e \text{ then } \text{com}_1 \text{ else } \text{com}_2 \text{ fi} : 
\]

\[
\ell, \overline{\text{pc}}, \text{br}, \ell', \Gamma', \text{time}, \text{term}, \text{block} \sqcup \text{com}_1 ; \text{com}_2 : \bot, \text{time}, \text{term}, \text{block}
\]

Fig. 10. Flow-sensitive (timing/control-sensitive) static analysis of the termination and typing environments bounds (part 1)

**Definition 7** (Surface syntax). Define set \( S \) to be the set of commands that do not contain commands join or more \( e \) do \( \text{com} \) od.

To define the static bounds oracle, we use a flow-sensitive type judgement

\[
\Gamma, \text{time}, \text{term, block} \vdash \text{com} : \ell', \Gamma', \text{time}, \text{term}, \text{block}
\]

This judgment is defined in Figures 10, 12. Using this judgment we define the static bounds oracle below.

**Definition 8** (Static bounds operator). Given program \( \text{com} \in S \), environment \( \Gamma \), mode state \( \text{lmdst} \), and levels \( \text{time}, \text{term} \) and \( \text{block} \), define partial function \( \text{SB}(\text{com}, \Gamma, \text{time}, \text{term}, \text{block}) \) to return a tuple of form \( (\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) \) as follows.

1) If \( \text{com} \) is if then \( \text{com}_1 \) else \( \text{com}_2 \) fi such that \( (\cup \overline{\text{pc}}) \cup \Gamma(e) \cup \text{time} = \bot \) then return tuple \( (\bot, \bot, \bot, \bot, \Gamma) \)
The static bounds operator is sound, in that it returns a conservative approximation for its arguments. We sketch the proof of
com : \in S \quad \forall x \in e. x \in \text{NonFloatVar} \quad \ell_0 = \Gamma(e) \sqcup \text{time} \quad \ell_{i+1} = \Gamma'_i(e) \sqcup \ell_i \quad \ell_i \neq \bot
\overline{br}_i = \overline{br}(time'_i, \text{term}'_i, \text{block}'_i, \Gamma'_i)
\Gamma'_i, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell'_i, \overline{pc}, \overline{br}, \Gamma''_i, \text{time}''_i, \text{term}''_i, \text{block}''_i \quad 0 \leq i \leq n
\Gamma'_0 = \Gamma, \quad \Gamma'_{i+1} = \Gamma'_i \sqcup \Gamma, \quad \Gamma'_{n+1} = \Gamma''
\text{time}''_i = \text{time}''_1 \sqcup \cdots \sqcup \text{time}''_i \quad \text{time}''_{n+1} = \text{time}''_n
\text{term}'_i = \text{term}''_i \sqcup \cdots \sqcup \text{term}''_i \quad \text{term}''_{n+1} = \text{term}''_n
\text{block}'_i = \left\{ \begin{array}{ll}
\text{block} \sqcup \text{block}'_1 \sqcup \cdots \sqcup \text{block}'_{n} & \text{if lmdst } \triangleright \text{mayread}(e)
\text{block} \sqcup \text{block}'_1 \sqcup \cdots \sqcup \text{block}'_{n} \sqcup \text{term} \sqcup \text{time} \sqcup (\overline{\text{pc}}) & \text{otherwise}
\end{array} \right.
\text{block}'_{n+1} = \text{block}'_n
\Gamma, \text{lmdst}, \overline{pc}, \overline{br}, \text{time}, \text{term}, \text{block} \vdash \text{while} \; e \; \text{do com od} : \ell_n, \overline{pc}, \overline{br}, \Gamma'_n, \text{time}'_n, \text{term}'_n, \text{block}'_n

Fig. 11. Flow-sensitive (timing/control-sensitive) static analysis of the termination and typing environments bounds (part 2)

\begin{align*}
\text{STATIC-STOP} \\
\Gamma, \text{lmdst}, \overline{pc}, \overline{br}, \text{time}, \text{term}, \text{block} & \vdash \text{stop} : \bot, \overline{pc}, \overline{br}, \Gamma, \text{time}, \text{term}, \text{block} \\
\text{STATIC-JOIN} \\
\Gamma'' = \lambda y \in \text{pre}(\Gamma) \cdot \Gamma(y) \sqcup \Gamma'(y) \\
\Gamma, \text{lmdst}, \overline{pc}, \overline{br}, \text{time}, \text{term}, \text{block} & \vdash \text{join} : \bot, \overline{pc}, \overline{br}, \Gamma''', \text{time}''\sqcup\text{time}'\sqcup\text{term}'\sqcup\text{block}'\sqcup\text{block}''
\text{STATIC-MORE} \\
\forall x \in e. x \in \text{NonFloatVar} \quad \ell_0 = \Gamma(e) \sqcup \text{time} \quad \ell_{i+1} = \Gamma'_i(e) \sqcup \ell_i \quad \ell_i \neq \bot \quad \overline{br}_i = \overline{br}(\text{time}_i, \text{term}_i, \text{block}_i, \Gamma_i)
\Gamma'_i, \text{lmdst}, \overline{pc}, \overline{br}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell'_i, \overline{pc}, \overline{br}, \Gamma''_i, \text{time}''_i, \text{term}''_i, \text{block}''_i \quad 0 \leq i \leq n
\Gamma'_0 = \Gamma, \quad \Gamma'_{i+1} = \Gamma'_i \sqcup \Gamma, \quad \Gamma'_{n+1} = \Gamma''
\text{time}''_i = \text{time}''_1 \sqcup \cdots \sqcup \text{time}''_i \quad \text{time}''_{n+1} = \text{time}''_n
\text{term}'_i = \text{term}''_i \sqcup \cdots \sqcup \text{term}''_i \quad \text{term}''_{n+1} = \text{term}''_n
\text{block}'_i = \left\{ \begin{array}{ll}
\text{block} \sqcup \text{block}'_1 \sqcup \cdots \sqcup \text{block}'_{n} & \text{if lmdst } \triangleright \text{mayread}(e)
\text{block} \sqcup \text{block}'_1 \sqcup \cdots \sqcup \text{block}'_{n} \sqcup \text{term} \sqcup \text{time} \sqcup (\overline{\text{pc}}) & \text{otherwise}
\end{array} \right.
\text{block}'_{n+1} = \text{block}'_n
\Gamma, \text{lmdst}, \overline{pc}, \overline{br}, \text{time}, \text{term}, \text{block} \vdash \text{more} \; e \; \text{do com od} : \bot, \overline{pc}'', \overline{br}, \Gamma'', \text{time}'', \text{term}'', \text{block}'

Fig. 12. Extension of flow-sensitive (timing/control-sensitive) static analysis to intermediate commands

2) If com is while e do com od such that \forall x \in e. x \in \text{NonFloatVar} and (\overline{\text{pc}}) \sqcup \Gamma(e) \sqcup \text{time} = \bot then return tuple (\bot, \bot, \bot, \bot, \Gamma).

3) If none of the above applies, then return (\ell_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}, \Gamma_{sb}) such that \Gamma, \text{lmdst}, \overline{pc}, e, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \overline{pc}, e, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}

Note that the static bounds oracle is a partial function: it may not return any result, which would cause the local monitor to block.

The static bounds oracle is sound, in that it returns a conservative approximation for its arguments. We sketch the proof of this claim below.

**Definition 9** (Well-formedness of typing environments with respect to mode states). Given \Gamma and lmdst, say that \Gamma is well-formed w.r.t. lmdst, written lmdst ok \Gamma, if

1) For all variables x, lmdst \triangleright \text{othersmightread}(x) \implies \Gamma(x) \subseteq \mathcal{L}(x)
2) For all variables $x$, $\text{lmdst} \triangleright \text{othersmightwrite}(x) \implies \mathcal{L}(x) \subseteq \Gamma(x)$

**Lemma 9** (Local properties of static bounds). Given $\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block}$ such that

1) $\text{term} \subseteq \text{time}$
2) $\text{lmdst} \text{ ok } \Gamma$
3) $\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$
then it holds that

1) $\text{term}_{sb} \subseteq \text{time}_{sb}$
2) $\text{lmdst} \text{ ok } \Gamma_{sb}$

**Proof:** Straightforward induction on the typing derivation.

**Lemma 10** (Monotonicity of branching environments). For all $\text{com}, \Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block}$, and $\vec{\text{br}}'$ it holds that

$\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \vec{\text{pr}}_{sb}, \vec{\text{br}}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$

then $\vec{\text{br}}_{sb}$ is a prefix of $\vec{\text{br}}$.

**Proof:** By induction on the typing derivation.

**Lemma 11** (Irrelevance of branching environments for surface commands). For all $\text{com} \in S$, such that, and all $\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block}$, and $\vec{\text{br}}'$ it holds that

$\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \vec{\text{pr}}_{sb}, \vec{\text{br}}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$

then

$\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}', \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \vec{\text{pr}}_{sb}, \vec{\text{br}}', \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$

**Proof:** By induction on the typing derivation using Lemma 10.

**Lemma 12** (Static bound typing rules are deterministic). For all $\text{com}, \Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block}$, and $\vec{\text{br}}'$ such that

$\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \vec{\text{pr}}_{sb}, \vec{\text{br}}', \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$

and

$\Gamma, \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}', \vec{\text{pr}}_{sb}', \vec{\text{br}}', \Gamma_{sb}', \text{time}_{sb}', \text{term}_{sb}', \text{block}_{sb}'$

it holds that

1) $\ell_{sb} = \ell_{sb}'$
2) $\vec{\text{pr}}_{sb} = \vec{\text{pr}}_{sb}'$
3) $\vec{\text{br}}_{sb} = \vec{\text{br}}_{sb}'$
4) $\Gamma_{sb} = \Gamma_{sb}'$
5) $\text{time}_{sb} = \text{time}_{sb}'$
6) $\text{term}_{sb} = \text{term}_{sb}'$
7) $\text{block}_{sb} = \text{block}_{sb}'$

**Proof:** By induction on the typing derivation.

**Lemma 13** (Monotonicity of typing for surface commands). Given a $\text{com} \in S$, $\Gamma, \text{lmdst}, \vec{\text{pr}}, \ell', \vec{\text{br}}, \text{time}, \text{term}, \text{block}$ and $\Gamma'$, $\text{lmdst}', \vec{\text{pr}}', \vec{\text{br}}$, $\text{time}', \text{term}'$, $\text{block}'$ such that

1) $\text{lmdst} \text{ ok } \Gamma$
2) $\text{lmdst} \text{ ok } \Gamma'$
3) $\Gamma, \text{lmdst}, \vec{\text{pr}}, \ell', \vec{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \vec{\text{pr}}_{sb}, \vec{\text{br}}', \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}$
4) $\text{time}' \subseteq \text{time}$
5) $\text{term}' \subseteq \text{term}$
6) $\text{block}' \subseteq \text{block}$
7) $\forall x \in \text{Var. } \Gamma'(x) \subseteq \Gamma(x)$
8) $\ell' \subseteq \ell$
then there are $\Gamma_{sb}', \text{lmdst}_{sb}', \vec{\text{pr}}_{sb}', \ell', \text{time}_{sb}', \text{term}_{sb}', \text{block}_{sb}'$ such that

1) $\Gamma', \text{lmdst}, \vec{\text{pr}}, \vec{\text{br}}, \text{time}', \text{term}', \text{block}' \vdash \text{com} : \ell_{sb}', \vec{\text{pr}}_{sb}', \vec{\text{br}}', \Gamma_{sb}', \text{time}_{sb}', \text{term}_{sb}', \text{block}_{sb}'$
2) $\text{time}_{sb}' \subseteq \text{time}_{sb}$
3) $\text{term}_{sb}' \subseteq \text{term}_{sb}$
4) \( \text{block}'_{sb} \subseteq \text{block}_{sb} \)
5) \( \forall x \in \text{Var}. \Gamma'_{sb}(x) \subseteq \Gamma_{sb}(x) \)

Proof: By induction on the typing derivation.

Lemma 14 (Preservation of static bounds). Given \( \text{pool} \in \text{PSt} \), \( i \in \text{pre}(\text{pool}) \), \( \sigma \in \Sigma \), \( \text{mem} \in \text{Mem} \), \( \tau \in \text{Tr} \), \( \text{gmon} \in \text{GMon} \), and \( \text{pool}' \in \text{PSt} \), \( \text{mem}' \in \text{Mem} \), \( \tau' \in \text{Tr} \), \( \text{gmon}' \in \text{GMon} \) and \( \Gamma' \), \( \text{Inds}' \), \( \text{br}' \), \( \text{time}' \), \( \text{term}' \), and \( \text{block}' \) such that

1) \( \text{pool}(i) = [\text{com}, \text{mc}, \text{mdst}] \) where \( \text{mc} = \langle \Gamma', \text{Inds}', \text{br}', \text{time}, \text{term}, \text{block} \rangle \)
2) \( \langle \text{pool}, \text{mem}, \tau, \text{gmon} \rangle \rightarrow_{\sigma} \langle \text{pool}', \text{mem}', \tau', \text{gmon}' \rangle \)
3) \( \text{pool}(i) = [\text{com}', \text{mc}', \text{mdst}'] \) where \( \text{mc}' = \langle \Gamma', \text{Inds}', \text{br}', \text{time}, \text{term}', \text{block}' \rangle \)
4) \( \Gamma', \text{Inds}', \text{br}', \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \text{br}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb} \)
5) \( \text{com} \) does not satisfy conditions (1) and (2) in Definition [8]

then

1) \( \Gamma'', \text{Inds}'', \text{br}'', \text{time}'', \text{term}'', \text{block}'' \vdash \text{com} : \ell'', \text{br}'', \text{time}'', \text{term}'', \text{block}'' \)
2) \( \text{time}''_{sb} = \text{time}_{sb} \)
3) \( \text{term}''_{sb} = \text{term}_{sb} \)
4) \( \text{block}''_{sb} = \text{block}_{sb} \)
5) \( \Gamma''_{sb} = \Gamma_{sb} \)

Proof: There are two possible inference rules that allow a global configuration to take a step. In one of them, all local configurations take a synchronization step. This does not satisfy assumptions of our Lemma, because barrier command cannot be typed according to the rules of Figures [10] and [11]. Consider the other inference rule that allows the global configuration to take a step. There are two sub-cases here. In one of the sub-cases, the step is taken by some thread \( j \neq i \), and therefore, \( \text{pool}(i) = \text{pool}(i) \); then we are done trivially. In the other sub-case, the \( i \)-th configuration takes a step:

\[
\langle \text{pool}(i), \text{mem}, \tau \rangle \rightarrow_{\sigma} \langle \text{pool}'(i), \text{mem}', \tau' \rangle
\]

Consider the local configuration step and an associated local monitor step.

\( \langle \text{com}, \text{mem}, \tau \rangle \rightarrow_{\sigma} \langle \text{com}', \text{mem}', \tau' \rangle \)

and

\[
\langle \Gamma, \text{Inds}, \text{br}, \text{time}, \text{term}, \text{block} \rangle \rightarrow_{\text{perm}} \langle \Gamma', \text{Inds}', \text{br}', \text{time}', \text{term}', \text{block}' \rangle
\]

We proceed by induction on the typing derivation

\[
\langle \Gamma, \text{Inds}, \text{br}, \text{time}, \text{term}, \text{block} \vdash \text{com} : \ell_{sb}, \text{br}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb} \rangle
\]

Case Static-Skip In this case, it must be that \( \text{com}' = \text{stop} \); the corresponding local monitor transition rule is (M-Skip); then we are done by inspecting rule (Static-Stop).

Case Static-Assign We have that \( \text{com}' = \text{stop} \). There are two possible local monitor transition rules: (M-Assign1) and (M-Assign2). In both cases, we observe that it holds that \( \text{time}' = \text{time}, \text{term}' = \text{term} \), and \( \text{block}' = \text{block} \). Moreover, the environment \( \Gamma'' \) is obtained by updating \( \Gamma \) in both of the rules exactly as it is updated in (Static-Assign), namely by using \( \ell = \Gamma' \langle e \rangle \sqcup \text{time} \sqcup (\sqcup \text{br} \sqcup \text{term} \sqcup \text{block} \sqcup \text{block}' \rangle \). Then we are done by inspecting the rule (Static-Stop).

Case Static-Input Similar to (Static-Assign).

Case Static-Output Similar to (Static-Assign), but we only have one matching monitor rule.

Case Static-Seq By induction hypothesis.

Case Static-Branch Assume we take branch \( i \) (\( i = 1, 2 \)). In this case \( \text{com}' = \text{com}_i \). Join. We have that \( \alpha = \text{b}(e, \text{com}_1, \text{com}_2) \) and the only matching monitor rule is (M-Branch). We have

\[
\langle \Gamma, \text{Inds}, \text{br}, \text{time}, \text{term}, \text{block} \rangle \rightarrow_{\text{perm}} \langle \Gamma, \text{Inds}, \ell', \text{br} \cdot (\text{time}'', \text{term}'', \text{block}'', \Gamma'' \rangle, \text{time}, \text{term}, \text{block} \rangle
\]

and

\[
\langle \Gamma, \text{Inds}, \text{br}, \text{time}, \text{term}, \text{block} \vdash \text{if } e \text{ then } \text{com}_1 \text{ else } \text{com}_2 \text{ fi : } \ell_{sb}, \text{br}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb} \rangle
\]

In this case, \( \ast \)-annotated environments are obtained from running the static oracle

\[
\text{SB}(e) \rightarrow \text{com}_1 \text{ else } \text{com}_2 \text{ fi, } \Gamma, \text{Inds}, \text{br}, \text{time}, \text{term}, \text{block}
\]

By Definition [9] we have three possible cases

1) \( (\sqcup \text{br}) \sqcup \Gamma'(e) \sqcup \text{time} = \bot \). This contradicts condition (3) of our Lemma.
2) Does not match our command.

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3) This is the only applicable case. We consider it below. In this case, by Definition 8 (Static bounds operator) it must be that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}}, e, \text{time}, \text{term}, \text{block} \vdash \text{if} \ e \ \text{then} \ \text{com}_1 \ \text{else} \ \text{com}_2 \ \text{fi} : \ell^*, \overline{\text{pc}}, e, \Gamma^*, \text{time}^*, \text{term}^*, \text{block}^* \]

By Lemma 11 (Irrelevance of branching environments for surface commands) we have that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}}, \overline{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{if} \ e \ \text{then} \ \text{com}_1 \ \text{else} \ \text{com}_2 \ \text{fi} : \ell^*, \overline{\text{pc}}, \overline{\text{br}}, \Gamma^*, \text{time}^*, \text{term}^*, \text{block}^* \]

By Lemma 12 (Static bound typing rules are deterministic) we have that

- \( \ell_{sb} = \ell^* \)
- \( \overline{\text{pc}}_{sb} = \overline{\text{pc}}^* \)
- \( \overline{\text{br}}_{sb} = \overline{\text{br}}^* \)
- \( \Gamma_{sb} = \Gamma^* \)
- \( \text{time}_{sb} = \text{time}^* \)
- \( \text{term}_{sb} = \text{term}^* \)
- \( \text{block}_{sb} = \text{block}^* \)

By examining the rule (Static-Branch) we have that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}_i, \text{term}_i, \text{block}_i, \Gamma_i), \text{time}, \text{term}, \text{block}' \vdash \text{com}_i : \ell_i, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}, \text{term}_i, \text{block}_i, \Gamma_i), \Gamma_i, \text{time}_i, \text{term}_i, \text{block}_i \]

where for all \( y \in \text{pre}(\Gamma) \) it holds that \( \Gamma_i(y) \subseteq \Gamma^* \), \( \text{time}_i \subseteq \text{time}^* \), \( \text{term}_i \subseteq \text{term}^* \), and \( \text{block}_i \subseteq \text{block}_i \). By Lemma 11 (Irrelevance of branching environments for surface commands) we have that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*), \text{time}, \text{term}, \text{block}' \vdash \text{com}_i : \ell_i, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*), \Gamma_i, \text{time}_i, \text{term}_i, \text{block}_i \]

From the typing rules (Static-Join) and (Static-Seq), we obtain that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*), \text{time}, \text{term}, \text{block}' \vdash \text{com}_i \text{ join } : \ell_i, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*), \Gamma_i, \text{time}_i, \text{term}_i, \text{block}_i \]

This concludes this case.

Case Static-Stop: Not applicable, because there are no semantic transitions from stop.

Case Static-Join: Immediate from the typing rule.

Case Static-Loop-Algorithmic: We have that \( \text{com} = \text{while } e \ \text{do} \ \text{c} \ \text{od} \), in this case \( \text{com}' = \text{more } e \ \text{do} \ \text{c} \ \text{od} \), and the only matching monitoring rule is (M-Enter). That is,

\[ \langle \Gamma, \text{lmdst}, \overline{\text{pc}}, \overline{\text{br}}, \text{time}, \text{term}, \text{block} \rangle \rightarrow_{\text{perm}}^{\text{enter}(e, \text{com})} \langle \Gamma, \text{lmdst}, \overline{\text{pc}} \cdot \ell, \overline{\text{br}} \cdot (\text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*), \text{time}, \text{term}, \text{block} \rangle \]

and

\[ \Gamma, \text{lmdst}, \overline{\text{pc}}, \overline{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{while} \ e \ \text{do} \ \text{c} \ \text{od} : \ell_{sb}, \overline{\text{pc}}_{sb}, \overline{\text{br}}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb} \]

where \( ^* \)-environments are obtained from running the static bounds oracle:

\[ (\ell^*, \text{time}^*, \text{term}^*, \text{block}^*, \Gamma^*) = \text{SB}(\text{while } e \ \text{do} \ \text{c} \ \text{od}, \Gamma, \text{lmdst}, \overline{\text{pc}}, \text{time}, \text{term}, \text{block}) \]

By Definition 8 (Static bounds operator) it must be that

\[ \Gamma, \text{lmdst}, \overline{\text{pc}}, \overline{\text{br}}, \text{time}, \text{term}, \text{block} \vdash \text{while} \ e \ \text{do} \ \text{c} \ \text{od} : \ell^*, \overline{\text{pc}}, \overline{\text{br}}, \Gamma^*, \text{time}^*, \text{term}^*, \text{block}^* \]

By Lemma 12 (Static bound typing rules are deterministic) we have that

- \( \ell_{sb} = \ell^* \)
- \( \overline{\text{pc}}_{sb} = \overline{\text{pc}}^* \)
- \( \overline{\text{br}}_{sb} = \overline{\text{br}}^* \)
- \( \Gamma_{sb} = \Gamma^* \)
- \( \text{time}_{sb} = \text{time}^* \)
- \( \text{term}_{sb} = \text{term}^* \)
- \( \text{block}_{sb} = \text{block}^* \)
Then we are done by matching the premises of the rule (Static-More) with the premises of the rule (Static-Loop).

**Case Static-More** We have that \(\text{com} = \text{more \, e \, do \, c \, od}\). There are two possible semantic and monitor transitions.

1) (M-More). In this case we continue executing the loop. In this case, \(\text{com}' = c; \text{more \, e \, do \, c \, od}\).

\[
\text{\begin{array}{c}
\{\Gamma, \text{lindst}, \bar{\text{pc}}, \bar{\text{br}}, \text{time}, \text{term}, \text{block}\} \rightarrow^\text{perm(e,com)} \{\Gamma, \text{lindst}, \bar{\text{pc}}, \bar{\text{br}}, \text{time}, \text{term}, \text{block}\}
\end{array}}
\]

and

\[
\text{\begin{array}{c}
\{\Gamma, \text{lindst}, \bar{\text{pc}}, \bar{\text{br}}, \text{time}, \text{term}, \text{block}\} \vdash \text{more \, e \, do \, c \, od : } \ell_{sb}, \bar{\text{pc}}_{sb}, \bar{\text{br}}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}
\end{array}}
\]

For clarity, we fold the reasoning about the timing, termination, and blocking environments below, into the reasoning about the typing environment. We have that there is an \(n\) such that

\[
\Gamma \vdash c : \ldots \Gamma_0 \ldots
\]

\[
\Gamma_0'' \cup \Gamma \vdash c : \ldots \Gamma_1'' \ldots
\]

\[
\Gamma_1'' \cup \Gamma \vdash c : \ldots \Gamma_2'' \ldots
\]

\[
\vdots
\]

\[
\Gamma_{n-1}'' \cup \Gamma \vdash c : \ldots \Gamma_n'' \ldots
\]

\[
\Gamma_n'' \cup \Gamma \vdash c : \ldots \Gamma_{n+1}'' \ldots
\]

where \(\Gamma_{n-1}'' \cup \Gamma = \Gamma_n'' \cup \Gamma\) and \(\Gamma_n'' \cup \Gamma \subseteq \Gamma_{sb}\), where \(\Gamma_{sb}\) is the typing environment on the top of the branching environment stack \(\bar{\text{br}}\). Note also that, by Lemma 13 (Monotonicity of typing for surface commands) it holds that \(\Gamma_i'' \subseteq \Gamma_{i+1}''\), for \(0 \leq i \leq n - 1\), and by Lemma 12 (Static bound typing rules are deterministic) \(\Gamma_n'' = \Gamma_{n+1}''\).

Consider command \(c; \text{more \, e \, do \, c \, od}\). Because \(c \in S\), it does not change the branching or pc environments. By Lemma 12 (Static bound typing rules are deterministic) we have that

\[
\{\Gamma, \text{lindst}, \bar{\text{pc}}, \bar{\text{br}}, \text{time}, \text{term}, \text{block}\} \vdash c : \ell_0''_{sb}, \bar{\text{pc}}_{sb}, \bar{\text{br}}_{sb}, \Gamma_{sb}, \text{time}_{sb}, \text{term}_{sb}, \text{block}_{sb}
\]

We want to show that

\[
\{\Gamma_0'' \cup \Gamma \vdash c : \ldots \Gamma_1'' \ldots \} \Rightarrow \{\Gamma_0'' \vdash c : \ldots \Delta_0'' \ldots \} \quad \text{such that} \quad \Delta_0'' \subseteq \Gamma_1''
\]

From this, using that \(\Gamma_0'' \subseteq \Gamma_1''\), we obtain that

\[
\{\Gamma_1'' \cup \Gamma \vdash c : \ldots \Gamma_2'' \ldots \} \Rightarrow \{\Gamma_0'' \cup \Gamma_0'' \vdash c : \ldots \Delta_1'' \ldots \} \quad \text{such that} \quad \Delta_1'' \subseteq \Gamma_2''
\]

Subsequently, for all \(i \geq 1\), we have

\[
\{\Gamma_i'' \cup \Gamma \vdash c : \ldots \Gamma_i'' \ldots \} \Rightarrow \{\Gamma_{i-1}'' \cup \Gamma_0'' \vdash c : \ldots \Delta_i'' \ldots \} \quad \text{such that} \quad \Delta_i'' \subseteq \Gamma_{i+1}''
\]

We observe that the sequence \(\Delta_0'', \Delta_1'', \ldots, \Delta_i'', \ldots\) is monotonically increasing, and is bound from above by \(\Gamma_n''\).

Then, there must exist \(k\) such that \(\Delta_k'' = \Delta_{k+1}''\), and correspondingly, it must be that

\[
\{\Gamma_0'' \vdash c : \ldots \Delta_0'' \ldots \}
\]

\[
\{\Delta_0'' \vdash \Gamma_0'' \vdash c : \ldots \Delta_1'' \ldots \}
\]

\[
\vdots
\]

\[
\{\Delta_{k-1}'' \vdash \Gamma_k'' \vdash c : \ldots \Delta_k'' \ldots \}
\]

\[
\{\Gamma_k'' \vdash c : \ldots \Delta_{k+1}'' \ldots \}
\]

where \(\Delta_{k-1}'' \vdash \Gamma_k'' = \Delta_k'' \cup \Gamma_k''\). Moreover, from \(\Delta_k'' \subseteq \Gamma_k''\) and \(\Gamma_k'' \subseteq \Gamma_n''\), we obtain that and \(\Delta_k'' \cup \Gamma_k'' \subseteq \Gamma_n''\). Hence, \(\Delta_k'' \cup \Gamma_k'' \subseteq \Gamma_{sb}\). The same reasoning follows for the timing, termination, and branching environments. This shows that we satisfy all the premises of (Static-More). Then we are done by (Static-Seq).

2) (M-Leave). In this case, we leave the loop, and \(\text{com}' = \text{stop}\). Then we are done by inspecting the rule (Static-Stop).

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Lemma 15. Given pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon, and pool' ∈ PST, mem' ∈ Mem, τ' ∈ Tr, gmon' ∈ GMon and Γ', lmdst', pč, br', time', term', and block' such that

- pool(i) = [com, mc, mdst] where mc = (Γ, lmdst, pč, br, time, term, block)
- fold(pool, mem, τ, gmon) →* fold(pool', mem', τ', gmon')
- pool(i) = [stop, (Γ', lmdst', pč', br', time', term', block'), mdst']
- Γ', lmdst, pč, br, time, term, block ⊢ com : ℓsb, Γsb, time sb, term sb, block sb

we have:

- Either ℓsb = ⊥ or ( time' ⊆ time sb and term' ⊆ term sb and block' ⊆ block sb and pre(Γ') = pre(Γ sb) and ∀x ∈ Var. Γ'(x) ⊆ Γ sb(x))

Proof: By induction on the number of local steps. Base cases include all the transitions where the final configuration is reached in one step, and the result of the lemma is obtained by analyzing the associated monitor transitions. For the inductive cases, we use Lemma 14 to relate the static bounds of the given command with the bounds obtained through the induction hypothesis.

Lemma 16. Given com = while e do com₁ od, pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon, and pool' ∈ PST, mem' ∈ Mem, τ' ∈ Tr, gmon' ∈ GMon, and Γ', lmdst', pč', br', time', term', and block' such that

- pool(i) = [while e do com₁ od, mc, mdst] where mc = (Γ, lmdst, pč, br, time, term, block)
- fold(pool, mem, τ, gmon) →* fold(pool', mem', τ', gmon')
- pool(i) = [more e do com₁ od, (Γ', lmdst', pč', br', time', term', block'), mdst']
- Γ', lmdst, pč, br, time, term, block ⊢ com : ℓsb, Γsb, time sb, term sb, block sb

we have all of the following:

- Γ'(e) ∈ time sb
- Either ℓsb = ⊥ or ( time' ⊆ time sb and term' ⊆ term sb and block' ⊆ block sb and pre(Γ') = pre(Γ sb) and ∀x ∈ Var. Γ'(x) ⊆ Γ sb(x))

Proof: By induction on the number of local steps using Lemma 14, similar to Lemma 15.

Lemma 17. Given pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon such that

- Γ, lmdst, pč, br, time, term, block ⊢ com : ℓsb, Γsb, time sb, term sb, block sb

we have:

- (∪pč) ⊆ term sb

Proof: By induction on the typing derivation.

Lemma 18. Given pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon such that

- Γ, lmdst, pč, br, time, term, block ⊢ com : ℓsb, Γsb, time sb, term sb, block sb

we have:

- (∪pč) ⊆ block sb

Proof: By induction on the typing derivation.

Lemma 19. Given pool ∈ PST, i ∈ pre(pool), σ ∈ Σ, mem ∈ Mem, τ ∈ Tr, gmon ∈ GMon such that

- Γ, lmdst, pč, br, time, term, block ⊢ com : ℓsb, Γsb, time sb, term sb, block sb

we have:

- ℓsb ⊆ term sb or ℓsb ⊆ block sb

Proof: We consider possible divergence and blocking behaviors separately.

Execution of com diverges. We proceed by induction on the typing derivation

$$\Gamma, \text{lmdst}, p\tilde{c}, br, time, term, block \vdash \text{com} : \ell sb, \Gamma sb, time sb, term sb, block sb$$
Cases (Static-Skip, Static-Assgin, Static-Input, Static-Output, Static-Seq, Static-Stop, Static-Join, Static-More). In all of these cases, $\ell_{sb} = \bot$, and we are done trivially.

Case (Static-Branch) Follows from Lemma 17, because $\ell_{sb}$ is pushed on the pc-stack when typing either branches.

Case (Static-Loop-Algorithmic) Immediate from the typing rule.

Execution of $com$ is blocked by the monitor. Similar to the previous case, using Lemma 18.

Theorem 14 (Soundness of static bounds function). Given program $com$, environment $\Gamma$, mode state $lmdst$, program counter stack $\overline{pc}$, and levels $time$, term and block, if $SB(com, \Gamma, lmdst, \overline{pc}, time, term, block) = (\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})$ then $(\ell_{sb}, time_{sb}, term_{sb}, block_{sb}, \Gamma_{sb})$ is a conservative approximation of the timing, termination, blocking, and floating behavior for $com$, $\Gamma$, lmdst, $\overline{pc}$, $\overline{br}$, $time$, $term$, and $block$.

Proof: We proceed by analyzing the cases in Definition 8.

1) $com$ is $if$ $e$ $then$ $com_1$ $else$ $com_2$ $fi$ such that $(\sqcup \overline{pc}) \sqcup \Gamma(e) \sqcup time = \bot$. We have that $\ell_{sb} = \bot$, $time_{sb} = \bot$, $term_{sb} = \bot$, $block_{sb} = \bot$, and $\Gamma_{sb} = \Gamma$.

We examine all of the requirements imposed by Definition 2. Requirement (1) is trivial. Requirements (2) and (3) follow by assumption. Requirements (4) - (6) are trivial. Requirement (7) follows from the assumption in the current case. Requirement (8) is not applicable.

2) $com$ is $while$ $e$ $do$ $com$ $od$ such that $\forall x \in e. x \in NonFloatVar$ and $(\sqcup \overline{pc}) \sqcup \Gamma(e) \sqcup time = \bot$.

Similar to the above, with the only difference that Requirement (7) is not applicable, and Requirement (8) follows from the assumption in the current case.

3) None of the above applies. We examine all of the requirements imposed by Definition 2.

- Requirements (1) – (3) follow from Definition 8 and Lemma 9.
- Requirements (4) and (8) follow from Lemma 15 and 16 respectively.
- Requirement (5) follows from Lemma 19.
- Requirement (6) follows from analysis of the rules in Figures 10-12 and observing that they contain no judgment for the barrier command.
- Requirement (7) is straightforward by analyzing rule (Static-Branch).

$\blacksquare$