Automatic Enforcement of Expressive Security Policies using Enclaves

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Accessibility
Automatic Enforcement of Expressive Security Policies using Enclaves

Anitha Gollamudi
and
Stephen Chong

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Computer Science Group
Harvard University
Cambridge, Massachusetts
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Anitha Gollamudi  Stephen Chong
Harvard University, Cambridge, MA, USA
agollamudi@g.harvard.edu  chong@seas.harvard.edu

Abstract
Hardware-based enclave protection mechanisms, such as Intel’s SGX, ARM’s TrustZone, and Apple’s Secure Enclave, can protect code and data from powerful low-level attackers. In this work, we use enclaves to enforce strong application-specific information security policies.

We present IMP, a novel calculus that captures the essence of SGX-like enclave mechanisms, and show that a security-type system for IMP can enforce expressive confidentiality policies (including erasure policies and delimited release policies) against powerful low-level attackers, including attackers that can arbitrarily corrupt non-enclave code, and, under some circumstances, corrupt enclave code.

We present a translation from an expressive security-typed calculus (that is not aware of enclaves) to IMP. The translation automatically places code and data into enclaves to enforce the security policies of the source program.

Categories and Subject Descriptors D.3.3 [Programming Languages]: Language Constructs and Features; D.4.6 [Operating Systems]: Security and Protection—Information flow controls

Keywords Enclave programs, information erasure, declas-sification, security-type system, information-flow control, language-based security.

1. Introduction
Language-based techniques for security can enforce expressive information security policies for applications. Enforceable policies include ensuring that application-level adversaries learn nothing about confidential information [29, 38], that some clearly specified confidential information may be released under controlled circumstances [31], and that sensitive information is correctly removed from the system at appropriate times [7,10]. However, these language-based guarantees may fail to hold in the presence of low-level attackers, such as attackers that observe execution at the level of operating-system or hardware abstractions, or attackers that can inject arbitrary code into a process.

Recent hardware-based enclave protection mechanisms (including Intel’s SGX [24], ARM’s TrustZone [4], and Apple’s Secure Enclave [2]) can protect code and data from low-level attacks, including compromised kernels. These new mechanisms present an opportunity to extend strong application-specific information security guarantees to hold against low-level attackers.

We take advantage of this opportunity: we present a language model that captures the essence of enclave protection mechanisms, and give a security-type system for this language that enforces strong non-interference-based information security guarantees [12,18], including delimited release [30] and information erasure [9]. Moreover, we provide a translation from a non-enclave source language that automatically infers which code and data to place in enclaves in order to enforce expressive security policies.

As an example of application-specific information security requirements, consider code that authenticates a user. The user provides a guess that is checked against the actual password. If the guess matches the password, the user is authenticated and the computation continues. After authentication, the guess is no longer needed, and the subsequent computation should in no way depend on the guess. This information security requirement can be expressed as an erasure policy [9] that requires restrictions on the use of sensitive information (i.e., the user’s guess) after certain conditions are satisfied (i.e., the user is successfully authenticated). Language-based techniques can ensure that these restrictions are respected by the subsequent computation (e.g., [7,23]).

However, these techniques typically enforce security against a language-level attacker that passively observes the program’s output or perhaps provides code that is subject to similar enforcement mechanisms as the program itself (e.g., [3,23]). The desired security guarantees may fail to hold in settings where an attacker has privileged access to a machine (such as in cloud services or on mobile devices) or an attacker is able to exploit vulnerabilities to observe more data than anticipated (such as in the Heartbleed attack) or inject arbitrary code into the program’s process (such as in buffer overrun attacks). In these cases, an attacker that compromises the system sometime after the user has authenticated may be able to learn the user’s password. For example, even though the program may not in any way use...
We present IMP$_2$. We show that a security-type system for enclaves. We make several contributions.

Enclave protection mechanisms can secure code and data against powerful attackers, including malicious code within the same process or a malicious operating system. Intel’s SGX extends the x86 instruction set with additional instructions that allow a contiguous region of memory within a process’s address space to be established as an enclave, and subsequently uses hardware-enforced access control to ensure that code outside of an enclave is unable to access data within an enclave. Moreover, execution may enter an enclave only via specified entry points. Memory within an enclave is encrypted before being paged out.

But leveraging enclaves to enforce application-specific information security guarantees is hard. Enclave mechanisms place the onus on the programmer to secure an application by effectively decoupling the security-critical parts of the application from the non-critical and/or untrusted parts of the application. Hardening an application to carefully isolate the dependencies requires non-trivial effort [32, 35].

In this paper we consider the automatic enforcement of application-specific information security policies using enclaves. We make several contributions.

1. We present IMPE, a novel calculus that captures the essence of SGX-like enclave mechanisms (Section 2).
2. We show that a security-type system for IMPE can enforce expressive confidentiality policies (including erasure policies and delimited release policies) against several attackers, including attackers that can arbitrarily corrupt non-enclave code, and, under certain circumstances, corrupt enclave code (Sections 3 and 4).
3. We present a translation from a non-enclave source language to IMPE (Sections 5 and 6). The programmer can focus on the correct handling of information in the source language, and the translation will automatically infer appropriate placement of code and data into enclaves to ensure security guarantees against powerful low-level attackers. The translation can be configured to optimize various criteria, including reducing the size of the trusted computing base, reducing the runtime performance impact of using enclave mechanisms, or removing erased data as soon as possible.

In addition, we validate the translation and the expressiveness of IMPE by implementing several simple models of applications with application-specific security guarantees (Section 8).

2. IMPE: A Calculus for Enclaves

We present IMPE, an imperative higher-order calculus that captures the key features of enclaves and moreover supports the specification of information security policies, including policies for information erasure.

2.1 Security Levels and Policies

We use a set of security levels $\mathcal{L} = \{L, H, T\}$ to express confidentiality restrictions on information. Security level $L$ ("low security") is for public information that anyone, including an attacker, is permitted to learn. Security level $H$ ("high security") is for confidential information that only trusted entities are permitted to learn. Security level $T$ is for information so confidential that no-one is permitted to learn it. Ideally, the system never contains information with security level $T$. Let partial order $\sqsubseteq$ be the smallest reflexive and transitive relation such that $L \sqsubseteq H$ and $H \sqsubseteq T$. Intuitively, if $\ell_1 \sqsubseteq \ell_2$, then information with security level $\ell_2$ is at least as confidential as information with security level $\ell_1$. Security levels ordered by $\sqsubseteq$ form a lattice.

The security level to enforce on information may change over time. In this paper, we focus on information erasure: the requirement that when a specific condition is met, information needs to become more confidential.

Security policies describe how the security level of information must change over time. A security level policy $\ell$ simply means that information must be handled with security level $\ell$ at all times. An erasure policy $\ell_1 \xrightarrow{\text{cnd}} \ell_2$ means that initially information can be handled according to security level $\ell_1$. However, when condition $\text{cnd}$ is met, the information must be handled according to security level $\ell_2$, where $\ell_1 \sqsubseteq \ell_2$. Conditions are used to express when information must be "erased" or made more restrictive. In general, conditions for erasure can be arbitrary state predicates [9]. However, we encode conditions using mutable memory locations: a condition $\text{cnd}$ is represented by a single memory location, and the condition is regarded as satisfied exactly when the location contains a non-zero value. The program is responsible for setting the condition location to a non-zero value to correctly reflect the intended meaning of the condition. Once set (i.e., assigned a non-zero value), we do not allow a condition to be unset. This approach is sufficiently expressive and simplifies specification and reasoning about erasure policies [7].

We write $P$ to denote the set of policies, and use metavariables $p, q$ to range over policies. We refer to any information labeled with a policy more restrictive than $L$ as confidential information.

Consider a program that authenticates a password. Let password be a memory location that stores password input from a user. Once authentication succeeds, it is desirable to erase password entirely from the memory. If end is a condition that indicates whether the authentication session has ended, a suitable policy for password can be $L \xrightarrow{\text{end}} \ell \xrightarrow{\text{end}} \ell \xrightarrow{\text{end}} \ell \xrightarrow{\text{end}} \ell \xrightarrow{\text{end}} T$. The policy says that the confidentiality level of password is initially $L$, and once end is set, it must be $T$. The user’s guess in the subsequent computation, the bits representing the guess may still be present in physical or virtual memory, and accessible to a low-level attacker [20, 21].
e ::= n | x | e₁ + e₂ | l | *e | isunset(cnd) | λµ.c
v ::= λµ.c | n | l
c ::= skip | x := e | x := decclassify(e) | e₁ ← e₂
    | output e to ℓ | call(e) | set(cnd) | enclave(i,c)
    | kill(i) | e₁; ...; eₙ | if e then e₁ else e₂ | while e do c
l ∈ Loc  cnd ∈ Cond  Cond ⊆ Loc
x ∈ Vars  i, n ∈ N
µ ∈ Mode = {N, E₁, E₂, ...}

Figure 1. IMPE Syntax

2.2 Syntax

IMPE is a simple imperative language. However, it includes first-class locations and functions, output commands, and models enclaves. An enclave consists of code and memory locations. Memory locations within an enclave can be accessed only by that enclave’s code. Control can be transferred to code inside an enclave only through a predefined set of entry points. Thus, data stored inside an enclave’s memory locations is protected from non-enclave code (and also from code in other enclaves). In IMPE, enclaves provide a simple yet expressive model of architectural features—such as Intel’s SGX [24]—that can provide strong isolation guarantees for code and data from other code within the same process or machine.

We allow an arbitrary number of enclaves, indexed with natural numbers. We use modes to indicate which enclave code or data exists in, or whether it is outside of any enclave. Specifically, we use metavariable µ to range over the set Mode = {N, E₁, E₂, ...}, where Eᵢ indicates the i-th enclave and N indicates non-enclave (or “normal”) mode.

Figure 1 shows the syntax of IMPE. Expressions e include integers n, variables x, and memory locations l. All variables have global scope. Variables are analogous to registers: they are mutable locations, but are not first-class values. By contrast, memory locations are first-class, and can be passed as values. Conditions Cond are a subset of the memory locations and cnd ranges over conditions. We write Loc for the set of memory locations.

Operator + ranges over arbitrary (total) binary operations over integers. Dereference *e evaluates e to a memory location and evaluates to the contents of that location.

Expression isunset(cnd) tests whether condition cnd has been set, and evaluates to 1 if it is not set, 0 otherwise. Although this expression is semantically equivalent to *cnd ≠ 0, our type system gains precision through the use of isunset(cnd).

Expression λµ.c is a first-class function. It can be thought of as a code pointer to command c. Arguments to the function are given via variables and memory locations, as are any values returned by the function. Annotation µ indicates the mode in which the function is defined. It can be thought of as indicating whether the code pointer is to an enclave or non-enclave region of memory. The annotation is used to restrict how functions can be invoked, to ensure that non-enclave code cannot enter an enclave by invoking a function that resides in the enclave.

Values v in IMPE include integers, memory locations (including conditions), and first-class functions.

Commands in IMPE include standard imperative commands (skip, x := e, if e then e₁ else e₂, and while e do c). We assume sequences c₁; ...; cₙ are flattened (i.e., that none of c₁; ...; cₙ are sequence commands), and for convenience assume that all sub-commands are sequences (possibly of length 1). Indirect assignment e₁ ← e₂ evaluates e₁ to a memory location, and updates the contents of that location with the result of e₂. We further require that e₁ does not evaluate to a condition. Command set(cnd) updates the contents of cnd to 1. Conditions can be updated only with a set command.

Command output e to ℓ evaluates e to a value and outputs it to channel ℓ. Output commands model observations by trusted and untrusted entities. We restrict ℓ to either L or H. Intuitively, an output to channel L may be observed by an untrusted entity, such as an attacker, whereas output to channel H may be observed only by trusted entities.

Command x := decclassify(e) is semantically equivalent to assignment x := e, but indicates a declassification, which is relevant for both our semantic security conditions (Section 3.2) and type system (Section 4). To simplify our semantic security condition, we require that expression e does not contain any variables (although it may contain memory locations). Command call(e) evaluates e to a function, and invokes the function.

Command enclave(i,c) defines an entry point for the enclave Eᵢ. That is, command c is code that resides inside enclave Eᵢ, and non-enclave code is permitted to execute c. We require that c does not contain any subcommands of the form enclave(i′, c′), i.e., enclave commands cannot be nested, regardless of whether for the same enclave or a different enclave. Commands not lexically nested in an enclave(i, ...) are non-enclave code.

We allow an enclave to have multiple entry points. That is, a program may contain multiple commands of the form enclave(i, c) with the same enclave identifier i.

Command kill(i) tears down enclave Eᵢ. Once killed, an enclave cannot be used: its memory locations can not be accessed, nor can its code be executed.

2.3 Operational Semantics

A configuration (c, r, m, K) describes the current state of the system. Command c is the rest of the program to execute. Register file r maps variables to values, and memory m maps locations to values. Kill set K is the set of enclaves that have been killed so far in the execution.
As a program executes, it performs observable actions (i.e., outputting values on channels) and non-observable security-relevant actions (such as declassifications). We refer to these actions as events and use metavariable $\alpha$ to range over events. A trace $t = \alpha_1 \ldots \alpha_n$ is a finite sequence of events. We write $e$ for the empty trace, $|t|$ for the length of trace $t$, and $t_1 \cdot t_2$ for the concatenation of traces $t_1$ and $t_2$.

We define the semantics of IMP with a large step operational semantics. The judgment for the evaluation of commands has the following form.

$$
\mu \vdash \delta \langle c, r, m, K \rangle \Downarrow v' ; m' ; K' \triangleright t'
$$

The judgment is parameterized by mode $\mu$, which indicates whether command $c$ is executing in normal mode ($\mu = N$) or in an enclave ($\mu = E_i$). Initially, program execution always starts in normal mode (since all enclave code is inside enclave$(i, \ldots)$ commands).

The judgment is also parameterized by function $\delta : \text{Loc} \rightarrow \text{Mode}$ which indicates for each memory location which enclave, if any, it belongs to. If $\delta(l) = E_i$ then location $l$ is in enclave $E_i$, and if $\delta(l) = N$ then $l$ is not in an enclave.

Evaluation of commands makes use of an additional judgment to evaluate expressions: $\mu \vdash \delta \langle e, r, m, K \rangle \Downarrow v$. This judgment means that, given register file $r$, memory $m$, and kill set $K$, expression $e$ evaluates in mode $\mu$ to value $v$. Expression evaluation does not modify the register file, memory, or the kill set.

Figure 2 presents the inference rules for expression evaluation. The rules are straightforward except for $\text{DEREF}$. Integers, locations, and functions are already values.

Rule $\text{DEREF}$ evaluates expression $e$ to a memory location $l$ and reads the contents of $l$. The premise $\delta(l) \in \{N, \mu\} \setminus K$ states that in normal mode (i.e., $\mu = N$), only normal locations can be read; in enclave mode $E_i$ (i.e., $\mu = E_i$), both normal and enclave $E_i$ locations may be read (i.e., $\delta(l) \in \{N, E_i\}$). Locations from a different enclave cannot be read, and if an enclave has been killed ($E_i \in K$), then no locations in that enclave can be read.

Rule $\text{ISUNSET}$ returns 1 if the contents of condition $cnd$ is 0 (i.e., if $cnd$ is not set), otherwise it evaluates to 0.

Rule $\text{FUNCTION}$ requires that the mode annotation $\mu$ on function $\lambda^\mu.c$ equals the mode of the expression evaluation.

Figure 3 shows the inference rules for command execution. Many rules have the premise $\mu \not\in K$, which collectively ensure that code in killed enclaves can not be executed. Rules $\text{SKIP}$, $\text{ASSIGN}$, $\text{SEQ}$, $\text{IF-ELSE}$, $\text{WHILE-T}$, and $\text{WHILE-F}$ are standard. We write $r[x \mapsto v]$ for the register file that maps $x$ to $v$ but otherwise is the same as $r$. Similarly, memory $m[l \mapsto v]$ maps $l$ to $v$ but otherwise is the same as $m$.

The command $\text{call}(e)$ evaluates expression $e$ to a function and invokes it. The modes of callee and caller should match. This ensures that function calls cannot be used to amplify privilege, and the only way execution can transition modes is via an enclave$(i, c)$ command.

Rule $\text{UPDATE}$ updates a memory location, and like $\text{DEREF}$, ensures that an enclave’s memory locations can be accessed only by code within the enclave. This rule is used only for non-condition locations. Rule $\text{SETCND}$ is used to set conditions. Since this is a security relevant action, $\text{SETCND}$ produces event Mem($m'$) in the trace where $m'$ is the new memory location. These events are used in the definition of the semantic security conditions.

Rule $\text{DECLASSIFY}$ declassifies expression $e$ and assigns the result to variable $x$. Operationally, it is similar to $\text{ASSIGN}$ but uses predicate hashNoVars($e$) to enforce the syntactic restriction that expression $e$ contains no variables: it may contain expressions built from values and memory locations (including conditions). Declassifications are security relevant events, and so a declassification event Decl$(e, m)$ is emitted to the trace. Rule $\text{OUTPUT}$ evaluates the expression $e$ to a value $v$, outputs $v$ on channel $\ell$, and adds events Mem$(m)$ and Out$(\ell, v)$ to the trace.

Rule $\text{ENCLAVE}$ executes enclave code. Note that enclaves can be entered only from normal mode (i.e., mode $\mu$ in the conclusion must be $N$). This reflects the operation of Intel SGX-like mechanisms: execution of enclave code occurs only by non-enclave code jumping to a well-defined enclave entry point; execution of enclave code ends only by exiting the enclave, not by calling back to non-enclave code; code in one enclave can not directly execute code in another enclave.
The code uses two locations, password and guess, containing the password and user’s input respectively. Assume $\delta(password) = E_1$ and $\delta(guess) = N$, i.e., password belongs to enclave $E_1$ and guess is not in an enclave. The program enters enclave $E_1$, checks if the password matches the guess by dereferencing the corresponding locations, sets variable status to the result, and exits the enclave. Variable status is then output on channel $L$. Note that dereferencing password would fail if done outside enclave $E_1$.

### 3. Attacker Model and Security

In this section we define security for the IMPE language for a variety of attackers. We consider a passive attacker that can only observe outputs on certain channels, an active attacker that can arbitrarily corrupt non-enclave computation, and an active attacker that can, under certain conditions, corrupt computation both outside and inside enclaves.

The definition of security is that at all times, an attacker knows no more than what the attacker is permitted to know. What the attacker is permitted to know is determined by the security policies on information, which conditions are set when, and what declassifications the program performs. We model active attackers by allowing additional transitions in the operational semantics of IMPE. Thus, the definition of security is parameterized on variants of the operational semantics of IMPE.

![Figure 3. Large-step semantics for select IMPE commands](image-url)
We assume the only source of confidential information is the initial memory. A security specification $\gamma$ maps locations to policies and indicates the policy to enforce on information in an initial memory. For example, if $\gamma(l) = \ell_1 \land \ell_2$, and $m$ is the memory from which we start an execution, then we should enforce erasure policy $\ell_1 \land \ell_2$ on the data in $m(l)$. We say that a security specification $\gamma$ is well-formed if $\forall l \in \text{Loc}. \gamma(l) \neq \top$ (since security level $\top$ is for information so confidential that it should not be on the machine).

3.1 Attacker Knowledge

We associate an attacker with a security level $\ell \in \mathcal{L}$ and assume the attacker is able to observe outputs on any channel $\ell'$ such that $\ell' \subseteq \ell$. Given trace $t$, $[t]_{\ell}$ is the output events that an attacker at level $\ell$ can observe.

$$[t]_{\ell} = \begin{cases} \text{Out}(\ell', v) \cdot [t']_{\ell} & \text{if } t = \text{Out}(\ell', v) \cdot t' \text{ and } \ell' \subseteq \ell \\ [t']_{\ell} & \text{if } t = \text{Out}(\ell', v) \cdot t' \text{ and } \ell' \not\subseteq \ell \\ \ell & \text{if } t = \alpha \cdot t' \land \alpha \neq \text{Out}(\ell', v) \end{cases}$$

Given an execution of program $c$, an attacker at level $\ell$ observes some portion of the execution (i.e., some subsequence $t_{\text{obs}}$ of the trace produced during execution). The knowledge of the attacker is the set of initial memories for which execution of $c$ could produce a trace $t$ such that some subsequence of $t$ looks the same to the attacker as $t_{\text{obs}}$. That is, an attacker’s knowledge is the set of initial memories that the attacker believes are possible. Thus, the smaller the attacker’s knowledge, the more precise is the attacker’s knowledge.

We base our definition of attacker knowledge on that of Askarov et al. [7] by parametrizing it on the large-step semantics. That is, we will instantiate $\downarrow_{\text{kind}}$ with different large-step semantics that represent different attackers. We assume that all initial configurations use a register file $r_{\text{init}}$ that maps all variables to zero.

**Definition 1** (Attacker knowledge). Given program $c$, security level $\ell$, large-step semantics $\downarrow_{\text{kind}}$, and trace $t_{\text{obs}}$, attacker knowledge is defined as:

$$k^{\downarrow_{\text{kind}}}_{\ell}(c, t_{\text{obs}}) = \{ m \mid N \vdash (c, r_{\text{init}}, m, \emptyset) \downarrow_{\text{kind}} r'; m'; K' \triangleright t \wedge \exists t_0, t_1, t_2. t = t_0 \cdot t_1 \cdot t_2 \wedge [t_{\text{obs}}]_{\ell} = [t_1]_{\ell} \}$$

Consider the password authentication example from Section 2.3. Let $c$ be the program, $m_0$ be the initial memory and $t_{\text{obs}} = \text{Mem}(m_0) \cdot \text{Out}(L, 1)$ be the trace produced by the program executed with semantics $\downarrow$. The knowledge of a passive attacker at security level $L$ is the set of all initial memories such that the contents of locations password and guess are equal. More formally, $k_L^{\downarrow}(c, t_{\text{obs}}) = \{ m' \mid m'(\text{password}) = m'(\text{guess}) \}$.

3.2 Security

The intuition for knowledge-based security conditions [5,7] is that an attacker should know only what it is permitted to know. We thus define what an attacker is permitted to know.

We are concerned with attackers that may observe only a portion of a program’s execution. Thus, an attacker at level $\ell$ that starts observing the execution after condition $\text{cnd}$ has been set should in general not be able to learn anything about information with erasure policy $\ell_1 \land \ell_2$ where $\ell_2 \not\subseteq \ell$. However, an attacker is permitted to learn information that has already been declassified, including declassifications that occurred before the attacker started observing the execution.

**Permitted knowledge via erasure policies** Whether an attacker at level $\ell$ is permitted to observe information with policy $p$ depends on which conditions have been set. Let $U \subseteq \text{Cond}$ be the currently unset conditions. We write $\text{cur}(p, U)$ for the security level that should currently be enforced on information with policy $p$. If $p$ is an erasure policy $\ell_1 \land \ell_2$, then we should enforce security level $\ell_1$ if $\text{cnd} \in U$ and enforce $\ell_2$ if $\text{cnd} \notin U$. Formally:

$$\text{cur}(p, U) = \begin{cases} \ell_1 & \text{if } p = \ell_1 \\ \ell_2 & \text{if } p = \ell_1 \land \ell_2 \text{ and } \text{cnd} \in U \\ \ell_2 & \text{if } p = \ell_1 \land \ell_2 \text{ and } \text{cnd} \notin U \end{cases}$$

Based on the current security level to enforce on information, we define equivalence classes of initial memories that an attacker at level $\ell$ should not be allowed to distinguish. Intuitively, if initial memories $m$ and $m'$ are identical at every location $l$ for which the current security level permits the attacker to learn information (i.e., $\text{cur}(\gamma(l), U) \subseteq \ell$), then the attacker should not be allowed to distinguish $m$ and $m'$.

**Definition 2** (Indistinguishable Memories). Given memory $m$, security specification $\gamma$, unset conditions $U$, and security level $\ell$, we define $\text{ind}_{\ell}(m, \gamma, U)$ as

$$\{ m' \mid \forall l \in \text{Loc}. \text{cur}(\gamma(l), U) \subseteq \ell \implies m(l) = m'(l) \}$$

Given an execution from initial memory $m_0$ where an attacker at level $\ell$ starts observing the execution when $U$ are the unset conditions, then the attacker should not learn whether the initial memory for the execution was $m_0$ or some memory in $\text{ind}_{\ell}(m_0, \gamma, U)$. That is, the attacker’s knowledge should be a superset of $\text{ind}_{\ell}(m_0, \gamma, U)$.

**Permitted knowledge via escape hatches** Declassifications permit an attacker to learn more information. Following Sabelfeld and Myers [30], we use escape hatches to characterize what information declassification commands $x := \text{declassify}(e)$ reveal. An escape hatch is a computation over confidential information such that attackers are permitted to learn the result of the computation. In our setting, an escape hatch is an expression $e$ evaluated against the initial memory. Recall that confidential information is input
to a program only via the initial memory. Thus, by evaluating escape hatch expression $e$ against the initial memory, $e$ describes a computation over confidential inputs that is permitted to be declassified.

We connect declassification events $\text{Decl}(e, m)$ (where $m$ is the current memory at the time of declassification, and expression $e$ contains only operations over constants and memory locations) to escape hatches by requiring that the evaluation of $e$ using $m$ produces the same value as the evaluation of $e$ using the initial memory. If so, the attacker is permitted to learn the result of evaluating $e$ for initial memory $m$.

We define $\text{Decl}(e, m)$ as the set of all initial memories if expression $e$ evaluates to different values in $m$ and $m_0$ (i.e., the attacker should not learn any information from the declassification), and otherwise is equal to all initial memories $m'$ such that $e$ evaluates to the same value in $m'$ as it does in $m_0$ (i.e., the attacker is permitted to learn the result of evaluating $e$).

Definition 3 (Escape-hatch indistinguishability). Given initial memory $m_0$, current memory $m$, semantics $\psi_{\text{kind}}$ and escape hatch $e$, we define $\text{Esc}_{\text{kind}}(m_0, m, e)$ as

\[
\{ m' \mid \exists \mu. \mu \vdash_\delta \langle e, r_{\text{init}}, m_0, \emptyset \rangle \psi_{\text{kind}} \mu \land \mu \vdash_\delta \langle e, r_{\text{init}}, m, \emptyset \rangle \psi_{\text{kind}} \mu \implies \mu \vdash_\delta \langle e, r_{\text{init}}, m', \emptyset \rangle \psi_{\text{kind}} \mu \}\]

Given semantics $\psi_{\text{kind}}$, declassification event $\text{Decl}(e, m)$, and initial memory $m_0$, $\text{Esc}_{\text{kind}}(m_0, m, e)$ is equal to the set of all initial memories if expression $e$ evaluates to different values in $m$ and $m_0$ (i.e., the attacker should not learn any information from the declassification), and otherwise is equal to all initial memories $m'$ such that $e$ evaluates to the same value in $m'$ as it does in $m_0$ (i.e., the attacker is permitted to learn the result of evaluating $e$).

Security definition. We define $[t]_{\text{mem}} = \{ m \mid \text{Mem}(m) \in t \}$ to be the set of memory events that occur in trace $t$ and $[t]_{\text{esc}} = \{(e, m) \mid \text{Decl}(e, m) \in t \}$ to be the set of tuples corresponding to the declassification events in trace $t$.

Suppose we have an execution from initial memory $m_0$ with specification $\gamma$ that produces trace $t \cdot t_{\text{obs}} \cdot t''$, where an attacker at level $\ell$ observes $t_{\text{obs}}$. Then the attacker is permitted to learn any information that a memory $m'' \in [t_{\text{obs}}]_{\text{mem}}$ permits. That is, the intersection of the sets $\text{Esc}_{\text{kind}}(m_0, m', e')$ for $(e', m') \in [t \cdot t_{\text{obs}}]_{\text{esc}}$ describes what information the attacker is permitted to know based on the current security levels of information.

Moreover, the attacker is allowed to learn declassified information. The intersection of sets $\text{Esc}_{\text{kind}}(m_0, m', e')$ for $(e', m') \in [t \cdot t_{\text{obs}}]_{\text{esc}}$ describes what information the attacker is permitted to know based on declassifications that occurred before or during the attacker observation.

A program is secure if the attacker’s knowledge is indeed no more precise than the information the attacker is permitted to know. Definition 4 captures this intuition.

Definition 4 (Security). Program $c$ is secure at security level $\ell$ for security specification $\gamma$ and large-step semantics $\psi_{\text{kind}}$ if for all initial memories $m_0$ and all executions

\[
N \vdash_\delta \langle c, r_{\text{init}}, m_0, \emptyset \rangle \psi_{\text{kind}} \gamma; m; K \triangleright t \cdot t_{\text{obs}} \cdot t''
\]

where $t_{\text{obs}} = \text{Mem}(m'') \cdot t''$ for some memory $m''$ and trace $t''$.

Note that the definition quantifies over all possible observations $t_{\text{obs}}$. The definition requires that the first event in the observed trace $t_{\text{obs}}$ is a memory event to ensure we know the current security level to enforce on information as at the start of the attacker’s observation. This is without loss of generality, since every output event is immediately preceded by a memory event (see rule $\text{OUTPUT}$ in Figure 3).

For example, let $c$ be the password authentication program modified to set condition end on enclave exit.

\[
\text{enclave}(1, \text{status} := +\text{password} = +\text{guess}); \text{set}(\text{end}); \text{output status} \rightarrow L
\]

The program is insecure for the specification $\gamma$, where $\gamma(\text{guess}) = L \land \nrightarrow \top$ and $\gamma(\text{password}) = H$. Intuitively, for initial memory $m_0$ and $t_{\text{obs}} = \text{Mem}(m_0[\text{end} \rightarrow 1]) \cdot \text{Out}(L, 1)$ produced by execution with semantics $\psi$, then the lower bound $\text{ind}_L(m_0, \gamma, \emptyset)$ on the knowledge of an attacker at security level $L$ is the set of all memories. However, the attacker learns that password and guess are equal.

Suppose we now modify the program to include declassification:

\[
\text{enclave}(1, \text{status} := \text{declassify}(+\text{password} = +\text{guess}); \ldots
\]

The declassification event induces a new lower bound: $\{ m' \mid m'(\text{password}) = m'(\text{guess}) \}$ which is same as the attacker’s knowledge. The program is now secure for an attacker at security level $L$.

3.3 Attackers

A passive attacker simply observes the execution of program and attempts to learn information about confidential input. By contrast, an active attacker can modify or influence the execution of a program. Active attackers represent many malicious behaviors, including attacks that can modify execution arbitrarily (e.g., by gaining control of the program counter or overwriting code) or modify some set of memory locations (e.g., by buffer overflows or by providing malicious input to a program).
We consider three attackers: (1) A passive attacker that can only observe output on the L channel; (2) A non-enclave active attacker that can observe output on the L channel and arbitrarily modify non-enclave code; and (3) An enclave active attacker that can observe output on the L and H channels, and can arbitrarily modify (enclave and non-enclave) code but only under certain conditions.

We use different operational semantics to represent the different attackers. The passive attacker corresponds to the semantics $\llbracket$ (Figure 3). That is, programs execute as written, and the attacker passively observes output. We define two new semantics to capture the abilities of the active attackers.

**Non-enclave active attacker** Relation $\llbracket N$-chaos allows the attacker to arbitrarily change non-enclave code. Inference rules for judgment $\mu \vdash_{\beta} \langle c', r, m, K \rangle \llbracket N$-chaos $r'; m'; K' \triangleright t'$ include all rules from Figure 5 (appropriately adapted) and the rule in Figure 4. This new rule allows command $c$ to change to command $c'$, so long as both commands have the same enclave code, expressed by relation $c \approx c'$ (defined in Appendix A). This corresponds to an attacker that can exploit a vulnerability in non-enclave code but is unable to corrupt code within enclaves. Since modifying the program is a security relevant action, an event $A(c \approx c')$ is emitted to the trace (and we modify $[t]_t$ to include events of the form $A(c \approx c')$).

If a program is secure for $\llbracket N$-chaos then it is secure for $\llbracket$. The converse does not necessarily hold. For example, consider the following program, where $\delta(hi) = E_1$ and $\gamma(hi) = H$.

$$c \equiv \text{enclave}(1, x := hi); \text{output } 1 \text{ to } L$$

The program is secure at level $L$ for specification $\gamma$ and semantics $\llbracket$ but is insecure for semantics $\llbracket N$-chaos. Suppose the active attacker modifies the program to $c' \equiv \text{enclave}(1, x := \text{hi}); \text{output } x \text{ to } L$. Note that $c \approx c'$, since the code in enclaves for both $c$ and $c'$ is the same: $\text{enclave}(1, x := \text{hi})$. Suppose we execute $c'$ with an initial memory that maps hi to 42. The knowledge of an attacker observing output on channel $L$ is $\{m' \mid m'(hi) = 42\}$. However the permitted lower bound on attacker’s knowledge is the set of all initial memories (i.e., the attacker is not permitted to learn anything about the confidential data). So the program is not secure at level $L$ for $\gamma$ and $\llbracket N$-chaos.

**Enclave active attacker** Given a set of enclaves $I \subseteq \{E_1, E_2, \ldots \}$, relation $\llbracket E_1$-chaos allows the attacker to arbitrarily change (enclave and non-enclave) code but only after all enclaves in $I$ are killed. This corresponds to a setting where enclaves and non-enclave code all have exploitable vulnerabilities but the attacker does not immediately exploit these vulnerabilities.

Inference rules for judgment $\mu \vdash_{\beta} \langle c, r, m, K \rangle \llbracket E_1$-chaos $r'; m'; K' \triangleright t'$ include all inference rules from Figure 5 (appropriately adapted) and the rule in Figure 4. This new rule allows command $c$ to change to an arbitrary command $c'$ provided all enclaves in $I$ are killed ($I \subseteq K$). Event $A(c')$ is emitted to the trace (and we modify $[t]_t$ to include events of the form $A(c')$).

Consider the following program that stores credit card information in an enclave, where $\gamma(\text{ccard}) = H$ and $\gamma(l) = H$ for all other locations $l$ and $\delta(\text{ccard}) = E_1$.

$$\text{enclave}(1, \text{output } \text{ccard to } H; \text{set(endl)}); \text{kill}(1)$$

The program outputs the contents of ccard to the $H$ channel and sets condition cnd in enclave $E_1$. It then exits the enclave $E_1$ and kills it. The program is secure at security level $H$ for specification $\gamma$ and semantics $\llbracket E_1$-chaos, where $I = \{E_1\}$. This means that if an enclave active attacker modifies the program after kill(1) has been executed, it is unable to learn anything about the contents of ccard. Note that when $I = \emptyset$, the program is not secure for any subset of locations.

### 4. IMPE Type System

We introduce a security-type system [38] for IMPE that guarantees security, i.e., a well-typed program is secure against all the attackers described in Section 3.3.

Figure 6 shows the syntax of types. Base types $\sigma$ include integers, conditions, references, and functions. Recall that conditions are a subset of locations. We use type $\text{cond}^{\mu}$ for conditions (i.e., values in the set $\text{Cond}$). A reference type $\tau^{\mu}$ is a pointer to a memory location with contents of type $\tau$. Both condition types and reference types are annotated with mode $\mu$, indicating in which enclave, if any, the memory location resides. Reference types also have a mutability annotation $rt \in \{\text{mut}, \text{immut}\}$, indicating whether the reference is mutable or immutable. We use immutable references to ensure that declassified expressions are indeed es-
cape hatches, i.e., that declassified expressions are not modified prior to declassification. All conditions are mutable. We explain function types $\Gamma^+, K^+, U \xrightarrow{\mu, \mu'} \Gamma^+, K^+$ after explaining the type judgment.

A security type $\tau = \sigma_p$, is a base type $\sigma$ annotated with a security policy $p$. Intuitively, data with type $\sigma_p$ should have security policy $p$ or a more restrictive policy enforced on it.

A type environment $\Gamma$ maps variables to security types, and non-condition locations to pairs $(\tau, rt)$ of a security type and immutability annotation, where $\tau$ is the type of the location’s contents, and $rt$ describes the location’s immutability. For simplicity, we assume that whether a condition is set is public information, and so for any $\text{cond} \in \text{Cond}$, the type of $\text{cond}$ is $\text{cond}^\mu$, for some mode $\mu$ where $\delta(\text{cond}) = \mu$. Thus, we exclude $\text{Cond}$ from the domain of $\Gamma$.

A type environment is well-typed for $\delta$ if all locations containing confidential information belong to some enclave. Since security level $\top$ is meant to indicate information that is too confidential to be stored on the machine, we also require that well-typed environments do not map any variable or location to a type $\sigma_{\top}$. The following definition formally states the well-typedness of environment $\Gamma$ for $\delta$.

**Definition 5 (Well-Typed Environment).** A type environment $\Gamma$ is well-typed for $\delta$, denoted as $\Gamma_\delta \vdash \Gamma$, if

\[
\forall l \in \text{Loc} \setminus \text{Cond}. \Gamma(l) = (\sigma_p, rt) \land p \not\in L \implies \delta(l) \neq \bot \land p \neq \top
\]

and

\[
\forall x \in \text{Vars}. \Gamma(x) = \sigma_p \implies p \neq \top
\]

The IMP type system is flow-sensitive in that the type of variables may differ at different program points. Also, our type system tracks the set of killed enclaves to ensure that no code or data inside a killed enclave is accessed. To ensure that erasure policies are correctly enforced, our type system tracks the set of conditions that are definitely unset.

The typing judgment for commands has the form

\[
\text{pc}, \mu, \Gamma, K, U \vdash_\delta c : \Gamma', K'
\]

where:

- $\Gamma$ and $\Gamma'$ are, respectively, the type environments immediately before and after execution of command $c$;
- $K$ and $K'$ are, respectively, the set of killed enclaves immediately before and after execution of $c$;
- $U$ is the set of conditions that are known to be not set immediately before the execution of $c$;
- $\mu$ indicates whether $c$ executes in normal mode ($\mu = N$) or in an enclave ($\mu = E_i$);
- $\text{pc}$ is a policy representing an upper bound on the information that influences the decision to execute $c$, and is also a lower bound on the side-effects of $c$. This program counter policy $[29, 38]$ is used to prevent implicit flows $[14]$, i.e., information flows via the control decisions of a program.

- $\delta$ is a function which indicates for each memory location which enclave, if any, it belongs to.

The type judgment for expressions is $\mu, \Gamma \vdash_\delta e : \tau$, meaning that in mode $\mu$ under type environment $\Gamma$, expression $e$ has type $\tau$.

A function type $\Gamma^+, K^+, U \xrightarrow{\mu, \mu'} \Gamma^+, K^+$ indicates the type environment $\Gamma^+$ that must hold before the function is invoked, and the type environment $\Gamma^+$ that will hold immediately after function invocation. These environments may be partial functions, since the function may use only a subset of variables. Well-typedness of functions will ensure that $\text{dom}(\Gamma^-) \subseteq \text{dom}(\Gamma^+)$.

Kill set $K^-$ is the set of killed enclaves the function expects at invocation, and $K^+$ is the set of killed enclaves after function invocation. Set $U$ is the set of conditions that the function assumes are unset upon function invocation. Mode $\mu$ is the mode in which the function was defined, and policy $p$ is a lower bound on the side-effects of the function.

We define subtyping on security types based on the relative restrictiveness of security policies. Given policies $p$ and $q$, we say that $q$ is at least as restrictive as $p$, written $p \leq q$, if policy $q$ imposes at least as many restrictions on the use of data as policy $p$. The relation $\leq$ on policies forms a lattice. We write $\cup$ for the join operation. We overload the symbol $\leq$ and write $\sigma_1 \leq \sigma_2$ when base type $\sigma_1$ is a subtype of base type $\sigma_2$, and write $\tau_1 \leq \tau_2$ when security type $\tau_1$ is a subtype of security type $\tau_2$.

We lift subtyping to type environments and define $\Gamma_1 \leq \Gamma_2$ if and only if $\text{dom}(\Gamma_1) = \text{dom}(\Gamma_2)$ and $\forall y \in \text{dom}(\Gamma_1). \Gamma_1(y) \subseteq \Gamma_2(y)$. Function types are contravariant in the pre-environment, and the side-effect bound, covariant in the post-environment, and invariant otherwise. Inference rules for the subtyping ($\leq$) relation are presented in Appendix [3].

Figure 7 shows typing rules for expressions. As is standard in security-type systems, constants (including integers, conditions, references, and function definitions) are given policy $L$, the most permissive security policy.

Dereferencing an expression may reveal information about both which location is dereferenced and the contents of that location. Thus in rule $T$-Deref the result of a $*e$ expression has a security policy that is at least as restrictive as the policy on the reference and the contents of the reference. The premise $\mu' \neq N \implies \mu = \mu'$ (in both $T$-Deref and $T$-Sunset) requires that locations in enclaves can be accessed only by code in the same enclave.

Most of the commands follow the standard security typing rules for an imperative language (including subsumption). The rules further ensure that killed enclaves are never accessed (premise $\mu \notin K$ in many rules) and that enclave locations are accessed only by code in the appropri-
ate enclave, that only public information (i.e., with security policy \( L \)) can be accessed outside of enclaves (premise \( p \not\in L \implies \mu' \neq N \) in many rules), and that the program does not store information at security level \( \top \) (premise \( p \neq \top \) in many rules). To ensure that kill sets are tracked precisely, we require that both branches of conditionals kill the same enclaves, and that the body of loops kill no enclaves. We also require functions that expect non-empty \( U \) to in an enclave. This prevents non-enclave attackers from violating the assumption on \( U \) when invoking a function.

Figure 7 presents typing rules for commands.\(^2\) Rules T-Skip, T-Assign, T-Sub, T-Seq, T-If-Else, T-While are mostly standard. Rule T-Declassify ensures that a declassification uses only immutable locations and has no variables (so that the value declassified is the same as the valuation of the expression in the initial memory, and so the expression is an escape hatch). This is enforced by premises hasNoVars(\( e \)) and allLocImmutable(\( e \)). The latter is formally defined as \( \forall t \in e. l \not\in Cond \land (\Gamma(l) = (\sigma_p, rt) \implies rt = \text{immut}) \). To ensure that the decision to declassify information does not reveal information, we require that the program counter policy for a declassification is \( L \). Note that the result of the declassification has policy \( L \) (i.e., variable \( x \) maps to \( \sigma_L \)).

\(^2\) For presentation purposes, these rules are non-algorithmic. They can be easily adapted to enable syntax-directed type-checking algorithms.

Rule T-Output ensures that the current security level enforced on both a value output to a channel, and the decision to perform the output, is permitted to be output \( (\text{cur}(p, U) \cup \text{cur}(pc, U)) \subseteq \ell \).

Rule T-If-Isunset is similar to T-If-Else but is used when the branch condition tests whether a condition is unset. This allows the true branch to be type-checked under the assumption that condition \( \text{cond} \) is unset, which improves precision, of, for example, output commands.

Rule T-Enclave ensures that command enclave(\( i, c \)) can be executed only in non-enclave mode \( \mu = N \), and type checks \( c \) in mode \( E_i \) with an empty set of conditions that are assumed to be unset. This is to ensure that an attacker that can control non-enclave execution is unable to violate an assumption made by enclave code. Also, premise isVarLowContext(\( \Gamma' \)) ensures that at the end of the enclave code, all variables contain only information with policy \( L \). This ensures that at the end of execution of enclave code, there is no confidential information remaining in variables that could be leaked to an attacker.

Rule T-Kill requires that enclaves can only be killed by non-enclave code (\( \mu = N \)). This reflects the operation of Intel’s SGX enclaves. Since killing an enclave may be detectable by a non-enclave attacker, we ensure that the decision to kill an enclave relies on only non-confidential information (\( pc = L \)). Premise \( E_i \not\in K \) ensures that an enclave is not killed more than once.

Rule T-Update is mostly standard, but like T-Deref and T-Isunset, requires that locations in enclaves can be accessed only by code in the same enclave.

Rule T-SetCnd checks that condition \( \text{cond} \) can be set only if it is not currently assumed to be unset, i.e., \( \text{cond} \not\in U \).

Rule T-Call ensures that the preconditions for calling the function are satisfied, namely that the kill set and unset conditions assumed by the function is equal to the current kill set and unset conditions, and that the assumptions of the function’s type environment are satisfied (\( \forall y \in \text{dom}(\Gamma'), \Gamma'(y) \leq \Gamma'(y) \)). The program counter policy \( pc \) and the information revealed by which function to invoke \( (q) \) must be no more restrictive than \( p \), the lower bound on the function’s side effects. The premise \( U \neq \emptyset \implies \mu \neq N \) prevents a non-enclave active attacker from directly invoking a function and violating the assumption on set \( U \). The type environment after the function call respects the function’s post-environment: \( \forall y \in \text{dom}(\Gamma'), \Gamma'(y) \leq \Gamma'(y) \). Since \( \Gamma^- \) and \( \Gamma^+ \) are partial, we require that the types of variables not in \( \text{dom}(\Gamma') \) (which is a superset of \( \text{dom}(\Gamma') \)) remain unchanged: \( \forall y \in \text{dom}(\Gamma) \setminus \text{dom}(\Gamma^+) \). \( \Gamma(y) = \Gamma'(y) \). After the function invocation, the kill set is \( K^+ \).

**Type Soundness** Program execution starts with a known initial register file \( (r_{\text{init}}) \) that maps all variables to constant zero. We say that type environment \( \Gamma^+ \) corresponds to security specification \( \gamma \) if policies on locations agree with \( \gamma \) and \( \Gamma^+ \) maps all variables to \( \text{int}_L \) (since \( r_{\text{init}} \) maps every variable
### Theorem 1

Let \( \gamma \) be a well-formed security specification and \( \Gamma \) be a type environment that corresponds to \( \gamma \) and is well-typed for \( \delta \). If \( L, N, \Gamma, \emptyset, \emptyset \vdash \gamma \) then:

- \( c \) is secure at security level \( L \) for specification \( \gamma \) and semantics \( \llcorner :) \); and
- \( c \) is secure at security level \( L \) for specification \( \gamma \) and semantics \( \llcorner N \)-enclave-chaos-.

For all \( I \subseteq \{ E_1, E_2, \ldots \} \), define

\[
\gamma'(l) = \begin{cases} 
\gamma(l) & \text{if } \delta(l) \in I \\
L & \text{otherwise}
\end{cases}
\]

**Command** \( c \) is secure at security level \( H \) for specification \( \gamma' \) and semantics \( \llcorner E_1 \)-chaos-

Note that for an enclave active attacker that can attack enclaves in set \( I \) only after those enclaves have been killed, Theorem\([\text{I}]\) states that command \( c \) is secure for security specification \( \gamma' \) derived from \( \gamma \). Specification \( \gamma' \) is the same as \( \gamma \) for all locations placed in enclaves in \( I \), but for all other locations does not enforce any security restrictions (i.e., \( \gamma'(l) = L \) if \( \delta(l) \notin I \)). That is, we can protect information placed in enclaves in \( I \) against an enclave active at-
The type system is a simplified version of the IMPE type system. Judgment \( pc, G, U \vdash c : G' \) means that command \( c \) is well-typed, where \( G \) and \( G' \) are the type environments immediately before and after execution of \( c \), program counter policy \( pc \) is an upper bound on the information that influences the decision to execute \( c \), and set \( U \) are conditions that are definitely unset.

Judgment \( G \vdash e : \sigma_p \) means expression \( e \) has type \( \sigma_p \) under type environment \( G \). All typing rules are straightforward adaptations of the IMPE rules and are given in Appendix C.

6. Translation

We provide a translation of IPS programs to IMPE that automatically places code and locations into enclaves. Our translation is constraint-based: a type-directed algorithm produces a set of constraints, and any solution to the constraints provides a well-typed IMPE program. We consider various criteria for choosing one translation over another.

6.1 Constraint-Based Translation

The constraints place restrictions on where locations may be placed (i.e., on the function \( \delta \)), on kill sets, on mode annotations, and on which commands may be placed inside an enclave.

The constraints ensure that any location that contains confidential information (i.e., with a policy other than \( L \)) is placed in an enclave, and that subsequent use of these locations is consistent (i.e., accessed only by code in the same enclave). The constraints also ensure that confidential data cannot be accessed in non-enclave mode, and also that enclaves are killed appropriately (i.e., no enclave is accessed after it is killed, and enclaves are killed at most once).

The translation judgment for commands has the form

\[ pc, G, U, c, G' \vdash \mu, \Gamma, K, \delta, e', \Gamma', K' \]

We ensure that if \( pc, G, U \vdash c : G' \) for IPS command \( c \), then \( e' \) is the translated IMPE command such that, provided the constraints are satisfied, \( pc, \mu, \Gamma, K, U \vdash_{\delta} e : \Gamma', K' \).

Instead of the translation judgment explicitly producing a set of constraints, for brevity we present inference rules for the judgment such that constraints are implied by premises that restrict modes, mode annotations, kill sets, etc.

The translation judgment for expressions has the form

\[ G, e, \sigma_p \vdash \mu, \Gamma, \delta, e', \sigma_p \] where \( e \) is an IPS expression such that \( G \vdash e : \sigma_p \) holds and \( e' \) is the translated IMPE expression such that, provided the constraints are satisfied, \( \mu, \Gamma \vdash_{\delta} e' : \sigma_p \) holds.

The judgment for translating base types is \( \sigma \vdash_{\delta_3} \sigma \). It states that an IPS base type \( \sigma \) is translated to an IMPE base type \( \sigma \). It is parametrized by \( \delta \) to ensure that type environments for functions types are translated appropriately.

Figure 10 shows the type translation. In the rule for type environments, premise \( \forall l \in \text{dom}(\Gamma). G(l) = (\sigma_p, rt) \land p \not\leq L \implies \delta(l) \neq N \) ensures that all confidential locations

| e ::= n \mid x \mid e_1 \oplus e_2 \mid l \mid *e \mid \text{isunset}(\text{cond}) \mid \lambda.c |
| v ::= \lambda.c \mid n \mid l |
| c ::= \text{skip} \mid x ::= e \mid x ::= \text{declassify}(e) \mid e_1 \leftarrow e_2 |
| \quad \mid \text{output } e \text{ to } \ell \mid \text{call } (e) \mid \text{set}(\text{cond}) \mid c_1; \ldots; c_n |
| \quad \mid \text{if } e \text{ then } c_1 \text{ else } c_2 \mid \text{while } e \text{ do } c |
| l ::= \text{int} \mid \text{cond} \mid r \text{ ref}^{rt} \mid G^+, U \vdash_s G^+ |
| \Gamma ::= \sigma_p |
| rt \in \{\text{mut, immut}\} |

Figure 9. IMPS syntax and types

Type enclaves are a powerful mechanism, but management of enclaves may be error prone and distract the programmer from implementing correct functionality. We present a language IMPS that is similar to IMP, but removes all enclave-related commands and abstractions and thus allows the programmer to focus on functionality and high-level security requirements. In Section 6 we translate from IMPS to IMP, automatically inferring appropriate enclaves.

The syntax for IMPS (Figure 9) is similar to IMP, except that functions are not annotated with mode and commands enclave(i, c) and kill(i) are removed. An IMPS configuration is a triple \( \langle c, r, m \rangle \) where \( r \) and \( m \) are a register file and memory, respectively, as defined in Section 2.3.

The large-step semantic judgment for IMPS has the form \( \langle c, r, m \rangle \downarrow_s r' ; m' > t' \), meaning configuration \( \langle c, r, m \rangle \) executes and terminates with register file \( r' \) and memory \( m' \) and during execution produces trace \( t' \). The judgment for expression evaluation is \( \langle e, r, m \rangle \downarrow_s v \) and can be read as given register file \( r \) and memory \( m \), expression \( e \) evaluates to value \( v \). Inference rules for these judgments are straightforward, and similar to those of IMP, although without any restrictions based on modes.

Types in IMPS (Figure 9) are similar to those of IMP. We underline types and type metavariables to distinguish them from IMP types. Unlike IMP, conditions and references do not have mode annotations, and function types have neither mode nor kill set annotations. We use \( G \) to denote type environments in IMPS. A type environment \( G \) is well-typed if it does not map any location to type \( \sigma_T \). Similar to \( \Gamma \), we say that type environment \( G \) corresponds to security specification \( \gamma \), if \( G \) maps all variables to \( \text{int} \), and policies on locations agree with \( \gamma \).
have appropriate enclave assignments (even if these locations are not used by the program).

Figure 11 shows the translation for expressions. Translation for expressions proceeds by first translating the types. They enforce the invariant that a location in enclave $E_i$ is accessed in the same mode $E_i$. Rules TR-INT, TR-VAR, TR-LOC, TR-CND, and TR-OP are straightforward.

Rule TR-DEREF translates a dereference expression. The premise $\mu' \neq N \implies \mu = \mu'$ generates a conditional constraint such that if the dereferenced location is in an enclave ($\mu' \neq N$) then the expression is evaluated in the same enclave ($\mu = \mu'$). Similarly, rule TR-WSUNSET ensures that if a condition location is dereferenced, then the mode in which the expression is evaluated is appropriate.

Rule TR-FUNCTION requires that if the post type environment $\Gamma'$ has any variables with policies more restrictive than $L$ (isVarLowContext($\Gamma'$)), then the function is defined in an enclave ($\mu \neq N$). Intuitively, any data left in variables at the end of the function invocation may be observable by the code that invoked the function. If that data includes confidential information, then the function should not be invoked by non-enclave code.

Figure 12 shows the inference rules for translating commands. Most of these rules are straightforward and closely follow the premises of the corresponding typing rules. Premise $\mu \not\in K$ occurs in many of the rules, and ensures that code in killed enclaves cannot be executed.

Rule TR-SEQ drives the entire translation. Intuitively, given a sequence $c_1; \ldots; c_n$, it translates each sub-command $c_i$ by assigning them a different mode variable $\mu_i$. If the translation infers that $\mu_0 = N$ but $\mu_i \neq N$, then the translated sub-command $c'_i$ is placed inside an enclave. Variable $K_i$ is the kill set immediately before the execution of $c'_i$, $K'_i$ is the kill set immediately after the execution of $c'_i$, and $K''_i$ is the set of enclaves (if any) that can be safely killed after executing $c'_i$. Thus, we have that $K_{i+1} = K'_i \cup K''_i$.

Constraint $K'_i \cap K''_i = \emptyset$ ensures that an enclave is not killed more than once. Constraint $\mu_0 \neq N \implies (\mu_0 = \mu_i \wedge K''_i = \emptyset)$ states that if sequence executes entirely in an enclave ($\mu_0 \neq N$) then all sub-commands are in the same enclave and no enclaves are killed. Constraint $\mu_i \neq N \implies \mu_i + 1 \implies K''_i = \emptyset$ ensures that no enclave can be killed between sequences executing in same enclave.

Constraint $\mu_i \neq N \wedge (\mu_i \neq \mu_i + 1 \wedge K''_i \neq \emptyset) \implies$ isVarLowContext($\Gamma_i$) ensures that if execution exits an enclave (i.e., if command $c'_i$ executes in an enclave, but the sequence itself is not in an enclave) then no variables contain confidential information when the enclave exits. This is required to enforce typing rule T-ENCLAVE. Notice that an enclave exit after command $c_i$ does not necessarily mean that $\mu_{i+1} = N$. It may signal the start of a different enclave, hence we also state $\mu_i \neq \mu_i + 1$ in the antecedent.

Rule TR-SEQ uses utility function processSeqOutput which inserts enclave and kill commands appropriately into the translation. Intuitively, enclave is introduced for a command $c'_i$ if there is a mode change. Command kill($j$) is inserted after command $c'_i$ if $j \in K''_i$. Pseudo code for processSeqOutput is presented in Appendix 2.

Rule TR-IF-ELSE requires that same sets of enclaves are killed in both the branches. Also, if variables contain confidential information on exit of either branch, then the outer mode should not be normal. Rule TR-IF-UNSET always places the command in an enclave to ensure that the premises of typing rule T-IF-UNSET are met. Rule TR-WHILE requires that no enclave is killed in the loop body. Rule TR-CALL requires that if set $U$ is non-empty, then the function is defined in an enclave.

Soundness of Translation Successful translation of well-typed IMPs program produces a well-typed IMPE program.

Theorem 2 (Soundness of Translation). Let $\mathcal{G}$ be a well-typed IMPs environment and $\Gamma$ be an IMPE environment that is well-typed for $\delta$. For all commands $c$ in IMPs, if $pc, \mathcal{G}, U \vdash c : \mathcal{G}'$ and $pc, \mathcal{G}, K, c, \mathcal{G} \vdash \mu, \Gamma, U, \delta, c', \Gamma', K'$ then $pc, \mu, \Gamma, K, U \vdash c' : \Gamma', K'$.

Given a well-typed IMPs program and environment $\mathcal{G}$, if the translation is successful using an IMPE environment $\Gamma$ that is well-typed for $\delta$, then the translated IMPE program is also well-typed. Since a well-typed IMPE program is secure (Theorem 1), the translation thus guarantees security against all the attackers described earlier.

Note that well-typedness of the translated program is contingent on the success of translation. The only way that translation will fail is if predicate isVarLowContext($\Gamma'$) does not hold for $\Gamma'$ at enclave exit, i.e., a variable contains confidential information when the enclave exits. This is because the trivial solution of putting all code and data inside a single enclave will succeed provided there is no confidential information left in variables at the end of the program. One approach to ensure translation always succeeds is to add a semantics-
preserving transformation that zeroes-out variables as soon as they are dead.

6.2 Constraint Solution and Optimization

The constraints used in the translation of IMPs programs to IMPe can be expressed as a Boolean SAT instance, assuming that the mode set $\text{Mode} = \{N, E_1, E_2, \ldots \}$ is of a fixed finite size. Specifically, the constraints restrict modes of locations and code, and kill sets (which are sets of enclaves).

All constraints generated during translation can be encoded straightforwardly in a SAT formula. For a program of size $n$ with $m$ locations where $|\text{Mode}| = k$, the size of SAT formula is $O((n + m)^2 + nk)$.

There may be many possible translations of a given IMPs program without any of them being clearly the “best” translation. Naively, we could try to place the entire program and all locations in a single enclave. However, even if successful, this is not always desirable for at least two reasons. First, an enclave may have size restrictions and a program can be too large to fit. Second, even if the program can fit inside an enclave, it may be desirable to have as little code as possible in enclaves, to reduce the trusted computing base (i.e., the code that must be assumed to be correct; security for a non-enclave active attacker assumes that enclave code does not contain exploitable vulnerabilities).

There are several possible criteria for comparing the quality of translations, including minimizing the code and data inside enclaves (which corresponds to minimizing the trusted computing base (TCB)), reducing the lifetime of confidential data by killing enclaves as soon as possible, or minimizing the performance penalty of enclaves.

We can cast our translation as a constraint optimization problem that optimizes an objective function that approximates TCB-size, lifetime of enclaves, performance penalty, or a combination of these.

A pseudo-Boolean function $f : \{0, 1\}^n \rightarrow \mathbb{R}$ is a real-valued function of a finite number of 0-1 valued variables. A pseudo-Boolean constraint is an equality or inequality between pseudo-Boolean functions. Pseudo-Boolean optimization (PBO) optimizes a pseudo-Boolean function subject to pseudo-Boolean constraints. PBO is 0-1 multilinear integer programming and is NP-hard. We can encode the SAT formula for a translation as a pseudo-Boolean constraint and express TCB size and performance as pseudo-Boolean functions to be minimized.

We can compute the TCB cost by counting the number of non-sequence commands placed in enclaves.

Killing an enclave as soon as possible reduces the window of vulnerability. This can be achieved by maximizing the size of kill sets at all program points, which effectively kills enclaves as soon as possible. Moreover, we can facilitate killing enclaves as early as possible by using more enclaves, i.e., partitioning code and data into enclaves at fine granularity. This is also optimized by maximizing the size of kill sets. Note that to avoid spuriously putting public data into enclaves to increase the total number of enclaves that can be killed, we require that each killed enclave has at least some confidential data stored in it.

---

3 Intuitively, $(n + m)$ mode variables, pairs of which are constrained to be either equal or different are generated, resulting in at most $(n + m)^2$ constraints. Additionally, $nk$ kill set constraints (e.g., $K_{i+1} = K_i' \cup K_i''$ in TR-SEQ) are generated. Thus the size of the SAT formula is $O((n + m)^2 + nk)$.

4 On some models, SGX enclaves have a maximum size of $2^{31}$ bits [24].

5 In Intel SGX, entering or exiting an enclave flushes all TLB entries [24].
<table>
<thead>
<tr>
<th>Rule</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>TR-Skip</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, U, \text{skip} \Rightarrow \mu, \Gamma, \delta, \kappa, G$</td>
</tr>
<tr>
<td>TR-Declassify</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, e, \sigma_q \Rightarrow \mu, \Gamma, \delta, e', \sigma_q$</td>
</tr>
<tr>
<td>TR-Output</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, U, \text{output} e \leadsto \ell, \mathcal{G} \Rightarrow \mu, \Gamma, \delta, \text{output} e' \leadsto \ell, \Gamma, K$</td>
</tr>
<tr>
<td>TR-Update</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, e_1, \sigma_p \Rightarrow \mu, \Gamma, \delta, e_1', \sigma_p$, $\Gamma \vdash \text{pc}, \mathcal{G}, e_2, \sigma_p' \Rightarrow \mu, \Gamma, \delta, e_2', \sigma_p'$</td>
</tr>
<tr>
<td>TR-If-Else</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, e, \text{int}_p \Rightarrow \mu, \Gamma, \delta, e', \text{int}_p$, $\Gamma \vdash \text{pc}, \mathcal{G}, U, c_1, \sigma_p' \Rightarrow \mu, \Gamma, \delta, c_1', \Gamma'$</td>
</tr>
<tr>
<td>TR-If-Isunset</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, e, \text{int}_p \Rightarrow \mu, \Gamma, \delta, e', \text{int}_p$, $\Gamma \vdash \text{pc}, \mathcal{G}, U, c_1, \sigma_p' \Rightarrow \mu, \Gamma, \delta, c_1', \Gamma'$</td>
</tr>
<tr>
<td>TR-While</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, e, \text{int}_p \Rightarrow \mu, \Gamma, \delta, e', \text{int}_p$, $\Gamma \vdash \text{pc}, \mathcal{G}, U, c, \sigma_p' \Rightarrow \mu, \Gamma, \delta, c', \Gamma$</td>
</tr>
<tr>
<td>TR-Call</td>
<td>$\Gamma \vdash \text{pc}, \mathcal{G}, U, \text{call}(e), \mathcal{G}<em>{\text{out}} \Rightarrow \mu, \Gamma, \delta, \text{call}(e), \Gamma</em>{\text{out}}$, $\mathcal{G}<em>{\text{out}} \Rightarrow \mathcal{G}</em>{\text{out}}$, $\mathcal{G}_{\text{out}}$</td>
</tr>
</tbody>
</table>

**Translation for commands**

Figure 12.
Enclave entry and exit is expensive and penalizes the run-time performance. Although we have not implemented it, we could approximate the run-time cost using a Control Flow Graph (CFG) and approximating how frequently execution enters and exits enclaves.

7. Comparison with SGX

Although there are several hardware-enforced enclave-like mechanisms, IMPE is most heavily influenced by SGX. We discuss how IMPE relates to SGX.

First, we assume that enclaves are isolated from each other: code in enclave $E_i$ cannot access memory in enclave $E_j$ when $i \neq j$. SGX does enforce this via an access control mechanism, but uses a single encryption key to protect the contents of all enclaves. Some enclave mechanisms (such as TrustZone) do not provide multiple enclaves.

Second, we assume that once an enclave is killed, the contents of the enclave can never be recovered, thus providing forward secrecy. However, the current design of SGX bases access control decisions on the initial measurement of an enclave. That is, if another enclave is created that has the exact same initial contents as the killed enclave, a replay attack may be possible, whereby the new enclave decrypts memory pages from the killed enclave.

Third, our model assumes inputs to an execution are provided in the initial memory and output channels exist for security levels $L$ and $H$. Our model can be easily modified to use channels for input instead of the initial memory. Secure channels from an SGX enclave to remote parties can be straightforwardly implemented using cryptographic mechanisms. However, SGX currently provides little support for secure output to local devices and no support for secure local input, possibly making it unsuitable for, e.g., securely checking a locally-entered password. However, support for secure local I/O is emerging, such as TrustZone’s Trusted User Interface [17]. This supports our modeling choice to allow the enclave to receive and send confidential information, which can represent (remote or local) secure I/O.

8. Evaluation

We implement six case studies (many inspired by related work [7, 16, 34, 36]) to evaluate the expressiveness of security policies, and the translation from IMPS to IMPE. The translator and case studies are available online [19]. All case studies are implemented as (well-typed) IMPS programs which translate successfully to IMPE programs. Thus all case studies are secure against passive, enclave, and non-enclave active attackers.

We extend the calculi with strings, pairs, and arrays. The types of IMPE and IMPS are extended as follows.

\[
\sigma ::= \cdots \mid \text{string} \mid \sigma_1 \times \sigma_2 \mid \tau^\mu \mid r^t
\]

\[
\sigma ::= \cdots \mid \text{string} \mid \sigma_1 \times \sigma_2 \mid \tau \mid r^t
\]

An array is a sequence of locations with the constraint that all elements of the array are in the same enclave (or all elements are in no enclave). IMPE array type $\tau^\mu \mid r^t$ indicates an array that contains values of type $\tau$, mode $\mu$ indicates in which enclave (if any) the array is placed and $r^t$ indicates if the contents of array are mutable. The IMPS array type is similar except that there is no mode annotation. Types for strings and pairs are straightforward.

Password Authentication

Recall the password authentication example (with declassification) from Section 3.2. Consider an IMPS version (i.e., without any enclave annotations). Translating it with our tool gives the following.

\[
\text{enclave}(1, \text{status} := \text{declassify}(\ast \text{password} = \ast \text{guess}));
\]

\[
\text{kill}(1); \text{set}(\text{end}); \text{output} \text{status} \text{to} L
\]

The translation assigns enclave $E_i$ to locations password and guess (i.e., translated locations have types $\text{int}_{E_i} \text{ref}^{\text{immut}}$ and guess : $\text{int}_{E_i} \text{end} \times \top \text{ref}^{\text{immut}}$). The declassification is placed inside $E_i$ because it reads password and guess. The translation kills the enclave immediately after exiting the enclave. This is as early as possible, thus minimizing the window of vulnerability.

Private Browsing

A private session of a web browser allows a user to browse the web with the assurance that the browsing history cannot be retrieved after the private session has ended. However, private browsing implementations are error prone, and many leak information from private sessions [1, 33]. We model a private browsing session where the user starts a private session, browses, then ends the session. The security requirement can be expressed as an erasure policy that states that all private browsing data (and data derived from it) should be erased when a condition marking the end of the session is set.

Since our calculi model input as the initial memory, we assume that the initial memory contains the user’s input to the private session (e.g., an array of URLs to visit). The user’s input has erasure policy $H \end \times \top$, where condition end is set at the end of the private session. During the private session, output is sent to channel $H$. Once the session ends, we model normal browsing by output to channel $L$.

Translation assigns enclave $E_i$ to all the locations containing the user’s input to the private session. It also places all code related to the private browsing session inside enclave $E_i$ and generates a kill instruction before resuming normal browsing.

Secure Calculator

We implement a secure calculator that performs public operations on confidential data. This is a model of, for example, a tax computation, where well-known operations (the tax computation) are performed on confidential input (an individual’s financial information). The operations are chosen dynamically (i.e., public inputs specify which operations to perform). The result of the computation is output to channel $H$. The initial memory contains an array of operations to perform (with security policy
We model columns name and wages as arrays, with policies $L$ and $H$ respectively. Row selection chooses all indices of array name that are equal to key “alice”. Summing computes the sum of all wages corresponding to the selected indices.

The translation places array wages in enclave $E_1$, but leaves array name outside of any enclave. The row selection computation is placed outside an enclave, and the summing operation is placed inside enclave $E_1$. Our automated translation places the same data and computation in enclaves as the (manually coded) case study of Sinha et al.\cite{sinha2016secure}.

**Secure Chat Client** We model a secure chat client, inspired by the case study of Askarov et al.\cite{askarov2014provably}. Messages sent and received by the client are emitted to a log. When the user enters a “clear” command, all messages (including the log) should be erased. We model messages sent and received and commands entered by the user as data in the initial memory. We model logging as an update to location log. We give messages and the log the erasure policy $L \text{ clear} \rightarrow \top$, which states that the contents of log are erased when condition clear is set. The condition is set only when a “clear” command is issued. The translation places log in an enclave, as well as all code that updates the log.

**9. Related Work**

Models for Secure Hardware Architecture and Compilation Fournet and Planul\cite{fournet-palanuel} securely compile imperative programs into distributed programs using cryptography and hardware mechanisms (such as Trusted Platform Modules (TPM) and secure boot) to enforce noninterference for confidentiality and integrity. They emulate secure memory (that cannot be accessed by adversaries) and enforce control-flow restrictions on the distributed program. The compiled program is proven to be at least as secure as the source program: for every attack on the compiled program there is a corresponding attack on the source program, with the same information leakage. By contrast, we focus on expressive security policies (erasure and decapsulation) that go beyond noninterference. Their system doesn’t provide erasure guarantees. We target a single machine and use enclave mechanisms that directly provide secure memory (instead of emulating secure memory via cryptographic mechanisms). Both our work and theirs shield the programmer from the mechanisms used to enforce security. Although we do not focus on integrity guarantees in this work, we rely on enclaves to provide integrity guarantees on code running inside enclaves (cf. security against non-enclave active attackers). We believe that our target calculus IMP\text{e} can be extended to provide integrity guarantees about computation inside enclaves.

VC3\cite{gundalu2016secure} enables distributed map-reduce computations in untrusted cloud environments while keeping code and data secret, using SGX enclaves to protect against adversaries that might control the entire software stack of the cloud provider’s infrastructure. We instead focus on provid-
ing confidentiality for general programs in the presence of an attacker controlling the entire software stack of a single system. In VC3, all data is confidential (i.e., equivalent to our policy $H$) and all map-reduce computation of a given node is placed inside a single enclave on that node. By contrast, we use expressive security policies (i.e., declassification and erasure) and infer enclave placement to optimize various objective functions. VC3 ensures that only address-taken variables are read and written. The region-self-integrity mechanism prevents unintended disclosure of information due to low-level errors (e.g., buffer overflow). This can be used as a defense-in-depth mechanism in our work to reduce the possibility of an enclave active attacker exploiting vulnerabilities in enclave code.

Moat [35] models SGX using BoogiePL [13] and verifies the confidentiality of binary SGX programs in the presence of “havocing” adversaries capable of modifying non-enclave code. A havocing adversary is analogous to our non-enclave attacker, which can arbitrarily modify non-enclave code. Thus, ensuring confidentiality against a havocing adversary corresponds to security for a non-enclave active attacker. Our work also considers enclave active attackers, which are more powerful than havocing adversaries. Our work differs from Moat in shielding developers from low-level enclave-specific details.

Ironclad [22] provides verifiable remote equivalence: an application running on an untrusted server is indistinguishable from its high-level abstract state machine. Ironclad uses secure hardware (e.g., TPM) as the root of trust and to enable secure channels from verified software to remote clients. Our work could potentially be used in an Ironclad-like setting to reduce verification effort: enclave inference can be used to identify and minimize the security-critical parts of an application, which reduces the code that must be verified.

Sinha et al. [37] enforce confidentiality by placing an entire application inside an SGX enclave and restricting its communication with external memory through a narrow interface to a trusted library. They enforce Information Release Confinement, which ensures that the application satisfies a form of control-flow integrity and never directly accesses non-enclave memory. This work is complementary to ours, and could be used for defense-in-depth in our work, making it harder for an enclave active attacker to exploit vulnerabilities in enclave code.

Patrignani et al. [26] provide a fully-abstract secure compilation scheme for compiling strongly typed object-oriented languages to protected module architectures (PMAs) that offer memory isolation mechanisms and are similar to enclaves. Objects containing private methods are placed inside protected modules thus preventing a low-level attacker from bypassing encapsulation mechanisms. The compilation scheme is proven to preserve and reflect the encapsulation of the source program. Their low-level attacker is similar to the non-enclave active attacker in our model. Though we do not aim for full abstraction, our work provides a stronger information flow guarantee for applications with more expressive security requirements against different attackers.

None of the above works consider applications using multiple enclaves whereas our programming model supports multiple enclaves seamlessly.

**Language-based Information-Flow Control** Much work in language-based information-flow control is concerned with enforcing application-specific security guarantees [14, 25, 29, 38]. Our work extends these techniques to a setting where the underlying software stack is not trusted. That is, we consider strong low-level attackers that are capable of arbitrary corruption of some parts of a program.

**Information Erasure** A key emphasis in our work is the enforcement of information erasure using SGX-like mechanisms. Information erasure is related to data deletion, but requires that the observable behavior of a system reveals nothing about the deleted data, which may, for example, require tracking and deletion of data derived from the deleted data. Language-based information erasure was introduced by Chong and Myers [9], and several works present techniques for enforcing erasure (e.g., [7, 10, 23]). By contrast with these previous language-based approaches, we protect against more powerful lower-level attackers.

Other work uses language- and system-based techniques to ensure data deletion at the system- or architectural-level of abstraction. Chow et al. [11] enforce data deletion by analyzing the lifetime of sensitive data, and automatically zeroing out data in memory. Perlman [27] proposes a file system that uses cryptographic techniques to reliably delete files. These approaches may fail to remove derived data, and thus will not enforce information erasure. Lacuna [15] runs sensitive computations in a “private session” and can securely delete all session data at the end of the session (including data used to communicate with peripheral devices). Provided all sensitive information is contained within a private session, Lacuna can enforce both data deletion and information erasure.

**Acknowledgments**

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**References**


A. Enclave Equivalence \( \equiv_e \)

Equivalence relation \( \equiv_e \) is used to characterize the ability of an active attacker. Intuitively, given commands \( c_1 \) and \( c_2 \), we have \( c_1 \equiv_e c_2 \) if \( c_1 \) and \( c_2 \) have the same code in enclaves, but may differ arbitrarily on non-enclave code. We define \( \equiv_e \) using function \( \chi \) which syntactically extracts enclave code.

**Definition 6** (Enclave Equivalence). Two IMPs programs \( c_1 \) and \( c_2 \) are enclave equivalent, denoted \( c_1 \equiv_e c_2 \), iff

\[
\chi(c_1) = \chi(c_2)
\]

where \( \chi(\text{enclave}(j, c)) = \{(E_y, \sigma)\} \)

\[
\chi(\lambda E_y. c) = \{(E_y, \lambda E_y. c)\}
\]

\[
\chi(\lambda N. c) = \chi(c)
\]

and all atomic expressions and commands return the empty set, e.g.,

\[
\chi(\text{skip}) = \emptyset
\]

\[
\chi(n) = \emptyset
\]

and all other expressions and commands recurse on sub-expressions and sub-commands, e.g.,

\[
\chi(e_1; \ldots; e_n) = \chi(e_1) \cup \ldots \cup \chi(e_n)
\]

\[
\chi(e_1 + e_2) = \chi(e_1) \cup \chi(e_2)
\]

For example, given \( c_1 = \text{enclave}(1, \text{output} 42 \text{ to } L) \) and \( c_2 = l \leftarrow 1; \text{enclave}(1, \text{output} 42 \text{ to } L) \), we have \( \chi(c_1) = \chi(c_2) = \{(E_1, \text{output} 42 \text{ to } L)\} \). The programs \( c_1 \) and \( c_2 \) are thus enclave equivalent.

B. IMPs Type System

Figure 13 defines the relabeling relation \( \leq \) on policies.

\[
\begin{align*}
\ell_1 \leq \ell_2 & \quad p_1 \leq \ell_2 \\
\ell_1 \leq \ell_2 & \quad p_1 \leq \ell_2^\text{cond} \wedge \ell_2^\text{cond} \\
\ell_1^\text{cond} \wedge \ell_2^\text{cond} & \leq p_2 \\
\ell_1^\text{cond} \wedge \ell_2^\text{cond} & \leq p_2 \quad \sigma \leq \sigma' \quad p \leq q \quad \sigma_p \leq \sigma_q \\
\sigma \leq \sigma
\end{align*}
\]

**Figure 13.** Policy ordering and subtyping

C. IMPs Type System

Subtyping for IMPs types closely follows the subtyping of IMPe types. Figures 14 and 15 describe the type system.

D. Pseudo Code

Function processKill inserts kill\( (j) \) whenever an enclave \( E_j \) is killed and function processSeqOutput wraps the largest sequence of code with mode \( E_j \) in enclave\( (j, \ldots) \).

\[
\text{processKill}(K) =
\]

\[
\text{match } K \text{ with }
\]

\[
| k \cup K' \rightarrow \text{kill}(k); \text{processKill}(K') |
\]

\[
| \emptyset \rightarrow () |
\]

\[
\text{processSeqOutput}(c'_1, \mu_0, \mu_1; K'_1) =
\]

\[
\text{match } (\mu_0, \mu_1; K'_1) \text{ with }
\]

\[
| \mu', (\mu' \ldots) \rightarrow c'_1; \ldots; c'_z |
\]

\[
| N, (N, \bar{\mu}_2); (K', \bar{K}_2) \rightarrow |
\]

\[
\text{processSeqOutput}(c'_1, \mu_0, \mu_1; K'_1) =
\]

\[
\text{match } (\mu_0, \mu_1; K'_1) \text{ with }
\]

\[
| \mu', (\mu' \ldots) \rightarrow c'_1; \ldots; c'_z |
\]

\[
| N, (N, \bar{\mu}_2); (K', \bar{K}_2) \rightarrow |
\]
ST-Skip
\[ G(x) = σ_q \quad G \vdash e: σ_p \]
\[ pc, G, U \vdash skip: G' \]
\[ pc, G, U \vdash x := e: G[x \mapsto σ_{pc,l}p] \]

ST-Assign
\[ G(x) = σ_q \quad G \vdash e: σ_p \]
\[ pc = L \quad hasNoVars(e) \quad allLocImmutable(e) \]
\[ pc, G, U \vdash x := \text{declassify}(e): G[x \mapsto σ_L] \]

ST-Declify
\[ G \vdash e_1: (σ_p \text{ ref}^\prime)_q \quad G \vdash e_2: σ_p' \]
\[ p' \sqcup q \sqcup p \leq p \quad rt = \text{mut} \quad p, p', q \neq T \]
\[ pc, G, U \vdash e_1 \leftarrow e_2: G \]

ST-SetCnd
\[ ∀i \in \{1 \ldots n\}, pc, G_{i-1}, U \vdash c_i: G_i \]
\[ pc, G_0, U \vdash c_1; \ldots; c_n: G_n \]

ST-Output
\[ G \vdash e: σ_p \quad p \neq T \]
\[ pc, G, U \vdash \text{output } e \text{ to } \ell: G \]

ST-if-Isunset
\[ G \vdash \text{isunset}(\text{cnd}): \text{int}_L \]
\[ pc, G, U \uplus \{\text{cnd}\} \vdash c_1: G' \quad pc, G, U \vdash c_2: G'' \]
\[ pc, G, U \vdash \text{if isunset}(\text{cnd}) \text{ then } c_1 \text{ else } c_2: G'' \]

ST-if-Else
\[ pc', G, U \vdash c_1: G' \quad G \vdash e: \text{int}_p \]
\[ pc \sqcup p \leq pc' \quad pc', G, U \vdash e: G'' \quad p \neq T \]
\[ pc, G, U \vdash \text{if } e \text{ then } c_1 \text{ else } c_2: G'' \]

ST-Sub
\[ pc_1, G_1, U_1 \vdash c: G'_1 \quad pc_2 \leq pc_1 \]
\[ G_2 \leq G_1 \quad U_2 \geq U_1 \quad G'_1 \leq G'_2 \]
\[ pc_2, G_2, U_2 \vdash c: G'_2 \]

ST-While
\[ μ, G \vdash_s e : \text{int}_p \quad pc', G, U \vdash c: G \]
\[ pc \sqcup p \leq pc' \quad p \neq T \]
\[ pc, G, U \vdash \text{while } e \text{ do } c: G \]

ST-Call
\[ G \vdash e: (G^*, U \quad \bar{p} \quad G^*_n) \]
\[ pc \sqcup q \leq p \quad \forall y \in \text{dom}(G^*), G^*(y) \leq G^*(y) \]
\[ \forall y \in \text{dom}(G^*) \setminus \text{dom}(G^*_n), G^*_n(y) = G_{\text{out}}(y) \]
\[ \forall y \in \text{dom}(G) \setminus \text{dom}(G^*), G(y) = G_{\text{out}}(y) \quad q \neq T \]
\[ pc, G, U \vdash \text{call}(e): G_{\text{out}} \]

Figure 15. IMPs typing rules for commands
E. Proofs

We first prove the soundness of IMPE type system, Theorem 1 in Section E.1, and then prove the soundness of translation, Theorem 2 in Section E.2.

E.1 Soundness of IMPE Type System

The proof for Theorem 1 follows the technique of Pottier and Simonet [28]. The language IMPE² extends IMPE to include value pairs.

\[ v ::= \cdots | (v_1 \mid v_2) \]

The pair construct represents values that may arise in two different executions of a program. They are used to track how registers and memories differ in different executions of a program. Register (Memory) in IMPE is a function from variables (locations) to value pairs. We use \((c, r, m, K)\) to denote IMPE² configuration. It has the same meaning as the IMPE configuration except that the register \(r\) and memory \(m\) now refer to IMPE² register and memory functions.

We define the semantics of IMPE² with a large step operational semantics denoted by \(\Downarrow\). The judgment for the evaluation of commands has the following form is similar to the large step defined in Section 2.3.

\[
\begin{align*}
\mu & \vdash (c, r, m, K) \Downarrow^2_{com} \langle r' \mid m' \rangle; K \triangleright t' \\
\end{align*}
\]

Most of the rules are similar to the semantics defined in Figure 3. Rule SQ-SEQ is a straightforward adaptation of the rule SEQ. Rule SQ-IF-DIV states that if the conditional evaluates to pair value, then the final configuration is the result of merging the corresponding configurations on each projection. Figure 16 defines the merge operation on locations, registers, traces and kill sets. Similarly, rules SQ-WHILE-DIV and SQ-CALL-DIV state that if the expression evaluates to a pair value, then the final configuration is the result of merging the corresponding configurations on each projection.

**SQ-SEQ**

\[
\forall i \in \{1 \ldots z\}, \mu \vdash (c_i, r_{i-1}, m_{i-1}, K_{i-1}) \Downarrow^2_{com} r_i; m_i; K_i \triangleright t_i \\
\mu \vdash (c_1 ; \cdots ; c_z ; r_0 ; m_0 ; K_0) \Downarrow^2_{com} r_z ; m_z ; K_z \triangleright t_0 ; t_z \\
\]

**SQ-IF-DIV**

\[
\begin{align*}
\mu & \vdash \langle e, r, m, K \rangle \Downarrow^2_{exp} (v_0 \mid v_1) \quad c_{left} = (v_0 == 1)?c_0 : c_1 \\
\mu & \vdash \langle c_{left}, [r_1], [m_1], [K_1] \rangle \Downarrow r_1', m_1'; K_1 \triangleright t_1' \\
\mu & \vdash \langle c_{right}, [r_2], [m_2], [K_2] \rangle \Downarrow r_2', m_2'; K_2 \triangleright t_2' \\
\hat{r} & = \text{merge}(r_1', r_2') \quad \hat{m} = \text{merge}(m_1', m_2') \\
\mu & \vdash \langle if \ e \ then \ c_0 \ else \ c_1, r, m, K \rangle \Downarrow^2_{com} \hat{r}; \hat{m}; K \triangleright t \\
\end{align*}
\]

**SQ-WHILE-DIV**

\[
\begin{align*}
\mu & \vdash \langle e, r, m, K \rangle \Downarrow^2_{exp} (v_0 \mid v_1) \quad c_{left} = (v_0 == 1)?\text{skip} : c \\
\mu & \vdash \langle c_{left}, [r_1], [m_1], [K_1] \rangle \Downarrow r_1', m_1'; K_1 \triangleright t_1' \\
\mu & \vdash \langle c_{right}, [r_2], [m_2], [K_2] \rangle \Downarrow r_2', m_2'; K_2 \triangleright t_2' \\
\hat{r} & = \text{merge}(r_1', r_2') \quad \hat{m} = \text{merge}(m_1', m_2') \\
\mu & \vdash \langle \text{while} \ e \ do \ c, r, m, K \rangle \Downarrow^2_{com} \hat{r}; \hat{m}; K \triangleright t \\
\end{align*}
\]

**SQ-CALL-DIV**

\[
\begin{align*}
\mu & \vdash \langle e, r, m, K \rangle \Downarrow^2_{exp} (\lambda^x.c_1 \mid \lambda^x.c_2) \\
\mu & \vdash \langle c_1, [r_1], [m_1], [K_1] \rangle \Downarrow r_1', m_1'; K_1 \triangleright t_1' \\
\mu & \vdash \langle c_2, [r_2], [m_2], [K_2] \rangle \Downarrow r_2', m_2'; K_2 \triangleright t_2' \\
\hat{r} & = \text{merge}(r_1', r_2') \quad \hat{m} = \text{merge}(m_1', m_2') \\
\mu & \vdash \langle \text{call}(e), r, m, K \rangle \Downarrow^2_{com} \hat{r}; \hat{m}; K \triangleright t \\
\end{align*}
\]

Figure 17 defines the projection on pairs of locations, registers, traces and kill sets for \(i \in \{1, 2\}\). Recall that \(\alpha\) ranges over events.

For notation simplicity, we define the command projection of an IMPE² trace to represent the changes made by the attacker to the program in \(i^{th}\) projection.

\[
[t]_{i, cmd} = \left( [t]_i \right)_{cmd}
\]
\text{merge}(m_1, m_2, l) = \begin{cases} v & \text{if } m_1(l) = m_2(l) = v \\ (v_1 | v_2) & \text{if } m_i(l) = v_i \text{ and } v_1 \neq v_2 \end{cases}

\text{merge}(m_1, m_2) = m \mid \forall l \in \text{Loc. } m(l) = \text{merge}(m_1, m_2, l)

\text{merge}(r_1, r_2, x) = \begin{cases} v & \text{if } r_1(x) = r_2(x) = v \\ (v_1 | v_2) & \text{if } r_i(x) = v_i \text{ and } v_1 \neq v_2 \end{cases}

\text{merge}(r_1, r_2) = r \mid \forall x \in \text{Vars. } r(x) = \text{merge}(r_1, r_2, x)

\begin{align*}
\text{merge}(t_1, t_2) &= \begin{cases} (\alpha_1 | \alpha_2) \cdot \text{merge}(t'_1, t'_2) & \text{if } t_i = \alpha_i \cdot t'_i \land \\
(\alpha_1 | \epsilon) \cdot \text{merge}(t'_1, \epsilon) & \text{if } t_1 = \alpha_1 \cdot t'_1 \land \\
(\epsilon | \alpha_2) \cdot \text{merge}(\epsilon, t'_2) & \text{if } t_2 = \alpha_2 \cdot t'_2 \land \\
\epsilon & \text{o.w} \end{cases} \\
\text{merge}(K_1, K_2) &= (K_1 | K_2)
\end{align*}

\textbf{Figure 16.} Definition of \text{merge}

\begin{align*}
[r]_i(x) &= \begin{cases} v & \text{if } r(x) = v \\ v_i & \text{if } r(x) = (v_1 | v_2) \end{cases} \\
[m]_i(x) &= \begin{cases} v & \text{if } m(x) = v \\ v_i & \text{if } m(x) = (v_1 | v_2) \end{cases} \\
[t]_i &= \begin{cases} \text{Mem}([m]_i) \cdot [t']_i & \text{if } t = \text{Mem}(m) \cdot t' \\ \text{Decl}(e, [m]_i) \cdot [t']_i & \text{if } t = \text{Decl}(e, m) \cdot t' \\ \text{Out}(\ell, [v]_i) \cdot [t']_i & \text{if } t = \text{Out}(\ell, v) \cdot t' \\ A(c) \cdot [t']_i & \text{if } t = A(c) \cdot t' \\ A(c_1 \equiv c_2) \cdot [t']_i & \text{if } t = A(c_1 \equiv c_2) \cdot t' \\ \alpha_i \cdot [t']_i & \text{if } t = (\alpha_i | \alpha_2) \cdot t' \\ \epsilon & \text{o.w} \end{cases} \\
[K]_i &= K_i \text{ if } K = (K_1 | K_2)
\end{align*}

\textbf{Figure 17.} Definition of projections

\text{protected}(p, S) = \begin{cases} \text{true} & \text{if } p = H \text{ or } \top \\ \text{true} & \text{if } p = L \text{ and } cnd \in S \\ \text{true} & \text{if } p = \ell_1 \text{ and } cnd \in S \\ \text{false} & \text{o.w} \end{cases}
E.1.1 Adequacy

The language IMP² is adequate for reasoning about executions of two IMPE programs. We show that execution of IMP² program is sound, (large-step by a IMP² program corresponds to large-steps taken by its projections) and complete (given two IMPE executions, there exists an IMP² execution).

**Lemma 1 (IMP² is Sound).** If \( \mu \vdash \langle c, r, m, K \rangle \cdot \downarrow_{\text{com}}^2 r^*; m^*; K^* \triangleright t^* \cdot \), then \( \mu \vdash \langle c, [r]_i, [m]_i, [K]_i \rangle \downarrow \langle r^*_i \rangle_i; \langle m^*_i \rangle_i; \langle K^*_i \rangle_i \triangleright \langle t^*_i \rangle_i \) for \( i \in \{1, 2\} \).

**Proof Sketch.** Proof is by induction on the derivation of \( \mu \vdash \langle c, r, m, K \rangle \cdot \downarrow_{\text{com}}^2 r^*; m^*; K^* \triangleright t^* \cdot \).

**Lemma 2 (IMP² is Complete).** If \( \mu \vdash \delta \langle c, [r]_i, [m]_i, [K]_i \rangle \downarrow \langle r^*_i \rangle_i; m^*_i; K^*_i \triangleright t^*_i \cdot \), then \( \exists \langle r^*, m^*, K^*, t^* \rangle \), such that \( \mu \vdash \langle c, r, m, K \rangle \cdot \downarrow_{\text{com}}^2 r^*; m^*; K^* \triangleright t^* \cdot \) and \( \langle [r^*_i]_i, [m^*_i]_i, [K^*_i]_i, [t^*_i]_i \rangle = \langle r^*_i, m^*_i, K^*_i, t^*_i \rangle \) for \( i \in \{1, 2\} \).

**Proof Sketch.** We use induction on the derivation of \( \mu \vdash \delta \langle c, [r]_i, [m]_i, [K]_i \rangle \downarrow \langle r^*_i \rangle_i; m^*_i; K^*_i \triangleright t^*_i \cdot \) and prove that we can construct \( r^*, m^*, K^* \) and \( t^* \) such that \( \langle [r^*_i]_i, [m^*_i]_i, [K^*_i]_i, [t^*_i]_i \rangle = \langle r^*_i, m^*_i, K^*_i, t^*_i \rangle \) for \( i \in \{1, 2\} \).

Interesting cases are SQ-IF-DIV, SQ-WHILE-DIV and SQ-CALL-DIV. We give intuition for SQ-IF-DIV, the rest follow the same argument.

Given \( \mu \vdash \delta \langle e \rangle \) then \( c_1 \) else \( c_2, [r]_i, [m]_i, [K]_i \) \( \downarrow \langle r^*_i \rangle_i; m^*_i; K^*_i \triangleright t^*_i \cdot \). Let

\[
\begin{align*}
r^* &= \text{merge}(r^*_1, r^*_2) \\
m^* &= \text{merge}(m^*_1, m^*_2) \\
t^* &= \text{merge}(t^*_1, t^*_2) \\
K^* &= \text{merge}(K^*_1, K^*_2)
\end{align*}
\]

From the premise of SQ-IF-DIV, we thus have \( \mu \vdash \langle c, r, m, K \rangle \cdot \downarrow_{\text{com}}^2 r^*; m^*; K^* \triangleright t^* \cdot \).

E.1.2 IMP² Type System

Let \( S \) be the set of conditions set during some observed trace \( t_{obs} \), \( H \) be the set of escape hatches in the observed trace and \( m_0 \) be the initial IMP² memory. The IMP² type system is parametrized by \( \delta, S, H \) and \( m_0 \). The typing judgment for commands and expressions is shown below.

\[
pc, \mu, \Gamma, K, U \vdash_{S,H,m_0} c : \Gamma', K' \\
\mu, \Gamma \vdash_{S,H,m_0} e : \sigma_p
\]

The typing rules are similar to Figure 8 with 2 extra rules for typing configurations shown in Figure 18. Rule T-SQ-CONFIG says that a configuration \( \langle c, r, m, K \rangle \) is well-typed (or is ok) if:

- Command \( c \) is well-typed;
- all conditions in set \( U \) are unused;
- Security policy on any register mapped to a paired value is protected;
- Security policy on any location (that does not belong to the set of conditions) mapped to a paired value is protected;
- Evaluation of an escape hatch with current memory results in a value that is same as evaluating it with initial memory;
- Kill sets are same on both sides of the executions.

Rule T-SQ-VALUE says when a final configuration \( \langle r, m, K \rangle \) is well-typed (or is ok) and is similar to rule T-SQ-CONFIG.

Lemma 3 proves that if an IMPE program is well-typed according to IMP² type system, then for also well-typed according to IMP² type system.

**Lemma 3 (Type System).** If \( L, N, \Gamma, \emptyset, 0 \vdash_\delta c : \Gamma', K' \), then \( pc, \mu, \Gamma, K, U \vdash_{S,H,m_0} c : \Gamma', K' \).

**Proof.** Proof is by straight forward induction on the derivation of the typing judgment \( \mu, \Gamma \vdash_{\delta} e : \sigma_p \).

**Lemma 4 (Value Type Preservation).** If \( \mu, \Gamma \vdash_{S,H,m_0} e : \sigma_p \) and \( \mu \vdash \langle e, r, m, K \rangle \cdot \downarrow_{\text{exp}}^2 v \), then \( \mu, \Gamma \vdash_{S,H,m_0} v : \sigma_p \).

**Proof.** Proof is by straight forward induction on the derivation of the typing judgment \( \mu, \Gamma \vdash_{S,H,m_0} e : \sigma_p \).
Lemma 5 (IMPE\textsuperscript{2} Final Configuration Preservation). Let \( \Gamma \) be an environment that is well-typed for \( \delta \), \( H \) be the set of escape hatches and \( m_0 \) be the initial IMPE\textsuperscript{2} memory such that \( \ell \in \{ \text{locations}(e) \mid e \in H \} \), \( m_0(l) \neq (v_1 \mid v_2) \), i.e., not a pair value. If \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle e, r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle e, r, m, K \rangle \ast \parallel_{exp} v \) such that \( \mu \vdash \langle e, r, m, K \rangle \parallel_{exp} v \) and \( \mu = \langle e, r, m, K \rangle \ast \parallel_{exp} v \). We have to prove that \( \Gamma' \vdash_{δSHmio} \langle \ell', r', m' \rangle : \Gamma' \bullet ok \).

Proof. The proof is by induction on the derivation of the large-step \( \mu \vdash \langle e, r, m, K \rangle \ast \parallel_{exp} v \). For each case, we prove that the final configuration preserves the well-typedness of IMPE\textsuperscript{2} value configuration.

**Case SQ-Skip:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle \text{skip}, r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle \text{skip}, r, m, K \rangle \ast \parallel_{exp} v \). Configuration is not changed.

**Case SQ-Assign:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle x := e, r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle x := e, r, m, K \rangle \ast \parallel_{exp} v \) such that \( \mu \vdash \langle x := e, r, m, K \rangle \ast \parallel_{exp} v \) and \( r' = r[x \mapsto v] \). We have to prove that \( \Gamma' \vdash_{δSHmio} \langle \ell', r', m' \rangle : \Gamma' \bullet ok \).

From the initial configuration, we have \( \Gamma \vdash_{δSHmio} \langle r, m, K \rangle : \mu \vdash \langle e, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Hence proved.

**Case SQ-Declassify:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle \text{declassify}(e), e, r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle \text{declassify}(e), e, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Such that \( \mu \vdash \langle \text{declassify}(e), e, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \) and \( r = r[x \mapsto v] \). Also expression \( e \) has no variables syntactically present (large-step hasNoVars(e)), We have to prove that \( \Gamma' \vdash_{δSHmio} \langle r', m', K \rangle : \Gamma' \bullet ok \).

From the initial configuration, we have \( \Gamma \vdash_{δSHmio} \langle r, m, K \rangle : \mu \vdash \langle e, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Register files \( r \) and \( r' \) differ only in variable \( x \). Let \( v = (v_1 \mid v_2) \). If \( \mu = \langle e, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \), we have \( \Gamma' \vdash_{δSHmio} \langle r', m', K \rangle : \Gamma' \bullet ok \). Hence proved.

**Case SQ-Update:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle e_1 \leftarrow e_2, r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle e_1 \leftarrow e_2, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Such that \( \mu \vdash \langle e_1 \leftarrow e_2, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \) and \( m' = m[l \mapsto v] \). We have to prove that \( \Gamma \vdash_{δSHmio} \langle r', m', K \rangle : \Gamma' \bullet ok \).

From the premise of T-Sq-Config, we have \( \Gamma \vdash_{δSHmio} \langle r, m, K \rangle : \mu \vdash \langle e_1 \leftarrow e_2, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \) and \( \mu = \langle e_1 \leftarrow e_2, r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Hence proved.

**Case SQ-Output:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle \text{output}(e \to \ell), r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle \text{output}(e \to \ell), r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). From the premise of T-Sq-Config, we have \( \Gamma \vdash_{δSHmio} \langle r, m, K \rangle : \Gamma' \bullet ok \). Large-step does not modify register file, memory or killset. Hence proved.

**Case SQ-SetCnd:** Given \( pc, \mu, \Gamma, U \vdash_{δSHmio} \langle \text{set}(cnd), r, m, K \rangle : \Gamma', K' \bullet ok \) and \( \mu \vdash \langle \text{set}(cnd), r, m, K \rangle \ast \parallel_{exp} v \ast \parallel_{exp} \). Such that \( m' = m[cnd \mapsto 1] \). We have to prove that \( \Gamma \vdash_{δSHmio} \langle r, m', K \rangle : \Gamma' \bullet ok \). From the premise
of T-SQ-CONF, we have $\Gamma \vdash_{\text{SHio}} \langle r, m, K \rangle \cdot \text{ok}$. Since $m$ and $m'$ do not differ (set(cnd) always sets cnd to a non-pair value), we have $\Gamma \vdash_{\text{SHio}} \langle r, m', K \rangle \cdot \text{ok}$.

**Case SQ-KILL:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{killed}(i), r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $N \vdash \langle \text{killed}(i), r, m, K \rangle : \eta^2_{\text{com}} r; m; K \cup \{E_1\} \triangleright \star$. We have to prove that $\Gamma \vdash_{\text{SHio}} \langle r, m, K \cup \{E_1\} \rangle \cdot \text{ok}$. From the premise of T-SQ-CONF, we have $\Gamma \vdash_{\text{SHio}} \langle r, m, K \rangle \cdot \text{ok}$. Since $[K_1] = [K_2]$, we therefore have $[K \cup \{E_1\}]_1 = [K \cup \{E_1\}]_2$. Hence proved.

**Case SQ-SEQ:** Given $pc, N, \Gamma, U \vdash_{\text{SHio}} \langle c_1; \ldots; c_n, r_0, m_0, K_0 \rangle : \Gamma', K' \cdot \text{ok}$ and $N \vdash \langle c_1; \ldots; c_n, r_0, m_0, K_0 \rangle : \eta^2_{\text{com}} r_n; m_n; K_n \triangleright t_n$. We have to prove that $N, \Gamma_n \vdash_{\text{SHio}} \langle r_n, m_n, K_n \rangle \cdot ok$. From the premise of T-NSQ-CONF, we have $\Gamma_0 \vdash_{\text{SHio}} \langle r_0, m_0, K_0 \rangle \cdot ok$ and $pc, \mu, \Gamma_i, U \vdash_{\text{SHio}} c_i : \Gamma_i, K_i$ for $i \in \{1; \ldots; n\}$. Applying induction hypothesis, we thus have $\mu, \Gamma_i \vdash_{\text{SHio}} \langle r_1, m_1, K_1 \rangle \cdot ok$. Since the types of locations are fixed throughout the program, we have that if $\Gamma_0$ is well-typed for $\delta$ then $\Gamma_1$ is also well-typed for $\delta$. Applying induction hypothesis continuously, we thus have $N, \Gamma_n \vdash_{\text{SHio}} \langle r_n, m_n, K_n \rangle \cdot ok$.

**Case SQ-ENAMEL:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{envelope}(i, c), r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $N \vdash \langle \text{envelope}(i, c), r, m, K \rangle : \eta^2_{\text{com}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. From the premise of T-SQ-CONF, we have $\Gamma \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. Also, $\mu, \Gamma, \Gamma \vdash_{\text{SHio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$. Applying induction hypothesis, we thus have $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$.

**Case SQ-IF-ELSE:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \eta^2_{\text{com}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. From the premise of T-SQ-CONF, we have $\Gamma \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. Also, $\mu, \Gamma, \Gamma \vdash_{\text{SHio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$. Also, $\mu \vdash \langle c, r, m, K \rangle : \eta^2_{\text{com}} r'; m'; K' \triangleright t'$. Applying induction hypothesis, we thus have $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$.

**Case SQ-WHILE:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{while } e \text{ do } c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle \text{while } e \text{ do } c, r, m, K \rangle : \eta^2_{\text{exp}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. Also, $\mu, \Gamma, \Gamma \vdash_{\text{SHio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$. Also, $\mu \vdash \langle c, r, m, K \rangle : \eta^2_{\text{exp}} r'; m'; K' \triangleright t'$. Applying induction hypothesis, we thus have $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$.

**Case SQ-CALL:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{call}(c), r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle \text{call}(c), r, m, K \rangle : \eta^2_{\text{exp}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. Also, $\mu, \Gamma, \Gamma \vdash_{\text{SHio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\Gamma, \Gamma \vdash_{\text{SHio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$. Applying induction hypothesis, we thus have $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$.

**Case SQ-IF-DIV:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{if } e \text{ then } c_0 \text{ else } c_1, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle \text{if } e \text{ then } c_0 \text{ else } c_1, r, m, K \rangle : \eta^2_{\text{exp}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. From the initial configuration, we have $pc, \mu, \Gamma, K, U \vdash_{\text{SHio}} c_i : \Gamma', K' \cdot \text{ok}$. Applying induction hypothesis, we have $\mu \vdash \langle e, r, m, K \rangle : \eta^2_{\text{exp}} (v_0 \mid v_1)$. So protected(p,S) and protected(pc,S).

Let $z$ be such that $\hat{r}(z) = (v_1 \mid v_2)$. If $\Gamma'(z) = \sigma_q$, then either $\Gamma'(z) = \sigma_q$ or there is an assignment to $z$ in $c_i$ for some $i \in \{0; 1\}$. If the former holds, then we already have protected(q,S). If the latter holds, then we have protected(q,S) (because an assignment is at least as restrictive as pc'). Let $\tilde{m}(l) = (v_1 \mid v_2)$ and $\Gamma'(l) = \sigma_q$. Since the type of location is invariant throughout the program, from the initial configuration we have protected(q,S). A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. Since both branches $c_0$ and $c_1$ have same kislets, we have $K_1 = K_2$. So $[K_1] = [K_2]$. Hence proved.

**Case SQ-WHILE-DIV:** Given $pc, \mu, \Gamma, U \vdash_{\text{SHio}} \langle \text{while } e \text{ do } c, r, m, K \rangle : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle \text{while } e \text{ do } c, r, m, K \rangle : \eta^2_{\text{exp}} r'; m'; K' \triangleright t'$. We have to prove that $\Gamma' \vdash_{\text{SHio}} \langle r', m', K' \rangle \cdot \text{ok}$. From the initial configuration, we have $pc, \mu, \Gamma, K, U \vdash_{\text{SHio}} c_i : \Gamma', K' \cdot \text{ok}$. Applying induction hypothesis, we have $\mu \vdash \langle e, r, m, K \rangle : \eta^2_{\text{exp}} (v_0 \mid v_1)$. So protected(p,S) and protected(pc,S). Let $z$ be such that $\hat{r}(z) = (v_1 \mid v_2)$. If $\Gamma'(z) = \sigma_q$, then either $\Gamma'(z) = \sigma_q$ or there is a pair value, and $\Gamma'(z) = \sigma_q$, then from the well-typedness, $pc, \mu, \Gamma, K, U \vdash_{\text{SHio}} c_i : \Gamma', K' \cdot \text{ok}$ and $\mu \vdash \langle e, r, m, K \rangle : \eta^2_{\text{exp}} (v_0 \mid v_1)$. So protected(p,S) and protected(pc,S) (because an assignment is at least as restrictive as pc'). Let $\tilde{m}(l) = (v_1 \mid v_2)$ and $\Gamma'(l) = \sigma_q$. Since the type of location is invariant throughout the program, from the initial configuration we have protected(q,S). A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. Killsets are unmodified. So $[K_1] = [K_2]$. Hence proved.
Case SQ-CALL-DIV: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle \text{call}(e), r, m, K \rangle : \Gamma', K' \cdot \text{ok} \) and \( \mu \vdash \langle \text{call}(e), r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t}. \)
We have to prove that \( \Gamma \vdash_{\text{ASH-nio}} \langle \hat{r}, \hat{m}, K \rangle \cdot \text{ok} \).

From the initial configuration, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \), \( \mu, \Gamma \vdash_{\text{ASH-nio}} e : (\Gamma', K', U \overset{p}{\rightarrow} \Gamma'^*, K'^*) \) and so \( p, \mu, \Gamma, K', U \vdash_{\text{ASH-nio}} c : \Gamma', K'^* \) such that \( K = K', K' = K^* \) and \( \Gamma = \Gamma'^* = \Gamma^* \). From the premise of SQ-CALL-DIV, we have \( \mu \vdash \langle r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t}. \)

Let \( z \) be such that \( \hat{r}(z) = (v_1 \mid v_2) \). If \( \hat{r}(z) = (v_1 \mid v_2) \) and \( \Gamma(z) = \sigma_y \), then from the premise of T-Sq-CONF, we already have \( \text{protected}(y, S) \). If \( \hat{r}(z) \neq (v_1 \mid v_2) \) i.e., not a pair value, and \( \Gamma(z) = \sigma_y \), then from the well-typedness \( p, \mu, \Gamma, K', U \vdash_{\text{ASH-nio}} c : \Gamma', K'^* \) we have \( \text{protected}(p, S) \) and so \( \text{protected}(y, S) \) (because an assignment is atleast as restrictive as \( p \)). Similarly, let \( \hat{m}(l) = (v_1 \mid v_2) \) and \( \Gamma(l) = \sigma_y \). Since the type of location is invariant throughout the program, from the initial configuration we have \( \text{protected}(y, S) \). A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. From the function type, post killsets are same. So \( \hat{K}_1 = \hat{K}_2 \). Hence proved.

Lemma 6 (IMP2 Type Preservation). Let \( \Gamma \) be a well-formed typing context and \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok} \). If \( \mu' \vdash \langle c', r', m', K' \rangle \cdot \mathbb{L}_{\text{com}} \hat{r'}; \hat{m'}; \hat{K'} \gg \hat{t'} \) is an immediate (command) premise in the evaluation of \( \mu \vdash \langle c, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \), then \( \exists \hat{p}, \Gamma, \Gamma', U \), \( \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \) to \( \mathbb{L}_{\text{com}} \hat{r'}; \hat{m'}; \hat{K'} \gg \hat{t'} \). Hence \( \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

Proof. The proof is by induction on the derivation of the large step \( \mu \vdash \langle c, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \). Since rules SQ-ASSIGN, SQ-SKIP, SQ-UPDATE, SQ-OUTPUT, SQ-SETCND, SQ-IF-DIV, SQ-WHILE-DIV and SQ-CALL-DIV do not have IMP2 command premises, the only relevant cases are SQ-ENCLAVE, SQ-IF, SQ-WHILE, SQ-SEQ, SQ-CALL.

Case SQ-ENCLAVE: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle \text{enqueue}(i, c), r, m, K \rangle : \Gamma', K' \cdot \text{ok} \).

From the premises of T-Sq-CONF, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \) and \( pc, \mu, \Gamma, \emptyset \vdash_{\text{ASH-nio}} c : \Gamma', K' \cdot \text{ok} \). From the premises of the IMP2 large-step, we have \( E_i \vdash \langle c, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \). Hence \( pc, \emptyset, \Gamma, E_i \vdash_{\text{ASH-nio}} \langle c, r, m, K \rangle : \Gamma', K' \cdot \text{ok} \).

Case SQ-IF: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle \text{if} e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \Gamma', K' \cdot \text{ok} \).

From the premises of the IMP2 large-step, we have \( \mu \vdash \langle c_1, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \) and \( \mu \vdash \langle c_2, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

From the premises of T-Sq-CONF, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \) and \( pc, \mu, \Gamma, K, U \vdash_{\text{ASH-nio}} c_1 : \Gamma', K' \cdot \text{ok} \).

Case SQ-WHILE: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle \text{while } e \text{ do } c, r, m, K \rangle : \Gamma, K \cdot \text{ok} \).

From the premises of the IMP2 large-step, we have \( \mu \vdash \langle c, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \) and \( \mu \vdash \langle \text{while } e \text{ do } c', r', m', K' \rangle \cdot \mathbb{L}_{\text{com}} \hat{r'}; \hat{m'}; \hat{K}' \gg \hat{t'} \).

From the premises of T-Sq-CONF, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \) and \( pc, \mu, \Gamma, K, U \vdash_{\text{ASH-nio}} c' : \Gamma', K' \cdot \text{ok} \).

Case SQ-CALL: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle \text{call}(e), r, m, K \rangle : \Gamma', K' \cdot \text{ok} \).

From the premises of the IMP2 large-step, we have \( \mu \vdash \langle e, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{e}; \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

From the premises of T-Sq-CONF, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \) and \( \mu \vdash \langle \text{call}(e), r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{e}; \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

Case SQ-SEQ: Given \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle c_1, c_2, r, m, K \rangle : \Gamma, K \cdot \text{ok} \).

From the premises of the IMP2 large-step, we have \( \mu \vdash \langle c_2, r, m, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{c}_2; \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

From the premises of T-Sq-CONF, we have \( \Gamma \vdash_{\text{ASH-nio}} \langle r, m, K \rangle \cdot \text{ok} \) and \( \mu, \Gamma, \Gamma, 1_{i-1}, 1_i \cdot \mathbb{L}_{\text{com}} \hat{c}_i; \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

We also have \( \mu \vdash \langle c_1, r_1, m_1, K_1 \rangle \cdot \mathbb{L}_{\text{com}} \hat{c}_1; \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

Hence \( pc, \mu, \Gamma, U \vdash_{\text{ASH-nio}} \langle c_1, c_2, r, m, K \rangle : \Gamma, K \cdot \text{ok} \).

Proof. Given \( L, \mu, \Gamma, K, \emptyset \vdash c : \Gamma', K' \).

Let \( m_1 \) be some initial memory for which \( N \vdash \langle e, r, \text{init}, m_1, K \rangle \cdot \mathbb{L}_{\text{com}} \hat{r}; \hat{m}; \hat{K} \gg \hat{t} \).

We note that the attacker actually observes only low-events i.e. \( \{ t_{\text{obs}} \}_L \). We need to show that \( k^{\hat{q}}_L(c, t_{\text{obs}}) \geq M \).
where

\[
M = \bigg( \bigcap_{m' \in [t_{\text{obs}}]_m} \text{ind}_t(m_0, \gamma, \{ \text{cnd} | m'(\text{cnd}) = 0 \}) \bigg) \cap \bigg( e' = \bigcap_{(e', m') \in [t_{\text{obs}}]_{e}} \text{Exc}_{\text{kind}}(m_0, m', e') \bigg)
\]

Let \( S \) be the set of conditions that are set at the beginning of \( t_{\text{obs}} \), i.e., \( S = \{ \text{cnd} | m'(\text{cnd}) = 1 \} \). If \( Q \setminus S \) represents the set of all condition variables, then \( Q \setminus S \) is the set of conditions that are unset at some time during the observed trace. Also let \( H \) be the set of all escape hatches that are declassified till the last event of \( t_{\text{obs}} \) i.e. \( H = \{ e | (e, m) \in [t \cdot t_{\text{obs}}]_e \} \).

Let \( m_2 \in M \). Also let \( N \vdash \delta \langle c, r_{\text{init}}, m_2, K \rangle \Downarrow r'_2; m'_2; K'_2 \triangleright t_2 \). To ensure \( k^\delta_L(c, t_{\text{obs}}) \supseteq M \), we need to show that \( m_2 \in k^\delta_L(c, t_{\text{obs}}) \).

Note that \( m_1 \) and \( m_2 \) differ only in locations with policies that are protected by set \( S \). That is, for all locations \( l \in \text{Loc} \), if \( m_1(l) \neq m_2(l) \) then \( \Gamma(l) = \sigma_p \implies \text{protected}(p, S) \). Why? Suppose for some \( l \), s.t \( \Gamma(l) = (\sigma_p, rt) \) let \( m_1(l) \neq m_2(l) \) and \( \neg\text{protected}(p, S) \). So, \( p = L \) or \( L \land l_2 \) s.t. \( \text{cnd} \notin S \). Then for some \( m_j \in M \), we have \( m_1(l) = m_j(l) \). Since \( M \) is computed by the intersection of all such memories, every memory \( m'' \in M \) should satisfy \( m''(l) = m_1(l) \). This implies \( m_2(l) = m_1(l) \) which is a contradiction. Thus \( \text{protected}(p, S) \) must hold.

Also note that \( m_1 \) and \( m_2 \) satisfy

\[
\forall e \in H, \mu \vdash \delta \langle c, r_{\text{init}}, m_1, K \rangle \Downarrow v \iff \mu \vdash \delta \langle c, r, m_2, K \rangle \Downarrow v
\]

We will construct an IMPE execution that represents the IMPE executions starting from \( m_0 \) and \( m_2 \). Type-preservation of IMPE \( (\text{Lemma 5}) \) will ensure that both executions produce the same observable trace, thus showing that \( m_2 \in k^\delta_L(c, t_{\text{obs}}) \).

Let IMPE \( m = \text{merge}(m_1, m_2) \). If \( \mu \vdash \langle c, r_{\text{init}}, m, K \rangle \Downarrow^2 \langle r^*, \sigma'_e; \mu \rangle \triangleright t^* \), by the adequacy of IMPE \( (\text{Lemma 2}) \), we have that the IMPE execution represents IMPE executions with \( m_1 \) and \( m_2 \) as initial memories.

Let \( t^* = t^*_{\text{pre}} \cdot t^*_{\text{obs}} \cdot t^*_{\text{post}} \) for some \( t^*_{\text{obs}} \) such that \( \{ t^*_{\text{obs}} \}_1 = t_{\text{obs}} \). Define observation overlapped by an IMPE \( t^* \) trace \( t'^* \) as:

\[
\text{obsOverlap}(t'^*, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}}) = \begin{cases} 
\emptyset & \text{if } t'^* \subleq t^*_{\text{pre}} \\
\{ \langle c, r_{\text{init}}, m, \emptyset \rangle : \Gamma', K', \text{ok} \} & \text{if } t^*_{\text{pre}} \cdot t^*_{\text{obs}} \subleq t'^* \leq t^*_{\text{post}} \\
\text{obsOverlap}(t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}}) & \text{if } t'^* = t^*_{\text{pre}} \cdot t^*_{\text{obs}} \text{ and } t'^* \subleq t^*_{\text{obs}}
\end{cases}
\]

Intuitively, \( \text{obsOverlap}(t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}}) \) defines part of input trace \( t' \) that overlaps with an observed trace \( t^*_{\text{obs}} \).

Since \( L, \mu, \Gamma, K, \emptyset \vdash c : \Gamma', K' \), we have \( L, \mu, \Gamma, K, \emptyset \vdash_\delta(h_{\text{init}}, c : \Gamma', K') \). Note that our initial configuration satisfies

\[
L, N, \Gamma, \emptyset \vdash_\delta h_{\text{init}} \langle c, r_{\text{init}}, m, \emptyset \rangle : \Gamma', K', \text{ok}
\]

**Lemma 7** (Observational Equivalence is Preserved). Let \( S \) be the set of conditions that are set(non-zero) in some observed trace \( t_{\text{obs}} \). If \( pc, \mu, \Gamma, U \vdash_\delta h_{\text{init}} \langle c, r, m, K \rangle : \Gamma', K' \text{ ok and } \mu \vdash \langle c, r, m, K \rangle \Downarrow^2 \langle r^*, \mu \rangle \Downarrow \hat{\ell} \), then

\[
|t_{\text{pre}}| \Downarrow_{\text{obs}} |t_{\text{obs}}| \Downarrow_{\text{obs}} |t_{\text{post}}|
\]

**Proof.** The proof follows by induction on the derivation of \( \mu \vdash \langle c, r, m, K \rangle \Downarrow^2 \langle r^*, \mu \rangle \Downarrow \hat{\ell} \).

- **Case Sq-Skip:** Emitted trace is empty.
- **Case Sq-Assign:** Emitted trace is empty.
- **Case Sq-Declassify:** Emitted trace does not include out event.
- **Case Sq-Update:** Emitted trace is empty.
- **Case Sq-Kill:** Emitted trace is empty.
- **Case Sq-SetCnd:** Emitted trace does not include out event.
- **Case Sq-Output:** Given \( pc, \mu, \Gamma, U \vdash_\delta h_{\text{init}} \langle c, r, m, K \rangle : \Gamma', K' \text{ ok } \) and \( \mu \vdash \langle c, r, m, K \rangle \Downarrow^2 \langle r^*, \mu \rangle \Downarrow \hat{\ell} \). Let \( \hat{\ell} = \text{Mem}(m) \cdot \text{Out}(r, v) \). From the premise of T-Sq-CONFIG, we have \( pc, \mu, \Gamma, K, U \vdash_\delta \text{output } e \text{ to } \ell \). Then \( \mu \vdash_\delta \sigma_p, \text{cur}(pc, U) \cup \text{cur}(pc, U) \cup \ell \).
- **Case v = \langle v_1 | v_2 \rangle:** We have \( \text{protected}(p, S) \) and so \( \ell \neq L \).
- **Case v \neq \langle v_1 | v_2 \rangle:** In this case \( \ell = \{ L, H \} \).
In the both the cases, we have

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

**Case Sq-If-Else:** Given \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K) : \Gamma', K' \mathrel{\text{ok}} \) and \( \mu \vdash \langle e, r, m, K\rangle \Downarrow v \) such that \( v \) is not a pair, applying induction hypothesis to the premises of \( \text{SQ-IF-ELSE} \) gives us

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

**Case Sq-While-Div:** Given \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{while } e \text{ do } c, r, m, K) : \Gamma', K' \mathrel{\text{ok}} \) and \( \mu \vdash \langle e, r, m, K\rangle \Downarrow v \) such that \( v \) is not a pair, applying induction hypothesis to the premises of \( \text{SQ-WHILE} \) gives us

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

From Lemma 6 we have \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{while } e \text{ do } c', r', m', K) : \Gamma, K \mathrel{\text{ok}} \). Applying induction hypothesis to \( \mu \vdash \langle e, r', m', K\rangle \Downarrow v \) such that \( v \) is not a pair, applying induction hypothesis to the premise of \( \text{SQ-CALL} \) gives us

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

**Case Sq-If-Div:** Given \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{if } e \text{ then } c, r, m, K) : \Gamma', K' \mathrel{\text{ok}} \) and \( \mu \vdash \langle e, r, m, K\rangle \Downarrow v \) such that \( v \) is not a pair, applying induction hypothesis to the premises of \( \text{SQ-IF-DIV} \) gives us

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

**Case Sq-While-Seq:** Given \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{while } e \text{ do } c, r, m, K) : \Gamma, K \mathrel{\text{ok}} \). Applying induction hypothesis to \( \mu \vdash \langle e, r, m, K\rangle \Downarrow v \) such that \( v \) is not a pair, we have \( \text{protected}(p, S) \). From the well-typedness, \( \text{command } c \text{ does not emit any } out \text{ events to } L \text{ channel} \). Hence

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]

**Case Sq-Call-Div:** Given \( pc, \mu, \Gamma, U \vdash_{\text{SH}^{\text{Hrio}}} (\text{call}(e), r, m, K) : \Gamma', K' \mathrel{\text{ok}} \) and \( \mu \vdash \langle e, r, m, K\rangle \Downarrow v \) such that \( v \) is not a pair, we have \( \text{protected}(p, S) \). From the well-typedness, \( \text{command } c \text{ does not emit any } out \text{ events to } L \text{ channel} \). Hence

\[ |\text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_1 \approx_L \langle \text{obsOverlap}(\hat{t}, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})\rangle_2 \]
Case Sq-Enclave: Given $pc, \mu, \Gamma, U \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{R} \mathcal{m}_{\mathcal{O}}} (\text{enclave}(i, c), r, m, K) : \Gamma', K' \bullet \text{ok}$ and $N \vdash_{\delta} (\text{enclave}(i, c), r, m, K) \Downarrow r'; m'; K' \triangleright t'$. From the premises of T-Sq-CONFIG, we have $pc, \mu, \Gamma, K, U \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{R} \mathcal{m}_{\mathcal{O}}} \text{enclave}(i, c) : \Gamma', K'$. From Lemma 6 we have $pc, E_i, \Gamma, \emptyset \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{R} \mathcal{m}_{\mathcal{O}}} (c, r, m, K) : \Gamma', K' \bullet \text{ok}$. Applying induction hypothesis to the premise $E_i \vdash_{\delta} (c, r, m, K) \Downarrow r'; m'; K' \triangleright t'$

$$\left\{\text{obsOverlap}(t', t_{\text{pre}}^*, t_{\text{obs}}^*, t_{\text{post}}^*)\right\}_1 \approx L \left\{\text{obsOverlap}(t', t_{\text{pre}}^*, t_{\text{obs}}^*, t_{\text{post}}^*)\right\}_2$$

Since we have $L, N, \Gamma, \emptyset \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{R} \mathcal{m}_{\mathcal{O}}} (c, r_{\text{init}}, m, \emptyset) : \Gamma', K' \bullet \text{ok}$, applying Lemma 7 on $\mu \vdash (c, r_{\text{init}}, m, K) \bullet \approx_{\text{com}}^2 \mu_{\text{com}} r^*; m^*; K^* \triangleright t^*$, we have

$$\left\{\text{obsOverlap}(t^*, t_{\text{pre}}^*, t_{\text{obs}}^*, t_{\text{post}}^*)\right\}_1 \approx L \left\{\text{obsOverlap}(t^*, t_{\text{pre}}^*, t_{\text{obs}}^*, t_{\text{post}}^*)\right\}_2$$

Hence proved that $m_2 \in k_L^2(c, t_{obs})$.

We prove the second and third parts of Theorem 1 using similar approaches in Section E.1.3 and Section E.1.6.

E.1.3 Proofs for $N$-chaos Security

In this section we use a more permissive $N$-chaos type system and show that an IMPE program that is well-typed for the type system in Section 4 is also well-typed for $N$-chaos type system. Figure 19 presents the $N$-chaos type system. It relaxes the IMPE type system by unconstraining the commands running in normal mode. They can now read and write to memory locations with no restrictions on security policies. The new typing system relies on the guarantees provided by the operational semantics that a command running in normal mode does not access enclave memory. Typing rules for commands running in enclave mode are unchanged and are same as those presented in Figure 8.

Lemma 8 (Permissive Type System). If $L, N, \Gamma, \emptyset \vdash_{\delta} c : \Gamma', K'$, then $pc, \mu, \Gamma, K, U \vdash_{N\text{-chaos}} c : \Gamma', K'$.

Proof Sketch. Proof is by straightforward induction on the derivation of the typing judgment $\mu, \Gamma \vdash c : \sigma_p$.

E.1.4 IMPE$^{2N}$-chaos Adequacy

The language IMPE$^{2N}$-chaos is adequate for reasoning about executions of two IMPE programs. We show that the execution of IMPE$^{2N}$-chaos program using semantics $\Downarrow_{N}$ is sound (i.e., large-step taken by a IMPE$^{2N}$-chaos program corresponds to a large-step taken by either side of the execution) and complete (given two IMPE$^{2N}$-chaos executions, there exists an IMPE$^{2N}$-chaos execution).

Lemma 9 (IMPE$^{2N}$-chaos is Sound). If $\mu \vdash (c, r, m, K) \bullet \Downarrow_{N} r^*; m^*; K^* \triangleright t^*$, then $\mu \vdash_{\delta} (c, [r^*], [m^*], [K^*]) \Downarrow_{N\text{-chaos}} [r^*]_i ; [m^*]_i ; [K^*]_i \triangleright [t^*]_i$ for $i \in \{1, 2\}$.

Proof Sketch. Proof is by induction on the derivation of $\mu \vdash (c, r, m, K) \bullet \Downarrow_{\text{com}} r^*; m^*; K^* \triangleright t^*$.

Lemma 10 (IMPE$^{2N}$-chaos is Complete). Let $\mu \vdash_{\delta} (c, [r]_i ; [m]_i ; [K]_i) \Downarrow_{N\text{-chaos}} r^*_i ; m^*_i ; K^*_i \triangleright t^*_i$ such that $[t^*]_i \downarrow_{\text{cmd}} = [t^*]_{\downarrow_{\text{cmd}}}$. Then $\exists (r^*, m^*, K^*, t^*)$ such that $\mu \vdash (c, r, m, K) \bullet \Downarrow_{N} r^*; m^*; K^* \triangleright t^*$ and $\langle [r^*]_i ; [m^*]_i ; [K^*]_i ; [t^*]_i \rangle = \langle r^*_i, m^*_i, K^*_i, t^*_i \rangle$ for $i \in \{1, 2\}$.

Proof Sketch. Follows along the lines of proof of Lemma 2.

E.1.5 IMPE$^{2N}$-chaos N-chaos Type System

Let $\mathcal{S}$ be the set of conditions set during some observed trace $t_{obs}$, $\mathcal{H}$ be the set of escape hatches till the observed trace and $m_0$ be the initial IMPE$^{2N}$-chaos memory. The IMPE$^{2N}$-chaos type system is parametrized by $\delta$, $\mathcal{S}$, $\mathcal{H}$ and $m_0$. The typing judgment for commands and expressions is shown below.

$$pc, \mu, \Gamma, K, U \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{m}_{\mathcal{O}}} c : \Gamma', K'$$

$$\mu, \Gamma \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{m}_{\mathcal{O}}} c : \sigma_p$$

The typing rules are similar to Figure 19 with 2 extra rules for typing configurations shown in Figure 20.

Lemma 11 (Value Type Preservation). If $\mu, \Gamma \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{m}_{\mathcal{O}}} c : \sigma_p$ and $\mu \vdash (e, r, m, K) \Downarrow_{N\text{-chaos}} v$, then $pc, \mu, \Gamma, K, U \vdash_{\delta \mathcal{S} \mathcal{H} \mathcal{m}_{\mathcal{O}}} c : \Gamma', K'$.
Proof Sketch. Proof is by straightforward induction on the derivation of the typing judgment \( \mu, \Gamma \vdash_{\delta N} e : \sigma_p \).

Lemma 12 (Protected Expression). Let \( \mu, \Gamma \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} (r, m, K) \) ok. If \( \mu, \Gamma \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} e : \sigma_p \) and \( \mu \vdash \langle e, r, m, K \rangle \) ok \( v \) such that \( v = (v_1 | v_2) \) for some values \( v_1 \) and \( v_2 \), then \( \text{protected}(p, S) \) and \( \mu \neq N \).

Proof Sketch. Proof is by straightforward induction on the derivation of the typing judgment \( \mu, \Gamma \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} e : \sigma_p \).

Lemma 13 (IMPE\(^{2N}N_{\chi} \) N-chaos Final Configuration Preservation). Let \( \Gamma \) be an environment well-typed for \( \delta \), \( \mathcal{H} \) be the set of escape hatches and \( \mu_0 \) be the initial IMPE\(^{2N}N_{\chi} \) memory such that \( l \in \{\text{locations}(e) \mid e \in \mathcal{H}\} \), \( \mu_0(l) \neq (v_1 | v_2) \), i.e., not a pair value. If \( \mu, \Gamma, U \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} \langle r, m, K \rangle : \Gamma' \) ok and \( \mu \vdash \langle c, r, m, K \rangle \) ok \( v \) such that \( v = (v_1 | v_2) \) for some values \( v_1 \) and \( v_2 \), then \( \text{protected}(p, S) \) and \( \mu \neq N \).

Case NS\(\text{Q-Skip}\): Given \( pc, \mu, \Gamma, U \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} \langle \text{skip}, r, m, K \rangle : \Gamma' \) ok and \( \mu \vdash \langle \text{skip}, r, m, K \rangle \) ok. Configuration is not changed.

Case NS\(\text{Q-Assign}\): Given \( pc, \mu, \Gamma, U \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} \langle x := e, r, m, K \rangle : \Gamma \beta \text{ok} \) and \( \mu \vdash \langle x := e, r, m, K \rangle \) ok. Register files \( r \) and \( r' \) differ only in variable \( x \). Let \( v = (v_1 | v_2) \). From the well-typedness, we have \( \mu, \Gamma \vdash_{\delta S\delta H_{\mu_0}N_{\chi}} e : \sigma_p \). Applying Lemma 12 we have \( \text{protected}(p, S) \) and \( \mu \neq N \). Since \( \Gamma' = \Gamma[x \mapsto \sigma_{\text{pclp}}] \), we have \( \text{protected}(pc \cup q, S) \). Hence proved.
∀ c ∈ U, m(cn) = 0 pc, μ, Γ, K, U ⊢ δSHₘ₀Nₘₐₓ c : Γ', K'
∀ x ∈ Vars, r(x) = (v₁ | v₂) and Γ(x) = σ_p, \Rightarrow protected(p, S) and μ ≠ N
∀ l ∈ Loc \{ Cond, m(l) = (v₁ | v₂) and Γ(l) = (σ_p, rt), \Rightarrow protected(p, S) and δ(l) ≠ N
∀ e ∈ H, μ ⊢ ⟨e, r, m, K⟩ • \#₄ₚNₘₐₓ v \Rightarrow μ ⊢ ⟨e, r, m, K⟩ • \#₂ₚNₘₐₓ v

T-NSQ-CONFIG

∀ x ∈ Vars, r(x) = (v₁ | v₂) and Γ(x) = σ_p, \Rightarrow protected(p, S) and μ ≠ N
∀ l ∈ Loc \{ Cond, m(l) = (v₁ | v₂) and Γ(l) = (σ_p, rt), \Rightarrow protected(p, S) and δ(l) ≠ N
∀ e ∈ H, μ ⊢ ⟨e, r, m, K⟩ • \#₄ₚNₘₐₓ v \Rightarrow μ ⊢ ⟨e, r, m, K⟩ • \#₂ₚNₘₐₓ v

T-NSQ-VALUE

Case NSQ-DECLASSIFY: Given pc, μ, Γ, U ⊢ δSHₘ₀Nₘₐₓ (declassify(x)e, r, m, K) : Γ' • ok and μ ⊢ (declassify(x)e, r, m, K) • \#₄ₚNₘₐₓ r', \; m'; K > ω such that μ ⊢ ⟨e, r, m, K⟩ • \#₄ₚNₘₐₓ v and r' = r[x → v]. Also expression e has no variables syntactically present (large-step has the premise hasNoVars(e)). We have to prove that μ, Γ' ⊢ δSHₘ₀Nₘₐₓ (r', m', K) • ok.

From the initial configuration, we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok. Register files r and r' differ only for variable x. Let v = (v₁ | v₂) for some v₁ and v₂. We have Γ' = Γ[x → L]. From the well-typedness, we have allLocImmutable(e). Thus e ∈ H and so v ≠ (v₁ | v₂) (not a pair value).

Hence proved.

Case NSQ-UPDATE: Given pc, μ, Γ, U ⊢ δSHₘ₀Nₘₐₓ (e₁ ← e₂, r, m, K) : Γ' • ok and μ ⊢ (e₁ ← e₂, r, m, K) • \#₄ₚNₘₐₓ r, \; m'; K > ω such that μ ⊢ ⟨e₁, r, m, K⟩ • \#₄ₚNₘₐₓ l, μ ⊢ ⟨e₂, r, m, K⟩ • \#₄ₚNₘₐₓ v and m' = m[l → v]. We have to prove that μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok. From the premise of T-NSQ-CONFIG, we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok, μ, Γ ⊢ δSHₘ₀Nₘₐₓ e₁ : (σ_p ref'' q) and μ, Γ ⊢ δSHₘ₀Nₘₐₓ e₂ : σ_p' such that p' ∪ q ∪ pc ≤ p.

Case l = (l₁ | l₂), v = (v₁ | v₂): Applying Lemma[12] we have protected(p', S) and μ ≠ N. So protected(p, S). Since μ, Γ ⊢ δSHₘ₀Nₘₐₓ l : (σ_p ref'' l), from the well-typedness of environment, we have δ(l) = μ' ≠ N. Hence μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok.

Case l ≠ (l₁ | l₂), v ≠ (v₁ | v₂): Same as above.

Case l ≠ (l₁ | l₂), v ≠ (v₁ | v₂): Applying Lemma[12] we have protected(q, S) and μ ≠ N. So protected(p, S). Since μ, Γ ⊢ δSHₘ₀Nₘₐₓ l : (σ_p ref'' l), from the well-typedness of environment, we have δ(l) = μ' ≠ N. Hence μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok.

Case l ≠ (l₁ | l₂), v ≠ (v₁ | v₂): Trivially μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok.

Case NSQ-OUTPUT: Given pc, μ, Γ, U ⊢ δSHₘ₀Nₘₐₓ (output e to ℓ, r, m, K) : Γ' • ok and μ ⊢ (output e to ℓ, r, m, K) • \#₄ₚNₘₐₓ r; m; K ⊢ Mem(m) • Out(ℓ, v) • From the premise of T-NSQ-CONFIG, we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok. Large-step does not modify register file, memory or killset.

Case NSQ-SETCOND: Given pc, μ, Γ, U ⊢ δSHₘ₀Nₘₐₓ (set(cnd), r, m, K) : Γ' • ok and μ ⊢ (set(cnd), r, m, K) • \#₄ₚNₘₐₓ r; m', K ⊢ Mem(m') • such that m' = m[cnd → 1] We have to prove that μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok. From the premise of T-NSQ-CONFIG, we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok. Since m and m' do not differ (set(cnd) always sets cnd to a non-pair value), we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m', K) • ok.

Case NSQ-KILL: Given pc, μ, Γ, U ⊢ δSHₘ₀Nₘₐₓ (kill(i), r, m, K) : Γ' • ok and N ⊢ (kill(i), r, m, K) • \#₄ₚNₘₐₓ r; m; K ⊢ Mem(m') • We have to show that μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok. From the premise of T-NSQ-CONFIG, we have μ, Γ ⊢ δSHₘ₀Nₘₐₓ (r, m, K) • ok. Since [K ]₁ = [K₂], we therefore have [K ⊔ {E_i} ]₁ = [K ⊔ {E_i} ]₂. Hence proved.

Case NSQ-SEQ: Given pc, N, Γ, U ⊢ δSHₘ₀Nₘₐₓ (c₁; . . . ; cₙ, r₀, m₀, K₀) • \#₄ₚNₘₐₓ rₙ; mₙ; Kₙ≡n • We have to prove that N, Γ ⊢ δSHₘ₀Nₘₐₓ (rₙ, mₙ, Kₙ)• ok. From the premise of T-NSQ-CONFIG, we have μ, Γ₀ ⊢ δSHₘ₀Nₘₐₓ (r₀, m₀, K₀) • ok and pc, μ, Γ₀, U ⊢ δSHₘ₀Nₘₐₓ c₁ : Γ₁, K₁ for i ∈ {1 . . . n}. Applying induction hypothesis, we thus have μ, Γ₁ ⊢ δSHₘ₀Nₘₐₓ (r₁, m₁, K₁) • ok. Since the types of locations are fixed throughout the program, we have that if Γ₀ is well-typed for δ then Γ₁ is also well-typed for δ. Applying induction hypothesis continuously, we thus have N, Γ₀ ⊢ δSHₘ₀Nₘₐₓ (rₙ, mₙ, Kₙ) • ok.

Figure 20. N'-chaos Typing IMPE₂N'-chaos configurations
Case NSQ-ENCLOSE: Given $pc, N, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle \text{enclave}(i, c), r, m, K \rangle$ : $\Gamma \cdot \text{ok}$ and $N \vdash \langle \text{enclave}(i, c), r, m, K \rangle$. From the premise of T-NSQ-CONFIG, we have $\mu, \Gamma \vdash_{\deltaSH_{\text{nch}}} \langle r, m, K \rangle \cdot \text{ok}$ and $pc, E_i, \Gamma, \emptyset \vdash_{\deltaSH_{\text{nch}}} c : \Gamma', K'$. So, $pc, E_i, \Gamma, \emptyset \vdash_{\deltaSH_{\text{nch}}} \langle c, r, m, K \rangle : \Gamma \cdot \text{ok}$. Also, $E_i \vdash \langle c, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$. Applying induction hypothesis, we thus have $E_i \Gamma, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$. Hence $N, \Gamma, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$.

Case NSQ-IF-ELSE: Given $pc, \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle \text{if} \text{ then } c_1 \text{ else } c_2, r, m, K \rangle$ : $\Gamma \cdot \text{ok}$ and $\mu \vdash \langle \text{if} \text{ then } c_1 \text{ else } c_2, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$. We have to prove that $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$.

From the premise of T-NSQ-CONFIG, we have $\mu, \Gamma \vdash_{\deltaSH_{\text{nch}}} \langle r, m, K \rangle \cdot \text{ok}$ and $pc', \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} c_i : \Gamma', K'$ for $i = \{1, 2\}$ and $pc \leq pc'$. So, $pc, \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle c_i, r, m, K \rangle : \Gamma \cdot \text{ok}$. Also, $\mu \vdash \langle c_i, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$. Applying induction hypothesis to the premise $\mu \vdash \langle \text{c}_i, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$, we thus have $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$.

Case NSQ-WHILE: Given $pc, \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle \text{while } d \text{ do } e, c, r, m, K \rangle$ : $\Gamma \cdot \text{ok}$ and $\mu \vdash \langle \text{while } d \text{ do } e, c, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K'' \triangleright t''$. We have to prove that $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K'' \rangle \cdot \text{ok}$.

From the premise of T-NSQ-CONFIG, we have $\mu, \Gamma \vdash_{\deltaSH_{\text{nch}}} \langle r, m, K \rangle \cdot \text{ok}$ and $pc', \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle e, c, r, m, K \rangle : \Gamma \cdot \text{ok}$. Also, $\mu \vdash \langle e, c, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K'' \triangleright t''$. Applying induction hypothesis to the premise $\mu \vdash \langle e, c, r, m, K \rangle \cdot \text{not-chaos} r'; m'; K'' \triangleright t''$, we thus have $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K'' \rangle \cdot \text{ok}$.

Case NSQ-CALL: Given $pc, \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle \text{call}(e), r, m, K \rangle$ : $\Gamma \cdot \text{ok}$ and $\mu \vdash \langle \text{call}(e), r, m, K \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$. We have to prove that $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$.

From the premise of T-NSQ-CONFIG, we have $\mu, \Gamma \vdash_{\deltaSH_{\text{nch}}} \langle r, m, K \rangle \cdot \text{ok}$ and $pc', \mu, \Gamma, K', U \vdash_{\deltaSH_{\text{nch}}} \langle \text{call}(e) \rangle : \Gamma', K' \cdot \text{ok}$. From the premise of T-NSQ-CONFIG, we have $\mu, \Gamma \vdash_{\deltaSH_{\text{nch}}} \langle r, m, K \rangle \cdot \text{ok}$ and $pc', \mu, \Gamma, K', U \vdash_{\deltaSH_{\text{nch}}} \langle \text{call}(e) \rangle : \Gamma', K' \cdot \text{ok}$. By subjunction, $p, \mu, \Gamma, U \vdash_{\deltaSH_{\text{nch}}} \langle \text{call}(e) \rangle : \Gamma', K' \cdot \text{ok}$. Applying induction hypothesis to the premise $\mu \vdash \langle \text{call}(e) \rangle \cdot \text{not-chaos} r'; m'; K' \triangleright t'$, we thus have $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle r', m', K' \rangle \cdot \text{ok}$.

A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. Since, both branches $c_0$ and $c_1$ have same killsets, we have $K_1 = K_2$. So $|K_1| = |K_2|$. Hence $\mu, \Gamma' \vdash_{\deltaSH_{\text{nch}}} \langle \text{ok} \rangle$.
of NS-Q-CALL-DIV, we have $\mu \vdash \langle e, r, m, K \rangle \cdot \downarrow^2_{N-chaos} (v_0 \mid v_1)$. So $\mu \neq N$, protected$(q, S)$ and since $q \leq p$, protected$(p, S)$ follows.

Let $z$ be such that $\langle \vec{r}(z) = (v_1 \mid v_2)$. If $r(z) = (v_1 \mid v_2)$ and $\Gamma(z) = \sigma_y$, then from the premise of T-NS-Q-CONF, we already have protected$(y, S)$. If $\langle r(z) \neq (v_1 \mid v_2)$ i.e., not a pair value, and $\Gamma(z) = \sigma_y$, then from the well-typedness $p, \mu, \Gamma, K', U \vdash_{\delta SH_{\text{N-ch}} N-chaos} e : \Gamma^+, K^+$, we have protected$(p, S)$ and so protected$(y, S)$ (because an assignment is at least as restrictive as $p$). Similarly, let $\vec{m}(l) = (v_1 \mid v_2)$ and $\Gamma(l) = \sigma_y$. Since the type of location is invariant throughout the program, from the initial configuration we have protected$(y, S)$. A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. From the function type, post killsets are same. So $[K]_1 = [K]_2$. Hence $\mu, \Gamma \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle \vec{r}, \vec{m}, K \rangle \cdot ok$.

Hence proved.

**Lemma 14 (IMPE$^{2N-chaos}_{N-chaos}$ N-chaos Type Preservation).** Let $\Gamma$ be a well-formed typing context and $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c, r, m, K \rangle : \Gamma', K' \cdot ok$. If $\mu \vdash \langle e', r', m', K' \rangle \cdot \downarrow^2_{N-chaos} r'; m'; K' > t' \cdot$ is an immediate (command) premise in the evaluation of $\mu \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} \vec{r}; m; K > \vec{t} \cdot$, then $\exists \vec{p} \vec{c}, \Gamma', \vec{U},$ such that $pc \leq \vec{p}c$, either $U \subseteq \vec{U} = \emptyset$ and $pc, \mu, \Gamma, \vec{U} \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c', r', m', K' \rangle : \Gamma' \cdot ok$.

Proof. The proof is by induction on the derivation of the large step $\mu \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} r'; m'; K' > t' \cdot$. Since rules NSQ-ASSIGN, NSQ-SKIP, NSQ-UPDATE, NSQ-KILL, NSQ-OUTPUT, NSQ-SETCND, NSQ-IF-DIV, NSQ-WHILE-DIV and NSQ-CALL-DIV do not have IMPE$^{2N-chaos}_{N-chaos}$ command premises, the only relevant cases are NSQ-ENCLAVE, NSQ-IF-ELSE, NSQ-WHILE, NSQ-SEQ, NSQ-CALL.

**Case NSQ-ENCLAVE:** Given $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle \text{enclave}(i, c, r, m, K) : \Gamma', K' \cdot ok$. From the premises of T-SQ-CONF, we have $\mu, \Gamma \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle r, m, K \rangle \cdot ok$ and $pc, \Gamma, K, \emptyset \vdash_{\delta SH_{\text{N-ch}} N-chaos} c : \Gamma', K'$. From the premises of the IMPE$^{2N-chaos}_{N-chaos}$ large-step, we have $E_i \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} r'; m'; K' > t' \cdot$. Hence $pc, \emptyset, \Gamma, E_i \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c, r, m, K \rangle : \Gamma', K' \cdot ok$.

**Case NSQ-IF-ELSE:** Given $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \Gamma' \cdot ok$. From the premises of the IMPE$^{2N-chaos}_{N-chaos}$ large-step, we have $\mu \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} \langle c_1, r', m', K' \rangle \cdot ok$ and $pc, \mu, \Gamma, K, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c, r', m', K' \rangle : \Gamma' \cdot ok$. Note that if $e = \text{isunset} (\text{end})$, then we have $pc', \mu, \Gamma, U \cup \langle \text{end} \rangle \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_1, r, m, K \rangle : \Gamma' \cdot ok$ and $pc', \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_2, r, m, K \rangle : \Gamma' \cdot ok$.

**Case NSQ-WHILE:** Given $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle \text{while } e \text{ do } c', r, m, K \rangle : \Gamma', K' \cdot ok$. From the premises of the IMPE$^{2N-chaos}_{N-chaos}$ large-step, we have $\mu \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} \langle c', r', m', K' \rangle \cdot ok$ and $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c', r', m', K' \rangle : \Gamma' \cdot ok$. Applying Lemma [13] to $pc', \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c', r', m', K' \rangle : \Gamma' \cdot ok$.

**Case NSQ-CALL:** Given $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle \text{call}(e), r, m, K \rangle : \Gamma', K' \cdot ok$. From the premises of the IMPE$^{2N-chaos}_{N-chaos}$ large-step, we have $\mu \vdash \langle c, r, m, K \rangle \cdot \downarrow^2_{N-chaos} \langle c, r, m, K \rangle \cdot ok$ and $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c, r, m, K \rangle : \Gamma' \cdot ok$. Hence $pc, \mu, \Gamma, U \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c, r, m, K \rangle : \Gamma' \cdot ok$.

**Case NSQ-SEQ:** Given $pc, \mu, 0, 0 \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_1, \ldots , c_n, r_0, m_0, K_0 \rangle : \Gamma_n, K_n \cdot ok$. From the premises of the IMPE$^{2N-chaos}_{N-chaos}$ large-step, we have $\mu \vdash \langle c_i, r_{i-1}, m_{i-1}, K_{i-1} \rangle \cdot \downarrow^2_{N-chaos} \langle r_i, m_i, K_i \rangle \cdot ok$. From the premises of T-SQ-CONF, we have $\mu, \Gamma \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle r_0, m_0, K_0 \rangle \cdot ok$ and $pc, \mu, \Gamma, 0, 0, \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_1, r_0, m_0, K_0 \rangle : \Gamma_1, K_1 \cdot ok$. Applying Lemma [13] we have $\mu, \Gamma \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle r_1, m_1, K_1 \rangle \cdot ok$. Hence $pc, \mu, \Gamma, 0, 0 \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_2, r_1, m_1, K_1 \rangle : \Gamma_2, K_2 \cdot ok$. Repeatedly applying the above argument for $n$ times, we thus have $pc, \mu, \Gamma, n-1, \vdash_{\delta SH_{\text{N-ch}} N-chaos} \langle c_n, r_{n-1}, m_{n-1}, K_{n-1} \rangle : \Gamma_n, K_n \cdot ok$.

Hence proved.

Using Lemma[13] and Lemma[14] we prove the second part of Theorem[1] for semantics $\downarrow^2_{N-chaos}$ and security specification $\gamma$. 


Proof. Given \( L, \mu, \Gamma, K, \emptyset \vdash \delta \ b : \Gamma', K' \). Let \( m_1 \) be some initial memory for which \( N \vdash \delta (c, r_{init}, m_1, K) \vdashchaostypeN r_1' ; m_1' \vdash_{\Gamma'} t \cdot t_{obs} \cdot t' \) where \( t_{obs} = m' \cdot t'' \) for some memory \( m' \) and trace \( t'' \), and if \( t'' \) is not empty then the last element of \( t'' \) is an output event. Note that the attacker actually observes only low-events i.e. \( [t_{obs} ]_L \). We need to show that

\[
k_{\chaostypeN}^L (c, t_{obs}) \supseteq M
\]

where

\[
M = \left( \bigcap_{m' \in [t_{obs}]_{mem}} \text{ind}_l(m_0, \gamma, \{ \text{cnd} | m'(\text{cnd}) = 0 \}) \cap (e', m') \in [t_{obs}]_{exec} \text{Exc}^{\delta_{obs}}(m_0, m', e') \right)
\]

Let \( S \) be the set of conditions that are set at the beginning of \( t_{obs} \), i.e., \( S = \{ \text{cnd} | m'(\text{cnd}) = 1 \} \). If \( Cond \) represents the set of all condition variables, then \( Cond \setminus S \) is the set of conditions that are unset at some time during the observed trace. Also let \( \mathcal{H} \) be the set of all escape hatches that are declassified till the last event of \( t_{obs} \) i.e. \( \mathcal{H} = \{ e | (e, m) \in [t \cdot t_{obs}]_{exec} \} \).

Let \( m_2 \in M \). Also let \( N \vdash \delta (c, r_{init}, m_2, K) \vdashchaostypeN r_2'; m_2'; K_2 \triangleright t_2 \) such that

\[
[t_2]_{1 \cdot \text{cmd}} = [t_2]_{2 \cdot \text{cmd}}
\]

To ensure \( k_{\chaostypeN}^L (c, t_{obs}) \supseteq M \), we need to show that \( m_2 \in k_{\chaostypeN}^L (c, t_{obs}) \).

Note that \( m_1 \) and \( m_2 \) differ only in locations with policies that are protected by set \( S \). That is, for all locations \( l \in \text{Loc} \), if \( m_1(l) \neq m_2(l) \) then \( \Gamma(l) = \sigma_p \implies \text{protected}(p, S) \). Why? Suppose for some \( l \), s.t. \( \Gamma(l) = (\sigma_p, r_t) \) in \( m_1 \) and \( \neg \text{protected}(p, S) \). So, \( p = L \) or \( L \cap \text{cmd} \cap K_2 \) s.t. \( \text{cmd} \not\subseteq S \). Then for some \( m_1 \in M \), we have \( m_1(l) = m_1(l) \). Since \( M \) is computed by the intersection of all such memories, every memory \( m'' \in M \) should satisfy \( m''(l) = m_1(l) \). This implies \( m_2(l) = m_1(l) \) which is a contradiction. Thus \( \text{protected}(p, S) \) must hold.

Also note that \( m_1 \) and \( m_2 \) satisfy

\[
\forall e \in \mathcal{E}, \mu \vdash \delta (e, r_{init}, m_1, K) \vdash v \iff \mu \vdash \delta (e, r, m_2, K) \vdash v
\]

We will construct an IMPE\(^{2\chaostypeN} \) execution that represents the IMPE executions starting from \( m_0 \) and \( m_2 \). Type-preservation of IMPE\(^{2\chaostypeN} \) (Lemma 13) will ensure that both executions produce the same observable trace, thus showing that \( m_2 \in k_{\chaostypeN}^L (c, t_{obs}) \).

Let IMPE\(^{2\chaostypeN} \) memory \( m = \text{merge}(m_1, m_2) \). If \( \mu \vdash (c, r_{init}, m, K) \vdashchaostypeN r^*; m^*; K^* \triangleright t^* \) such that the attacker modifies the program in the same way in both the executions. By the adequacy of IMPE\(^{2\chaostypeN} \) (Lemma 10), we have that the IMPE\(^{2\chaostypeN} \) execution represents IMPE executions with \( m_1 \) and \( m_2 \) as initial memories.

Let \( t^* = t^*_pre \cdot t^*_obs \cdot t^*_post \) for some \( t^*_obs \) such that \([t^*_obs]_1 = t_{obs} \). Define observation overlapped (same as the function defined in Section E.1.2 but repeated here for the ease of reference) by an IMPE\(^{2\chaostypeN} \) trace \( t^* \) as:

\[
\text{obsOverlap}(t^*, t^*_pre, t^*_obs, t^*_post) = \begin{cases} 
  \text{cnd} & \text{if } t^* \leq t^*_pre \\
  t^*_obs & \text{if } t^*_pre \cdot t^*_obs \leq t^* \\
  t^*_post & \text{if } t^* = t^*_pre \cdot t^* \text{ and } t^* \leq t^*_obs 
\end{cases}
\]

Intuitively, \( \text{obsOverlap}(t^*, t^*_pre, t^*_obs, t^*_post) \) defines part of input trace \( t^* \) that overlaps with an observed trace \( t^*_obs \).

Since \( L, \mu, \Gamma, K, \emptyset \vdash \delta \ b : \Gamma', K' \), from Lemma 15 we have \( L, \mu, \Gamma, K, \emptyset \vdashchaostypeN r^*; m^*; K^*; \emptyset \vdashchaostypeN K \triangleright \hat{o} \).

\[
\text{Lemma 15 (Observational Equivalence is Preserved). Let } S \text{ be the set of conditions that are set(non-zero) in some observed trace } t_{obs}. \text{ If } \mu, \Gamma, U \vdashchaostypeN (c, r, m, K) : \Gamma', K' \text{ ok and } \mu \vdash (c, r, m, K) \vdashchaostypeN r^*; m^*; K \triangleright \hat{o}, \text{ then}
\]

\[
\text{[obsOverlap}(t^*, t^*_pre, t^*_obs, t^*_post)\text{]}_1 \approx L \cdot \text{[obsOverlap}(t^*, t^*_pre, t^*_obs, t^*_post)\text{]}_2
\]

Proof. The proof follows by induction on the derivation of \( \mu \vdash (c, r, m, K) \vdashchaostypeN r^*; m^*; K \triangleright \hat{o} \).
Case NSq-Skip: Emitted trace is empty.
Case NSq-Assign: Emitted trace is empty.
Case NSq-Declassify: Emitted trace does not include out event.
Case NSq-Update: Emitted trace is empty.
Case NSq-Kill: Emitted trace is empty.
Case NSq-SetCnd: Emitted trace does not include out event.

Case NSq-Call-Div: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle e, r, m, K \rangle : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{output } e \text{ to } \ell, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot r; m; K \triangleright \text{Mem}(m) \cdot \text{Out}(\ell, v) \). Let \( \ell = \text{Mem}(m) \cdot \text{Out}(\ell, v) \). From the premise of T-NSq-CONF, we have \( pc, \mu, \Gamma, K, U \vdash \text{output } e \text{ to } \ell, \Gamma, K \) and so \( \mu, \Gamma \vdash_{\delta H \mu_0 Nc} \langle e, r, m, K \rangle : \sigma_p \) and \( \text{cur}(p, U) \cup \text{cur}(pc, U) \subseteq \ell \).

Case NSq-If-Else: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{if } c = 1 \text{ else } c_2, r, m, K \rangle : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{if } c = 1 \text{ else } c_2, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot r'; m'; K'' \triangleright t'' \cdot \). Let \( \ell = t' \). Since \( \mu \vdash_{\delta} \langle e, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot v \) such that \( v \) is not a pair, applying induction hypothesis to the premises of NSQ-IF-ELSE gives us

\[
[\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

From Lemma 1\(^4\), we have \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{while } e \text{ do } c, r, m, K \rangle : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{while } e \text{ do } c, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot r'; m'; K'' \triangleright t'' \cdot \). Applying induction hypothesis to the premise of NSQ-WHILE gives us

\[
[\text{obsOverlap}(t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

Hence

\[
[\text{obsOverlap}(t \cdot t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(t \cdot t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

Case NSq-Call: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{call}(e) \rangle, r, m, K : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{call}(e) \rangle, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot v \) such that \( v \) is not a pair, applying induction hypothesis to the premise of NSQ-CALL gives us

\[
[\text{obsOverlap}(t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(t', t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

Case NSq-If-Div: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot \ell ; m; K \triangleright \circ \cdot \). From the premises of T-NSQ-CONF, we have \( pc, \mu, \Gamma, K, U \vdash_{\delta H \mu_0 Nc} \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle \). From the well-typedness, neither \( c_1 \) nor \( c_2 \) do emit any out events to L channel. Hence

\[
[\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

Case NSq-While-Div: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{while } e \text{ do } c, r, m, K \rangle : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{while } e \text{ do } c, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot \ell ; m; K \triangleright \circ \cdot \). From the premises of T-NSQ-CONF, we have \( pc, \mu, \Gamma, K, U \vdash_{\delta H \mu_0 Nc} \langle \text{while } e \text{ do } c, r, m, K \rangle \). From the well-typedness, command \( e \) does not emit any out events to L channel. Hence

\[
[\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]

Case NSq-Call-Div: Given \( pc, \mu, \Gamma, U \vdash_{\delta H \mu_0 Nc} \langle \text{call}(e) \rangle, r, m, K : \Gamma', K' \cdot \) ok and \( \mu \vdash \langle \text{call}(e) \rangle, r, m, K \rangle \cdot \frac{1}{2}_{\text{chaos}} \cdot \ell ; m; K \triangleright \circ \cdot \). From the premises of T-NSQ-CONF, we have \( pc, \mu, \Gamma, K, U \vdash_{\delta H \mu_0 Nc} \langle \text{call}(e) \rangle \). From the well-typedness, command \( e \) does not emit any out events to L channel. Hence

\[
[\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_1 \approx_L [\text{obsOverlap}(i, t^*_{\text{pre}}, t^*_{\text{obs}}, t^*_{\text{post}})]_2
\]
Case NSq-Seq: Given $pc, \mu, \Gamma, U \vdash_{\text{NSq-Config}} (c_1; \ldots; c_n, r_0, m_0, K_0) : \Gamma, K_0 \triangleright \langle c_1; \ldots; c_n, r_0, m_0, K_0 \rangle \triangleright \frac{N_{\text{chaos}}}{r_n; m_n; K_n \triangleright t_1 \cdot \ldots \cdot t_n}$. From the premises of T-NSq-Config, we have $pc, \mu, \Gamma, K_0, U \vdash_{\text{NSq-Config}} c_1; \ldots; c_n : \Gamma_1, K_n$.

Applying induction hypothesis to the premise, $\mu \vdash (c_1, r_0, m_0, K_0) \triangleright \langle c_1; \ldots; c_n, r_0, m_0, K_0 \rangle \triangleright \frac{N_{\text{chaos}}}{r_n; m_n; K_n \triangleright t_1 \cdot \ldots \cdot t_n}$. We have

$$\frac{\text{obsOverlap}(t_1, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{1} \approx L \frac{\text{obsOverlap}(t_1, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{2}$$

From Lemma 14, we have $pc, \mu, \Gamma, U \vdash_{\text{NSq-Config}} (c_2, r_1, m_1, K_1) : \Gamma_2, K_2 \triangleright \langle c_1, r_0, m_0, K_0 \rangle$. Applying inductive hypothesis to the premise

$$\frac{\text{obsOverlap}(t_2, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{1} \approx L \frac{\text{obsOverlap}(t_2, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{2}$$

Applying the inductive hypothesis continuously thus gives,

$$\frac{\text{obsOverlap}(t_n, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{1} \approx L \frac{\text{obsOverlap}(t_n, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{2}$$

Case NSq-Enclave: Given $pc, \mu, \Gamma, U \vdash_{\text{NSq-Config}} (\text{enclave}(i, c), r, m, K) \triangleright \frac{N_{\text{chaos}}}{r'; m'; K' \triangleright t'}$. From the premises of T-NSq-Config, we have $pc, \mu, \Gamma, K, U \vdash_{\text{NSq-Config}} \text{enclave}(i, c) : \Gamma', K'$. Applying induction hypothesis to the premise

$$\frac{\text{obsOverlap}(t', t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{1} \approx L \frac{\text{obsOverlap}(t', t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{2}$$

Since we have $L, N, \Gamma, \emptyset \vdash_{\text{NSq-Config}} (c, r_{\text{init}}, m, \emptyset) : \Gamma' \triangleright \langle c, r_{\text{init}}, m, \emptyset \rangle \triangleright \frac{N_{\text{chaos}}}{r^*; m^*; K^* \triangleright t^*}$. Applying Lemma 15 on $\mu \vdash (c, r_{\text{init}}, m, K) \triangleright \langle c, r_{\text{init}}, m, \emptyset \rangle \triangleright \frac{N_{\text{chaos}}}{r^*; m^*; K^* \triangleright t^*}$, we have

$$\frac{\text{obsOverlap}(t^*, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{1} \approx L \frac{\text{obsOverlap}(t^*, t_{\text{pre}}, t_{\text{obs}}, t_{\text{post}})}{2}$$

Hence proved that $m_2 \in L_{\frac{N_{\text{chaos}}}{c, t_{\text{obs}}}}$.

E.1.6 Proofs for E1-chaos Security

In this section we use an even more permissive E1-chaos type system and show that a IMPE program that is well-typed for the type system in Section 4 is also well-typed for E1-chaos type system. Figure 21 presents the E1-chaos type system. It further relaxes the N-chaos type system from Section E1.3 by unconstraining the commands running both in the normal mode and killed enclave modes. They can now read and write to memory locations with no restrictions on security policies. The new typing system relies on the guarantees provided by the operational semantics that a command running in normal mode does not access enclave memory, and that a location from a killed enclave is inaccessible. Typing rules for commands running in enclave mode are unchanged and are same as those presented in Figure 19.

Lemma 15 (Permissive Type System 2). Let I be the set of enclaves killed. If $L, N, \Gamma, \emptyset \vdash c : \Gamma', K'$, then $pc, \mu, \Gamma, K, U \vdash_{\text{NSq-chaos}} c : \Gamma', K'$.

Proof Sketch. Proof is by straight forward induction on the derivation of the typing judgment $\mu, \Gamma \vdash c : \sigma_p$.

E.1.7 IMPE2E1-chaos Adequacy

The language IMPE2E1-chaos is adequate for reasoning about executions of two IMPE programs. We show that the execution of IMPE2E1-chaos program using semantics $\frac{E_1}{\text{chaos}}$ is sound (i.e., large-step taken by a IMPE2E1-chaos program coresponds to a large-step taken by either side of the execution) and complete (given two IMPE E1-chaos executions, there exists an IMPE2E1-chaos execution).

Lemma 17 (IMPE2E1-chaos is Sound). If $\mu \vdash (c, r, m, K) \triangleright \frac{E_1}{\text{chaos}} r^*; m^*; K^* \triangleright t^*$, then $\mu \vdash (c, r, m, K) \triangleright \frac{E_1}{\text{chaos}} r^*; m^*; K^* \triangleright t^*$ for $i \in \{1, 2\}$.

Proof Sketch. Proof is by induction on the derivation of $\mu \vdash (c, r, m, K) \triangleright \frac{E_1}{\text{chaos}} r^*; m^*; K^* \triangleright t^*$.

Lemma 18 (IMPE2E1-chaos is Complete). If $\mu \vdash (c, r, m, K) \triangleright \frac{E_1}{\text{chaos}} r^*; m^*; K^* \triangleright t^*$, such that $t^* \approx [t^*]_{1, \text{cmd}} = [t^*]_{2, \text{cmd}} \text{ then } \exists (r^*, m^*, K^*, t^*)$. Then $\mu \vdash (c, r, m, K) \triangleright \frac{E_1}{\text{chaos}} r^*; m^*; K^* \triangleright t^*$ and $\langle [r^*], [m^*], [K^*], [t^*] \rangle = (r^*_i, m^*_i, K^*_i, t^*_i)$ for $i \in \{1, 2\}$.

Proof Sketch. Follows along the lines of proof of Lemma 2.
EI-Skip-N
\[ pc, N, \Gamma, K, U \vdash \text{skip} : \Gamma, K \]

EI-Assign
\[ \mu, \Gamma \vdash e : \sigma_p \quad \mu \notin I \]
\[ pc, \mu, \Gamma, K, U \vdash \text{assign} : \Gamma [x \mapsto \sigma_p] : K \]

EI-Output
\[ \mu, \Gamma, K, U \vdash \text{output} e \text{ to } \ell : \Gamma, K \]

EI-Seq
\[ \forall i \in \{1 \ldots n\}, pc, \mu, \Gamma_{i-1}, K_{i-1}, U \vdash \text{assign} : \Gamma_i, K_i \]
\[ pc, \mu, \Gamma_0, K_0, U \vdash \text{Seq} : \Gamma_1, K_1 \]

EI-If-Isunset
\[ \mu, \Gamma \vdash \text{isunset}(cnd) : \text{int}_L \]
\[ pc, \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]
\[ pc, \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]

EI-Else
\[ \mu, \Gamma \vdash e : \text{int}_p \]
\[ pc', N, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]
\[ pc', N, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]

EI-While
\[ \mu, \Gamma \vdash e : \text{int}_p \]
\[ pc', \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]
\[ pc', \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]

EI-Call
\[ \mu, \Gamma \vdash e : (\Gamma', K', U \vdash \text{assign} : \Gamma', K') \]
\[ pc, \mu, \Gamma, K', U \vdash \text{call}(e) : \Gamma_{\text{out}}, K^* \]

EI-Kill
\[ E_i \notin K \]
\[ pc, N, \Gamma, K, U \vdash \text{kill}(i) : \Gamma, K \]

EI-Declassify
\[ \mu, \Gamma \vdash e : \sigma_p \quad \mu \notin I \]
\[ pc, \mu, \Gamma, K, U \vdash \text{declassify} : \Gamma[x \mapsto \sigma_L] : K \]

EI-Update
\[ \mu, \Gamma \vdash e_1 : (\sigma_p \text{ ref}^t) \quad \mu, \Gamma \vdash e_2 : \sigma_{p'} \quad \mu \notin I \]
\[ pc, \mu, \Gamma, K, U \vdash e_1 \leftarrow e_2 : \Gamma, K \]

EI-SetCnd
\[ \delta(cnd) = N \quad \text{cond} \in \text{Cond} \setminus U \quad \mu \notin I \]
\[ pc, \mu, \Gamma, K, U \vdash \text{set}(cnd) : \Gamma, K \]

Figure 21. $E_{1-\text{chaos}}$ typing rules for IMPE

E.1.8 IMPE$^{2E_{1-\text{chaos}}}$ $E_{1-\text{chaos}}$ Type System

Let $S$ be the set of conditions set during some observed trace $t_{\text{obs}}$, $H$ be the set of escape hatches till the observed trace and $n_0$ be the initial IMPE$^{2E_{1-\text{chaos}}}$ memory. A policy is now protected if it is either $\top$ or $\ell \text{ end} \mathcal{F} \top$ s.t. $m\text{ end} = 1$. We define $\text{protected}_1(p, S)$ as follows:

\[
\text{protected}_1(p, S) = \begin{cases} 
\text{true} & \text{if } p = \top \\
\text{true} & \text{if } p = \ell \text{ end} \mathcal{F} \top \text{ and } \text{cond} \in S \\
\text{false} & \text{otherwise}
\end{cases}
\]

The definition differs from $\text{protected}(p, S)$ defined earlier in that an erasure policy is protected if the confidentiality level is raised to $\top$ only (after some condition $\text{end}$ is set).

The IMPE$^{2E_{1-\text{chaos}}}$ type system is parametrized by $\delta, S, H$ and $n_0$. The typing judgment for commands and expressions is shown below.

\[ pc, \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]
\[ pc, \mu, \Gamma, K, U \vdash \text{assign} : \Gamma', K' \]

The typing rules are shown in Figure 21 and ensure that commands running in enclaves that aren’t killed are well-typed according to IMPE$^{2E_{1-\text{chaos}}}$ type system. Rules for typing configurations are shown in Figure 22 and are similar to T-NSQ-CONFIG and T-NSQ-VALUE.
∀end ∈ U, m(end) = 0  pc, µ, Γ, K, U ⊢SH_mãE_ch e : Γ′, K′
∀e ∈Vars, r(x) = (v1 | v2) and Γ(x) = σp
∀l ∈ Loc \ Cond, m(l) = (v1 | v2) and Γ(l) = (σp, rt) → protected_l(p, S) and µ ∈ I
∀e ∈ H, µ ⊢ ⟨e, r_init, m̃_0, K⟩ •  ψ2_Echaos_v → µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v
T-ESQ-CONFIG

pc, µ, Γ, U ⊢SH_mãE_ch (c, r, m, K) : Γ′ • ok
∀e ∈ Vars, r(x) = (v1 | v2) and Γ(x) = σp → protected_l(p, S)
∀l ∈ Loc \ Cond, m(l) = (v1 | v2) and Γ(l) = (σp, rt) → protected_l(p, S) and µ ∈ I
∀e ∈ H, µ ⊢ ⟨e, r_init, m̃_0, K⟩ •  ψ2_Echaos_v → µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v and δ(l) ∈ I
T-ESQ-VALUE

Γ ⊢SH_mãE_ch (r, m, K) • ok

Figure 22. E_chaos Typing IMP2_Echaos configurations

Lemma 19 (E_chaos Value Type Preservation). If µ, Γ ⊢SH_mãE_ch e : σp and µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v, then µ, Γ ⊢SH_mãE_ch e : σp.

Proof Sketch. Proof is by straightforward induction on the derivation of the typing judgment µ, Γ ⊢SH_mãE_ch e : σp.

Lemma 20 (E_chaos Protected Expression). Let Γ ⊢SH_mãE_ch ⟨r, m, K⟩ • ok. If µ, Γ ⊢SH_mãE_ch e : σp and µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v such that v = (v1 | v2) for some values v1 and v2, then protected_l(p, S) and µ ∈ I.

Proof Sketch. Proof is by straightforward induction on the derivation of the typing judgment µ, Γ ⊢SH_mãE_ch e : σp.

Lemma 21 (IMP2_Echaos E_chaos Final Configuration Preservation). Let I be the set of enclaves killed, γ be the security specification such that γ(l) = L \ δ(l) = I and Γ be an environment that corresponds to γ and is well-typed for δ. Also let H be the set of escape hatches and m̃_0 be the initial IMP2_Echaos memory such that l ∈ {locations(e) | e ∈ H}, m̃_0(l) ≠ (v1 | v2), i.e., not a pair value. If pc, µ, Γ, U ⊢SH_mãE_ch ⟨c, r, m, K⟩ : Γ′ • ok and µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v′; m′; K′ ⊢ℓ′, then µ, Γ′ ⊢SH_mãE_ch ⟨r′, m′, K′⟩ • ok.

Proof. The proof is by induction on the derivation of the large step µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v′; m′; K′ ⊢ℓ′.

Case ESQ-Skip: Given pc, µ, Γ, U ⊢SH_mãN_ch ⟨skip, r, m, K⟩ : Γ′ • ok and µ ⊢ ⟨skip, r, m, K⟩ •  ψ2_Echaos_v′; m′; K′ ⊢ℓ′.

Case ESQ-Assign: Given pc, µ, Γ, U ⊢SH_mãN_ch ⟨x := e, r, m, K⟩ : Γ′ • ok and µ ⊢ ⟨x := e, r, m, K⟩ •  ψ2_Echaos_v′; m′; K ⊢ℓ′ such that µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v and v′ = r[x → v]. We have to prove that µ, Γ′ ⊢SH_mãE_ch ⟨r′, m′, K⟩ • ok.

From the initial configuration, we have µ, Γ ⊢SH_mãE_ch ⟨r, m, K⟩ • ok. Register files r and r′ differ only in variable x. Let v = (v1 | v2). If µ, Γ ⊢SH_mãE_ch e : σp, we have protected_l(p, S). Γ′ = Γ[x → σpc,l,p]. Applying Lemma 20, we have protected_l(pc ⊔ q, S) and µ ∈ I. Hence proved.

Case ESQ-Declassify: Given pc, µ, Γ, U ⊢SH_mãN_ch ⟨declassify(x)e, r, m, K⟩ : Γ′ • ok and µ ⊢ ⟨declassify(x)e, r, m, K⟩ •  ψ2_Echaos_v′; m′; K ⊢ℓ′ such that µ ⊢ ⟨e, r, m, K⟩ •  ψ2_Echaos_v and v′ = r[x → v]. Also expression e has no variables syntactically present (large-step has the premise hasNoVars(e)), We have to prove that µ, Γ′ ⊢SH_mãE_ch ⟨r′, m′, K⟩ • ok.

From the initial configuration, we have µ, Γ ⊢SH_mãE_ch ⟨r, m, K⟩ • ok. Register files r and r′ differ only for x. Let v = (v1 | v2) for some v1 and v2. We have Γ′ = Γ[x → L]. From the well-typedness, we have allLocImmutable(e). Thus e ∈ H and so v ≠ (v1 | v2) (not a pair value).

Hence proved.

Case ESQ-Update: Given pc, µ, Γ, U ⊢SH_mãN_ch ⟨e1 ← e2, r, m, K⟩ : Γ′ • ok and µ ⊢ ⟨e1 ← e2, r, m, K⟩ •  ψ2_Echaos_v′; m′; K ⊢ℓ′ such that µ ⊢ ⟨e1, r, m, K⟩ •  ψ2_Echaos_v and v′ = r[x → v]. We have to prove that µ, Γ′ ⊢SH_mãE_ch ⟨r′, m′, K⟩ • ok.
From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \) and \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} e_1 : (\sigma_p' \text{ ref}^{r+}) q \) and \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} e_2 : \sigma_p' q \) such that \( p' \cup q \cup p \subseteq p \).

Case \( l = (l_1 \mid l_2), v = (v_1 \mid v_2) \): Applying Lemma 20 we have \( \text{protected}_l (p', S) \) and \( \mu \in I \). So \( \text{protected}_l (p, S) \).

Since \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} l : (\sigma_p' \text{ ref}^{r+}) l' \), from the well-typedness of environment, we have \( \delta(l) = \mu' \in I \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m', K) \cdot \theta \).

Case \( l \neq (l_1 \mid l_2), v = (v_1 \mid v_2) \): Same as above.

Case \( l = (l_1 \mid l_2), v \neq (v_1 \mid v_2) \): Applying Lemma 20 we have \( \text{protected}_l (q, S) \) and \( \mu \in I \). So \( \text{protected}_l (p, S) \). Since \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} l : (\sigma_p' \text{ ref}^{r+}) l' \), from the well-typedness of environment, we have \( \delta(l) = \mu' \in I \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m', K) \cdot \theta \).

Case \( l \neq (l_1 \mid l_2), v \neq (v_1 \mid v_2) \): Trivially \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m', K) \cdot \theta \).

Case ES-Q-OUTPUT: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{output } e \to e, r, m, K\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{output } e \to e, r, m, K\} \cdot \phi \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \).

Case ES-Q-SETCND: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{set } cnd\} : m, r, m, K) : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{set } cnd\} : m, r, m, K \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \). Since \( m \) and \( m' \) do not differ \( \{\text{set } cnd\} \) always sets \( \text{cnd} \) to a non-empty value, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \).

Case ES-Q-KILL: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{kill}(i) \to r, m, K\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{kill}(i) \to r, m, K\} \cdot \phi \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \). Since \( \Gamma_{[i]} = [\Gamma_{[i]}] \), then we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \).

Case ES-Q-SEQ: Given \( pc, N, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{cnd}\} : c_1, \ldots, c_n, m_0, K_0) : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{cnd}\} : c_1, \ldots, c_n, m_0, K_0 \cdot \phi \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m_n, K_n) \cdot \theta \). We have to prove that \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m_n, K_n) \cdot \theta \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m_n, K_n) \cdot \theta \).

Case ES-Q-IF-ELSE: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{if } e \cdot \text{then } c_1 \cdot \text{else } c_2 \cdot \theta\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{if } e \cdot \text{then } c_1 \cdot \text{else } c_2 \cdot \theta\} : \Gamma' \cdot \theta \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \) and \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \). From the well-typedness, we have \( K = K' \). Applying induction hypothesis to the premise \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \), we thus have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \).

Case ES-Q-WHILE: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{while } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{while } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta \). From the premise of T-ESQ-CONFIG, we have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r, m, K) \cdot \theta \) and \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \). From the well-typedness, we have \( K = K' \). Applying induction hypothesis to the premise \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \), we thus have \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \).

Case ES-Q-CALL: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{call } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{call } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta \). From the well-typedness, we have \( K = K' \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \).

Case ES-Q-LET: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{let } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{let } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta \). From the well-typedness, we have \( K = K' \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \).

Case ES-Q-FOR: Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (\{\text{for } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta) \) and \( \mu, \Gamma \vdash \{\text{for } e \cdot \text{do } c \cdot \theta\} : \Gamma' \cdot \theta \). From the well-typedness, we have \( K = K' \). Hence \( \mu, \Gamma \vdash \delta_{\text{SH}m_i\text{E}_{\mu}^p} (r', m', K') \cdot \theta \).
From the premise of T-ESQ-CONFIG, we have $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} \langle r, m, K \rangle \bullet \text{ok}$ and $pc, \mu, \Gamma^*, K', U \vdash_{\delta \text{SH}m_0E^h} \text{call}(e) : \Gamma^*, K^+$ such that $\Gamma \leq \Gamma^*, \Gamma^* \leq \Gamma$ and $K = K', K' = K^*$. From subsumption, $p, \mu, \Gamma, U \vdash_{\delta \text{SH}m_0N_{\text{sh}}} \langle c, r, m, K \rangle : \Gamma\bullet \text{ok}$ $\Gamma\Gamma'K'$. Applying induction hypothesis to $\mu \vdash \langle c, r, m, K \rangle \bullet \psi^2_{E_1-\text{chaos}} r'; m'; K' \triangleright t' \bullet$, we thus have $\mu, \Gamma' \vdash_{\delta \text{SH}m_0E^h} \langle r', m', K' \rangle \bullet \text{ok}$.

**Case ESQ-IF-DIV**: Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}m_0N_{\text{sh}}} (if \ e \ then \ c_0 \ else \ c_1, r, m, K) : \Gamma' \bullet \text{ok}$ and $\mu \vdash (if \ e \ then \ c_0 \ else \ c_1, r, m, K) \bullet \psi^2_{E_1-\text{chaos}} \hat{r}; \hat{m}; K \triangleright \hat{t} \bullet$. We have to prove that $\mu, \Gamma' \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

From the initial configuration, we have $pc', \mu, \Gamma, K, U \vdash_{\delta \text{SH}m_0E^h} c_2' : \Gamma', K'$ and $\mu, \Gamma \vdash e : \text{int}_p$. From the premise of ESQ-IF-DIV, we have $\mu \vdash (e, r, m, K) \bullet \psi^2_{E_1-\text{chaos}} (v_0 | v_1)$. So $\mu \in I, \text{protected}_I(p, S)$ and $\text{protected}_I(pc', S)$.

Let $z$ be such that $r(z) = (v_1 | v_2)$. If $\Gamma(z) = \sigma_q$, then either $\Gamma(z) = \sigma_q$ or there is an assignment to $z$ in $c_i$ for some $i \in \{0, 1\}$. If the former holds, then we already have $\text{protected}_I(q, S)$. If the latter holds, then we have $\text{protected}_I(q, S)$ (because an assignment is at least as restrictive as $pc'$).

Let $m(l) = (v_1 | v_2)$ and $\Gamma(l) = \sigma_q$. Since the type of location is invariant throughout the program, from the initial configuration we have $\text{protected}_I(q, S)$.

A well-typed escape hatch has immutuable locations and thus evaluates to the same initial value. Since, both branches $c_0$ and $c_1$ have same killsets, we have $K_1 = K_2$. So $[K_1] = [K_2]$. Hence $\mu, \Gamma' \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

**Case ESQ-WHILE-DIV**: $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}m_0N_{\text{sh}}} (while \ e \ do \ c, r, m, K) : \Gamma, \Gamma \bullet \text{ok}$ and $\mu \vdash (while \ e \ do \ c, r, m, K) \bullet \psi^2_{E_1-\text{chaos}} \hat{r}; \hat{m}; K \triangleright \hat{t} \bullet$. We have to prove that $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

From the initial configuration, we have $pc', \mu, \Gamma, K, U \vdash_{\delta \text{SH}m_0E^h} c : \Gamma, K$ and $\mu, \Gamma \vdash e : \text{int}_p$ for $pc \leq pc'$. From the premise of ESQ-WHILE-DIV, we have $\mu \vdash (c, r, m, K) \bullet \psi^2_{E_1-\text{chaos}} (v_0 | v_1)$. So $\mu \in I, \text{protected}_I(p, S)$ and $\text{protected}_I(pc', S)$.

Let $z$ be such that $r(z) = (v_1 | v_2)$. If $r(z) = (v_1 | v_2)$ and $\Gamma(z) = \sigma_q$, then from the premise of T-ESQ-CONFIG, we already have $\text{protected}_I(q, S)$. If $r(z) \neq (v_1 | v_2)$, i.e., not a pair value, and $\Gamma(z) = \sigma_q$, then from the well-typedness, $pc', \mu, \Gamma, K, U \vdash_{\delta \text{SH}m_0E^h} c : \Gamma, K$, we have $\text{protected}_I(pc', S)$ and $\text{protected}_I(q, S)$ (because an assignment is at least as restrictive as $pc'$). Similarly, let $\hat{m}(l) = (v_1 | v_2)$ and $\Gamma(l) = \sigma_q$. Since the type of location is invariant throughout the program, from the initial configuration we have $\text{protected}_I(q, S)$.

A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. Killsets are unmodified. So $[K_1] = [K_2]$. Hence $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

**Case ESQ-CALL-DIV**: Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}m_0N_{\text{sh}}} (\text{call}(e), r, m, K) : \Gamma', K' \bullet \text{ok}$ and $\mu \vdash (\text{call}(e), r, m, K) \bullet \psi^2_{E_1-\text{chaos}} \hat{r}; \hat{m}; K \triangleright \hat{t} \bullet$. We have to prove that $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

From the initial configuration, we have $pc, \mu, \Gamma, K, U \vdash_{\delta \text{SH}m_0E^h} \langle r, m, K \rangle \bullet \text{ok}$, $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} e : (\Gamma^*, K^*, U \vdash_{\delta \text{SH}m_0E^h} p, \mu, \Gamma^*, K', U \vdash_{\delta \text{SH}m_0E^h} c) : \Gamma^*, K^+$ such that $K = K', K' = K^+$ and $\Gamma = \Gamma', \Gamma' = \Gamma^*$. From the premise of ESQ-CALL-DIV, we have $\mu \vdash (c, r, m, K) \bullet \psi^2_{E_1-\text{chaos}} (v_0 | v_1)$. So $\mu \in I, \text{protected}_I(q, S)$ and since $q \leq p$, $\text{protected}_I(p, S)$ follows.

Let $z$ be such that $r(z) = (v_1 | v_2)$. If $r(z) = (v_1 | v_2)$ and $\Gamma(z) = \sigma_y$, then from the premise of T-ESQ-CONFIG, we already have $\text{protected}_I(y, S)$. If $r(z) \neq (v_1 | v_2)$, i.e., not a pair value, and $\Gamma(z) = \sigma_y$, then from the well-typedness of $p, \mu, \Gamma, K, U \vdash_{\delta \text{SH}m_0E^h} c : \Gamma^*, K^+$, we have $\text{protected}_I(p, S)$ and $\text{protected}_I(y, S)$ (because an assignment is at least as restrictive as $p$). Similarly, let $\hat{m}(l) = (v_1 | v_2)$ and $\Gamma(l) = \sigma_y$. Since the type of location is invariant throughout the program, from the initial configuration we have $\text{protected}_I(y, S)$. A well-typed escape hatch has immutable locations and thus evaluates to the same initial value. From the function type, post killsets are same. So $[K_1] = [K_2]$. Hence $\mu, \Gamma \vdash_{\delta \text{SH}m_0E^h} \langle \hat{r}, \hat{m}, K \rangle \bullet \text{ok}$.

Hence proved.

**Lemma 22** (IMPE$^{2E_1-\text{chaos}}$ E$_1$-chaos Type Preservation). Let $I$ be the set of enclaves killed, $\gamma$ be the security specification such that $\gamma(I) = L \forall \delta(I) = I$ and $\Gamma$ be an environment that corresponds to $\gamma$ and is well-typed for $\delta$. Also let $H$ be the set of escape hatches and $m_{00}$ be the initial IMPE$^N_{\text{chaos}}$ memory such that $l \in \{\text{locations}(e) | e \in H\}$, $m_{00}(l) \neq (v_1 | v_2)$, i.e., not a pair value and $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}m_0E^h} \langle c, r, m, K \rangle : \Gamma' \bullet \text{ok}$. If $\mu \vdash \langle c', r', m', K' \rangle \bullet \psi^2_{E_1-\text{chaos}} \hat{r}; \hat{m}; K \triangleright \hat{t} \bullet$, then $\forall pc, \Gamma, \Gamma, U \vdash_{\delta \text{SH}m_0E^h} \langle c', r', m', K' \rangle : \Gamma' \bullet \text{ok}$.

**Proof.** The proof is by induction on the derivation of the large step $\mu \vdash \langle c, r, m, K \rangle \bullet \psi^2_{E_1-\text{chaos}} \hat{r}; \hat{m}; K \triangleright \hat{t} \bullet$. Since rules ESQ-ASSIGN, ESQ-SKIP, ESQ-UPDATE, ESQ-KILL, ESQ-OUTPUT, ESQ-SETCND, ESQ-IF-DIV, ESQ-WHILE-DIV and
**Case ESQ-ENCLAVE:** Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle \text{enclave}(i, c, r, m, K) : \Gamma' \bullet \text{ok} \rangle$. From the premises of T-SQ-CONFIG, we have $
vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle r, m, K \rangle \bullet$ and $pc, E_i, \Gamma, K, \emptyset \vdash_{\delta \text{SH}\text{\{i\}y}^h} c : \Gamma', K'$. From the premises of the IMPE$^{2E_1}$-chaos large-step, we have $E_i \vdash \langle c, r, m, K \rangle \bullet \tE_{\text{chaos}}^2 r'; m' ; K' \triangleright t' \bullet$. Hence $pc, \emptyset, \Gamma, E_i \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c, r, m, K \rangle : \Gamma' \bullet \text{ok}$.

**Case ESQ-IF-ELSE:** Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle e \text{ if then } \text{ else } \rangle$. From the premises of the IMPE$^{2E_1}$-chaos large-step, we have $\mu \vdash \langle e, r, m, K \rangle \bullet \tE_{\text{chaos}}^2 r'; m' ; K' \triangleright t' \bullet$. From the premises of T-SQ-CONFIG, we have $\nvdash_{\delta \text{SH}\text{\{i\}y}^h} \langle r, m, K \rangle \bullet$ and $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} c_i : \Gamma', K'$ for $i = 1, 2$ and $pc \leq pc'$. Hence $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_i, r, m, K \rangle : \Gamma' \bullet \text{ok}$.

Note that if $e = \text{isunset}(\text{cnd})$, then we have $pc', \mu, \Gamma, \cup\{\text{cnd}\} \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_i, r, m, K \rangle : \Gamma' \bullet \text{ok}$ and $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_2, r, m, K \rangle : \Gamma' \bullet \text{ok}$.

**Case ESQ-WHILE:** Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle \text{while do } \rangle$. From the premises of the IMPE$^{2E_1}$-chaos large-step, we have $\mu \vdash \langle e, r, m, K \rangle \bullet \tE_{\text{chaos}}^2 r'; m' ; K' \triangleright t' \bullet$ and $\mu \vdash \langle \text{while do } e, r', m', K' \rangle \bullet \tE_{\text{chaos}}^2 r'' ; m'' ; K'' \triangleright t'' \bullet$. From the premises of T-SQ-CONFIG, we have $\nvdash_{\delta \text{SH}\text{\{i\}y}^h} \langle r, m, K \rangle \bullet \text{ok}$ and $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c', r, m, K \rangle : \Gamma' \bullet \text{ok}$. Applying Lemma 21 to $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c', r, m, K \rangle : \Gamma, K \bullet \text{ok}$, we have $\mu \vdash \langle c', r, m, K \rangle \bullet \tE_{\text{chaos}}^2 r'; m' ; K' \triangleright t' \bullet$. Hence $pc', \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c, r, m, K \rangle : \Gamma', K' \bullet \text{ok}$.

**Case ESQ-CALL:** Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle \text{call}(e) \rangle$. From the premises of the IMPE$^{2E_1}$-chaos large-step, we have $\mu \vdash \langle e, r, m, K \rangle \bullet \tE_{\text{chaos}}^2 \mu \cdot c$ and $\mu \vdash \langle e, r, m, K \rangle \bullet \tE_{\text{chaos}}^2 r'; m'; K' \triangleright t' \bullet$. From the premises of T-SQ-CONFIG, we have $\nvdash_{\delta \text{SH}\text{\{i\}y}^h} \langle r, m, K \rangle \bullet \text{ok}$ and $\mu, \Gamma \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle e, \Gamma', K' \rangle \bullet$. Hence $\Sigma, K' = K' \triangleright K' \text{ and } \Gamma = \Gamma \triangleright K' = K' \triangleright K'$. We also have $pc, \mu, \Gamma, K' \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c, r, m, K \rangle : \Gamma', K' \bullet \text{ok}$.

**Case ESQ-SEQ:** Given $pc, \mu, \Gamma, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_1 ; \ldots ; c_n, r_0, m_0, K_0 \rangle : \Gamma_n, K_n \bullet \text{ok}$. From the premises of the IMPE$^{2E_1}$-chaos large-step, we have $\mu \vdash \langle c_1, r_{i-1}, m_{i-1}, K_{i-1} \rangle \bullet \tE_{\text{chaos}}^2 r_i ; m_i ; K_{i-1} \triangleright t_i \bullet$. From the premises of T-SQ-CONFIG, we have $\nvdash_{\delta \text{SH}\text{\{i\}y}^h} \langle r_0, m_0, K_0 \rangle \bullet$ and $pc, \mu, \Gamma, U, U \vdash_{\delta \text{SH}\text{\{i\}y}^h} c_i : \Gamma, K_i$ for $i = 1, \ldots, n$.

We already have $pc, U, \Gamma, \mu \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_1, r_0, m_0, K_0 \rangle : \Gamma_1, K_1 \bullet \text{ok}$. Applying Lemma 21, we have $\mu, \Gamma \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_2, r_1, m_1, K_1 \rangle : \Gamma_2, K_2 \bullet \text{ok}$. Repeatedly applying the above argument for $n$ times, we thus have $pc, U, \Gamma_{n-1}, \mu \vdash_{\delta \text{SH}\text{\{i\}y}^h} \langle c_n, r_{n-1}, m_{n-1}, K_{n-1} \rangle : \Gamma_n, K_n \bullet \text{ok}$.

Hence proved.

Using Lemma 21 and Lemma 22, we prove the final part of Theorem 1 for semantics $\tE_{\text{chaos}}^2$ and specification $\gamma'$.

**Proof.** Given $L, \mu, \Gamma, K, \emptyset \vdash c : \Gamma', K'$. Let $m_1$ be some initial memory for which $N \vdash \langle c, r_{\text{init}}, m_1, K \rangle \tE_{\text{chaos}}^2 r_1 ; m'_1 ; K' \triangleright t \cdot t_{\text{obs}} \cdot t' \bullet$, where $t_{\text{obs}} = m' \cdot t''$ for some memory $m'$ and trace $t''$, and if $t''$ is not empty then the last element of $t''$ is an output event. Note that the attacker actually observes only low-events i.e. $| t_{\text{obs}} |_L$. We need to show that $k^\tE_{\text{chaos}}^2_{L}(c, t_{\text{obs}}) \supseteq M$ where

$$M = \left( \bigcap_{m' \in t_{\text{obs}} \cdot m_{\text{em}}} \text{ind}(m_0, \gamma', \{ m' | m'(\text{cnd}) = 0 \}) \cap \left( \bigcap_{(e', m') \in t_{\text{obs}} \cdot \text{exec}} \text{ExE}^{\text{kind}}(m_0, m', e') \right) \right)$$

Let $S$ be the set of conditions that are set at the beginning of $t_{\text{obs}}$, i.e., $S = \{ m' | m'(\text{cnd}) = 1 \}$. If $\text{Cond}$ represents the set of all condition variables, then $\text{Cond} \setminus S$ is the set of conditions that are unset at some time during the observed trace. Also let $\mathcal{H}$ be the set of all escape hatches that are classified till the last event of $t_{\text{obs}}$ i.e. $\mathcal{H} = \{ e | (e, m) \in t \cdot t_{\text{obs}} \cdot \text{exec} \}$. Let $m_2 \in M$. Also let $N \vdash \langle c, r_{\text{init}}, m_2, K \rangle \tE_{\text{chaos}}^2 r_2 ; m'_2 ; K'_2 \triangleright t_2$ such that $| t_2 |_{1, \text{cnd}} = | t_2 |_{2, \text{cnd}}$. 

$$| t_2 |_{1, \text{cnd}} = | t_2 |_{2, \text{cnd}}$$
To ensure \( k_L^{\text{ESq}-\text{chaos}}(c, t_{\text{obs}}) \supseteq M \), we need to show that \( m_2 \in k_L^{\text{ESq}-\text{chaos}}(c, t_{\text{obs}}) \).

Note that \( m_1 \) and \( m_2 \) differ only in locations with policies that are protected by set \( S \). That is, for all locations \( l \in \text{Loc} \), if \( m_1(l) \neq m_2(l) \) then \( \Gamma(l) = \sigma_p \implies \text{protected}_l(p, S) \). Why? Suppose for some \( l \), s.t. \( \Gamma(l) = (\sigma_p, r_t) \) let \( m_1(l) \neq m_2(l) \) and \( \neg \text{protected}_l(p, S) \). So, \( p = \ell \) or \( \ell_1 \) and \( \ell_2 \) s.t. \( \ell_1, \ell_2 \notin S \). Then for some \( m_j \in M \), we have \( m_1(l) = m_j(l) \). Since \( M \) is computed by the intersection of all such memories, every memory \( m'' \in M \) should satisfy \( m''(l) = m_1(l) \). This implies \( m_2(l) = m_1(l) \) which is a contradiction. Thus \( \text{protected}_l(p, S) \) must hold.

Also note that \( m_1 \) and \( m_2 \) satisfy

\[
\forall e \in H, \mu \vdash_\delta \langle e, r_{\text{init}}, m_1, K \rangle \downarrow v \iff \mu \vdash_\delta \langle e, r, m_2, K \rangle \downarrow v
\]

We will construct an \( \text{IMPE}^{E_{\text{chaos}}} \) execution that represents the IMPE executions starting from \( m_1 \) and \( m_2 \). Type-preservation of \( \text{IMPE}^{E_{\text{chaos}}} \) (Lemma 21) will ensure that both executions produce the same observable trace, thus showing that \( m_2 \in k_L^{\text{ESq}-\text{chaos}}(c, t_{\text{obs}}) \).

Let \( \text{IMPE}^{E_{\text{chaos}}} \) memory \( m = \text{merge}(m_1, m_2) \) and \( \mu \vdash \langle c, r_{\text{init}}, m, K \rangle \downarrow 2^{E_{\text{chaos}}} \cdot m^*; K \triangleright t^* \) such that the attacker modifies the program in the same way in both the executions. By the adequacy of \( \text{IMPE}^{E_{\text{chaos}}} \) (Lemma 18), we have that the \( \text{IMPE}^{E_{\text{chaos}}} \) execution represents IMPE executions with \( m_1 \) and \( m_2 \) as initial memories.

Let \( t^* = t^*_\text{pre} \cdot t^*_\text{obs} \cdot t^*_\text{post} \) for some \( t^*_\text{obs} \) such that \( \lfloor t^*_\text{obs} \rfloor_1 = t_{\text{obs}} \). Define observation overlapped (same as the function defined in Section E.1.3 but repeated here for the ease of reference) by an \( \text{IMPE}^{E_{\text{chaos}}} \) trace \( t^* \) as:

\[
\text{obsOverlap}(t^*, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post}) = \begin{cases} 
\epsilon & \text{if } t^* \leq t^*_\text{pre} \\
 t^*_\text{obs} & \text{if } t^*_\text{pre} \cdot t^*_\text{obs} \leq t^* \\
 t^*_\text{obs} & \text{if } t^* = t^*_\text{pre} \cdot t^*_\text{post} \text{ and } t^*_\text{obs} \leq t^* \\
 t^* & \text{if } t^* = t^*_\text{obs} \end{cases}
\]

Intuitively, \( \text{obsOverlap}(t^*, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post}) \) defines part of input trace \( t^* \) that overlaps with an observed trace \( t^*_\text{obs} \).

Since \( L, \mu, \Gamma, K, \emptyset \vdash c : \Gamma', K' \), from Lemma 16 we have \( L, \mu, \Gamma, K, \emptyset \vdash \delta_{\text{SHi}^\circ} c : \Gamma', K' \) and so \( L, \mu, \Gamma, K, \emptyset \vdash \delta_{\text{SHi}^\circ} c : \Gamma', K' \). Note that our initial configuration satisfies

\[
L, N, \Gamma, \emptyset \vdash \delta_{\text{SHi}^\circ} \langle c, r_{\text{init}}, m, \emptyset \rangle : \Gamma' \triangleright \epsilon.
\]

**Lemma 23** (Observational Equivalence is Preserved). \( \text{Let } S \text{ be the set of conditions that are set(non-zero) in some observed trace } t_{\text{obs}}. \text{ If } c, r, m, K : \Gamma', K' \triangleright \epsilon \text{ and } \mu \vdash \langle c, r, m, K \rangle \downarrow 2^{E_{\text{chaos}}} \hat{r}; \hat{m}; K \triangleright \epsilon, \text{ then}
\]

\[
[\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_1 \equiv_L [\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_2
\]

**Proof.** The proof follows by induction on the derivation of \( \mu \vdash \langle c, r, m, K \rangle \downarrow 2^{E_{\text{chaos}}} \hat{r}; \hat{m}; K \triangleright \epsilon. \)

**Case ESq-Skip:** Emitted trace is empty.

**Case ESq-Assign:** Emitted trace is empty.

**Case ESq-Declassify:** Emitted trace does not include \textit{out} event.

**Case ESq-Update:** Emitted trace is empty.

**Case ESq-Kill:** Emitted trace is empty.

**Case ESq-SetCnd:** Emitted trace does not include \textit{out} event.

**Case ESq-Output:** Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SHi}^\circ} \langle c, r, m, K \rangle : \Gamma', K' \triangleright \epsilon \) and \( \mu \vdash \langle \text{output } e \in \ell, r, m, K \rangle \downarrow 2^{E_{\text{chaos}}} \hat{r}; m; K \triangleright \epsilon \), \( \hat{t} = \text{Mem}(m) \cdot \text{Out}(\ell, v). \text{ Let } \hat{t} = \text{Mem}(m) \cdot \text{Out}(\ell, v). \text{ From the premise of } T\text{-ESq-CONFIG}, \text{ we have}
\]

\[
[\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_1 \equiv_L [\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_2
\]

**Case ESq-If-Else:** Given \( pc, \mu, \Gamma, U \vdash \delta_{\text{SHi}^\circ} \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle : \Gamma', K' \triangleright \epsilon \) and \( \mu \vdash \langle \text{if } e \text{ then } c_1 \text{ else } c_2, r, m, K \rangle \downarrow 2^{E_{\text{chaos}}} \hat{r}; m; K' \triangleright \epsilon. \text{ Let } \hat{t} = \hat{t}. \text{ Since } \mu \vdash_\delta \langle c, r, m, K \rangle \downarrow v \text{ such that } v \text{ is not a pair, applying induction hypothesis to the premises of ESQ-IF-ELSE gives us}
\]

\[
[\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_1 \equiv_L [\text{obsOverlap}(\hat{t}, t^*_\text{pre}, t^*_\text{obs}, t^*_\text{post})]_2
\]
Case ESq-While: Given $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{while } e \text{ do } c, r, m, K) : \Gamma, K \bullet ok$ and $\mu \vdash (\text{while } e \text{ do } c, r, m, K) \bullet \varphi^2_{E1}\text{-chaos} v$ such that $v$ is not a pair, applying induction hypothesis to the premise of ESQ-While gives us

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

From Lemma 22, we have $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{while } e \text{ do } c, r', m', K) : \Gamma, K \bullet ok$. Applying induction hypothesis to $\mu \vdash (\text{while } e \text{ do } c, r', m', K) \bullet \varphi_{E1}\text{-chaos} r''; m''; K'' \triangleright t''$, we have

\[ \{|obsOverlap(t'', t_{\text{pre}}'', t_{\text{obs}}'', t_{\text{post}}'')\}|_L \| obsOverlap(t'', t_{\text{pre}}'', t_{\text{obs}}'', t_{\text{post}}'')\|_2 \]

Hence

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

Case ESq-If-Div: Given $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{if } \mu \vdash \langle \mu \rangle \bullet \varphi_{E1}\text{-chaos} r; m; K \triangleright t \bullet \langle \mu \rangle)\langle \mu \rangle$ and $\mu \vdash (\text{if } \mu \vdash \langle \mu \rangle \bullet \varphi_{E1}\text{-chaos} r; m; K \triangleright t \bullet \langle \mu \rangle)\langle \mu \rangle$ such that $v$ is not a pair, applying induction hypothesis to the premise of ESQ-Call gives us

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

Case ESq-Call: Given $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{call}(e), r, m, K) : \Gamma', K' \bullet ok$ and $\mu \vdash (\text{call}(e), r, m, K) \bullet \varphi_{E1}\text{-chaos} r'; m'; K' \triangleright t'$. Since $\mu \vdash (e, r, m, K) \bullet \varphi^2_{E1}\text{-chaos} v$ such that $v$ is a pair, we have $\text{protected}_I(p, S)$. From the well-typeness, neither $c_1$ nor $c_2$ do emit any out events to $L$ channel. Hence

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

Case ESq-If-Div: Given $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{if } \mu \vdash \langle \mu \rangle \bullet \varphi_{E1}\text{-chaos} r; m; K \triangleright t \bullet \langle \mu \rangle)$ and $\mu \vdash (\text{if } \mu \vdash \langle \mu \rangle \bullet \varphi_{E1}\text{-chaos} r; m; K \triangleright t \bullet \langle \mu \rangle)$ such that $v$ is a pair, we have $\text{protected}_I(p, S)$. From the well-typeness, command $c$ does not emit any out events to $L$ channel. Hence

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

Case ESq-Call-Div: Given $pc, \mu, \Gamma, U \vdash_{\deltaSHmioNc} (\text{call}(e), r, m, K) : \Gamma', K' \bullet ok$ and $\mu \vdash (\text{call}(e), r, m, K) \bullet \varphi_{E1}\text{-chaos} r; m; K \triangleright t \bullet \langle \mu \rangle$, from the premises of T-ESQ-CONF, we have $pc, \mu, \Gamma, K, U \vdash_{\deltaSHmioE_{\text{I}}} (\text{call}(e), r, m, K) \bullet \varphi_{E1}\text{-chaos} v$ such that $v$ is a pair, we have $\text{protected}_I(p, S)$. From the well-typeness, command $c$ does not emit any out events to $L$ channel. Hence

\[ \{|obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\}|_L \| obsOverlap(t', t_{\text{pre}}', t_{\text{obs}}', t_{\text{post}}')\|_2 \]

Case ESq-Seq: Given $pc, \mu, \Gamma, 0, U \vdash_{\deltaSHmioNc} (c_1; \ldots; c_n, r_0, m_0, K_0) : \Gamma_n, K_n \bullet ok$ and $\mu \vdash (c_1; \ldots; c_n, r_0, m_0, K_0) \bullet \varphi_{E1}\text{-chaos} r_n; m_n; K_n \triangleright t_1 \ldots \ldots t_N$. From the premises of T-ESQ-CONF, we have $pc, \mu, \Gamma, 0, U \vdash_{\deltaSHmioE_{\text{I}}} (c_1; \ldots; c_n : \Gamma_n, K_n)$. Applying induction hypothesis to the premise, $\mu \vdash (c_1, r_0, m_0, K_0) \bullet \varphi^2_{E1}\text{-chaos} r_1; m_1; K_1 \triangleright t_1$, we have

\[ \{|obsOverlap(t_1, t_{\text{pre}}_1, t_{\text{obs}}_1, t_{\text{post}}_1)\}|_L \| obsOverlap(t_1, t_{\text{pre}}_1, t_{\text{obs}}_1, t_{\text{post}}_1)\|_2 \]

From Lemma 22, we have $pc, \mu, \Gamma, 1, U \vdash_{\deltaSHmioNc} (c_2, r_1, m_1, K_1) : \Gamma_2, K_2 \bullet ok$. Applying induction hypothesis to the next premise, $\mu \vdash (c_2, r_1, m_1, K_1) \bullet \varphi^2_{E1}\text{-chaos} r_2; m_2; K_2 \triangleright t_2$, we have

\[ \{|obsOverlap(t_2, t_{\text{pre}}_2, t_{\text{obs}}_2, t_{\text{post}}_2)\}|_L \| obsOverlap(t_2, t_{\text{pre}}_2, t_{\text{obs}}_2, t_{\text{post}}_2)\|_2 \]

Applying the induction hypothesis continuously thus gives,

\[ \{|obsOverlap(t_n, t_{\text{pre}}_n, t_{\text{obs}}_n, t_{\text{post}}_n)\}|_L \| obsOverlap(t_n, t_{\text{pre}}_n, t_{\text{obs}}_n, t_{\text{post}}_n)\|_2 \]
Case ESeq-Enclave: Given \( pc, \mu, \Gamma, U \vdash_{\text{T-Seq-Config}} \langle \text{enclave}(i, c), r, m, K \rangle: \Gamma', K' \bullet ok \) and \( N \vdash \langle \text{enclave}(i, c), r, m, K \rangle \bullet \gamma \). From the premises of T-ESQ-CONFIG, we have \( pc, \mu, \Gamma, K, U \vdash_{\text{T-Seq-Config}} \langle \text{enclave}(i, c) \rangle: \Gamma', K' \). From Lemma 23, we have \( pc, E_i, \Gamma, \emptyset \vdash_{\text{T-Seq-Config}} \langle c, r, m, K \rangle: \Gamma', K' \bullet ok \). Applying induction hypothesis to the premise \( E_i \vdash \langle c, r, m, K \rangle \bullet \gamma \), we have

\[
\text{obsOverlap}(t', t'_{\text{pre}}, t'_{\text{obs}}, t'_{\text{post}})\]

Hence proved that \( m_2 \in k_{L_{E_{\text{-choa}}}}(c, t_{obs}) \).

E.2 Translation is Sound

Lemma 24. Let \( \mathcal{G} \) be an IMPS type environment that corresponds to \( \gamma \) and is well-typed for \( \delta \). Also let \( \text{pc}, \mathcal{G}, \emptyset \vdash c: \mathcal{G}' \) and \( c_{\text{sub}} \) is a sub-command of \( c \). If \( \text{pc}', \mathcal{G}, U \vdash c_{\text{sub}}: \mathcal{G}' \) such that \( U \neq \emptyset \) then \( \exists c', c_1, c_2, \text{cnd. such that} \)

\[
c' \overset{\text{def}}{=} \begin{cases} \text{if isunset(cnd) then } c_1 \text{ else } c_2 \\ \text{OR} \\ \lambda c_1 \end{cases}
\]

is a sub-command of \( c \) and \( c_{\text{sub}} \) is a sub-command of \( c_1 \).

Proof. Given \( pc, \mathcal{G}, \emptyset \vdash c: \mathcal{G}' \). It is easy to see that the only typing judgments that add to the set \( U \) for the sub-commands are T-FUNCTION and T-IF-ISUNSET. The remaining judgments T-ASSIGN, T-UPDATE, SEQ, T-IF-ELSE T-WHILE, T-SETCND, T-CALL and T-KILL do not change the set \( U \) for their sub-commands. Rule T-ENCLAVE nullifies the set \( U \) for its sub-commands.

Also given \( \text{pc}', \mathcal{G}, U \vdash c_{\text{sub}}: \mathcal{G}' \) such that \( U \neq \emptyset \). This is possible only if \( c_{\text{sub}} \) is a sub-command for some \{ if isunset(cnd) then \( c_1 \) else \( c_2, \lambda c_1 \} \)

Hence proved.

We now present the proof for Theorem 2.

Proof. The proof is by mutual induction on the translation derivation of expressions and commands.

1. For all expressions \( e \in \text{IMPS} \), if \( \mathcal{G} \vdash e: \sigma_p \) and \( \mathcal{G}, e, \sigma_p \vdash \mu, \Gamma, \delta, c', \sigma_p \) then \( \mu, \Gamma \vdash_{\gamma} e': \sigma_p \).
2. For all commands \( c \in \text{IMPS} \), if \( \mathcal{G}, \mu, \Gamma, U \vdash c: \mathcal{G}' \) and \( \mathcal{G}, \mu, \Gamma, U \vdash \mathcal{G}', \Gamma \vdash_{\gamma} c': \Gamma', K' \) then \( \mathcal{G}, \mu, \Gamma, U \vdash_{\gamma} c': \Gamma', K' \).

Case TR-INT: Given \( \mathcal{G}, n, \text{int}_p \vdash \mu, \Gamma, \delta, n, \text{int}_p \). From T-INT, we have \( \mu, \Gamma \vdash_{\gamma} n : \text{int}_p \).

Case TR-VAR: Given \( \mathcal{G}, x, \sigma_p \vdash \mu, \Gamma, \delta, x, \sigma_p \). Hence \( \Gamma(x) = \sigma_p \). From T-VAR, we have \( \mu, \Gamma \vdash_{\gamma} x : \sigma_p \).

Case TR-LOC: Given \( \mathcal{G}, l, (\sigma_p \text{ref}^\text{rt})_q \vdash \mu, \Gamma, \delta, l, (\sigma_p \text{ref}^\text{rt})_q \). From the premises, \( \Gamma(l) = \sigma_p \) and \( \delta(l) = \mu' \). From T-LOC, we have \( \mu, \Gamma \vdash_{\gamma} l : (\sigma_p \text{ref}^\text{rt})_L \).

Case TR-CND: Given \( \mathcal{G}, \text{cnd} \vdash \mu, \Gamma, \delta, \text{cnd}, \text{cnd}^\mu \). From premise, \( \delta(\text{cnd}) = \mu' \). Since \( \text{cnd} \in \text{Cond} \), from T-CND, we have \( \mu, \Gamma \vdash_{\gamma} \text{cnd} : \text{cond}^\mu \).

Case TR-DEREF: Given \( \mathcal{G}, e, \sigma_{\text{plq}} \vdash \mu, \Gamma, \delta, e', \sigma_{\text{plq}} \). Applying induction hypothesis to the premise, we have \( \mu, \Gamma \vdash_{\gamma} e' : (\sigma_{\text{p}} \text{ref}^\text{rt})_q \). Also, \( \mu' \neq N \implies \mu' = \mu \). From T-DEREF, we have \( \mu, \Gamma \vdash_{\gamma} e' : \sigma_{\text{plq}} \).

Case TR-ISUNSET: Given \( \mathcal{G}, \text{isunset}(\text{cnd}) \vdash \mu, \Gamma, \delta, \text{isunset}(\text{cnd}) \). From the premises, \( \delta(\text{cnd}) = \mu' \) and \( \mu' \neq N \implies \mu = \mu' \). From T-ISUNSET, we have \( \mu, \Gamma \vdash_{\gamma} \text{isunset}(\text{cnd}) : \text{int}_L \).

Case TR-FUNCTION: Given \( \mathcal{G}, \lambda c. (\mathcal{G}', U \stackrel{\beta}{\rightarrow} \mathcal{G}'_q) \vdash \mu, \Gamma, \delta, \lambda^\mu. c', (\Gamma^{-}, K^{-}, U \stackrel{\beta}{\rightarrow} \Gamma^{-} \text{ c }, \Gamma, K) \). Since \( \mathcal{G} \vdash \lambda c. \mathcal{G}' \vdash \lambda^\mu. c \vdash \mathcal{G}' \). We have \( q = L \). Applying induction hypothesis to the premise, we have \( \mu, \Gamma \vdash_{\gamma} \lambda^\mu. c : (\Gamma^{-}, K^{-}, U \stackrel{\beta}{\rightarrow} \Gamma^{-} \text{ c }, \Gamma, K) \).

Case TR-OP: Given \( \mathcal{G}, e_1 \oplus e_2, \sigma_{\text{plq}} \vdash \mu, \Gamma, e_1 \oplus e_2, \sigma_{\text{plq}} \). Applying induction hypothesis to the premises, we have \( \mu, \Gamma \vdash_{\gamma} e_1 : \text{int}_L \). From T-OP, we have \( \mu, \Gamma \vdash_{\gamma} e_2 : \text{int}_L \).

Case TR-SKIP: Given \( pc, \mathcal{G}, U, \text{skip} \vdash \mu, \Gamma, K, \text{skip}, \Gamma, K \). From T-SKIP, we have \( pc, \mu, \Gamma, K, U \vdash_{\gamma} \text{skip} : \Gamma, K \).
Case TR-ASSIGN: Given $pc, G, U, x := e, G[x \mapsto \sigma_{pc, G}] \vdash \mu, \Gamma, K, e \vdash e': \Gamma, K[U \vdash x := e': \Gamma]$. From the well-typedness of IMPS expression $e$, i.e., $G \vdash e: \sigma_g$, we have $q \neq \top$. Applying induction hypothesis to the premise we have $\mu, \Gamma \vdash_3 e': \sigma_q$. From the premises, $(pc \sqcup q) \not\subseteq L$ \iff $\mu \neq N, \mu \notin K$ and T-ASSIGN, we thus have $pc, \mu, \Gamma, K[U \vdash x := e': \Gamma[x \mapsto \sigma_{pc, G}], K]$.

Case TR-DECLASSIFY: Given $L, G, U, x := \text{declasify}(e), G[x \mapsto \sigma_L] \vdash \mu, \Gamma, K, e \vdash \text{declasify}(e'), \Gamma, K[U \vdash x := e': \Gamma]$. From the well-typedness of IMPS expression $e$, i.e., $G \vdash e: \sigma_g$, we have $q \neq \top$. Applying induction hypothesis to the premise we have $\mu, \Gamma \vdash_3 e': \sigma_q$. From the premises, $(pc \sqcup q) \not\subseteq L$ \iff $\mu \neq N, \mu \notin K$ and T-DECLASSIFY, we thus have $pc, \mu, \Gamma, K[U \vdash x := \text{declasify}(e'): \Gamma[x \mapsto \sigma_L], K]$.

Case TR-OUTPUT: Given $pc, G, U$, output $e$ to $\ell, G \vdash \mu, \Gamma, K, e \vdash e': \ell, \Gamma, K$. From the well-typedness of IMPS expression $e$, i.e., $G \vdash e: \sigma_g$, we have $p \neq \top$. Applying induction hypothesis to the premise we have $\mu, \Gamma \vdash_3 e': \sigma_q$. From T-OUTPUT, we thus have $pc, \mu, \Gamma, K[U \vdash \text{output}(e): \ell : \Gamma]$.

Case TR-SETCND: Given $pc, G, U, \text{set}(cnd), G \vdash \mu, \Gamma, K, e \vdash \text{set}(cnd), \Gamma, K$. From the well-typedness of IMPS command set(cndvar), i.e., $pc, G, U \vdash \text{set}(\text{cnd}): G$, we have $\text{cnd} \in \text{Cond} \setminus U$. From the premises and T-SETCND, we thus have $pc, \mu, \Gamma, K[U \vdash \text{set}(\text{cnd}) : \Gamma]$.

Case TR-UPDATE: Given $pc, G, U, e_1 \leftarrow e_2, G \vdash \mu, \Gamma, K, e_1 \vdash e_2: G$, we have $p, p', q \neq \top$. Applying induction hypothesis to the premises, we have $\mu, \Gamma \vdash_3 e_1 : (\sigma_{p'} \text{ref}^p)$ and $\mu, \Gamma \vdash_3 e_2 : \sigma_{p'}$. From the remaining premises and T-UPDATE, we thus have $pc, \mu, \Gamma, K[U \vdash e_1 \leftarrow e_2 : \Gamma, K]$.

Case TR-SEQ: Given $pc, G_0, U, c_1 \ldots c_n, G_n \vdash \mu_0, \Gamma_0, K, e, \Gamma_n, K_{n+1}$. Applying induction hypothesis to the premises, we have $\forall i \in \{1 \ldots n\}, \mu_i, \Gamma_{i-1}, K_{i-1}, U \vdash c_i : \Gamma_i, K_i$.

Consider the case when processSeqOutput($i^{th}_n; \mu_0, \mu_1, K_0, K_1, K_2$) matches $(\mu_0, \mu_1, K_0, K_1, K_2, K_{m+2})$. To show that the output $\text{enclave}(j, c'_1 \ldots c'_m); \text{processKill}(K_{m-1})$ is well-typed, we first show that $pc, \mu_0, \Gamma_0, K_0, U \vdash \text{enclave}(j, c'_1 \ldots c'_m); \Gamma_{m-1}, K_{m-1}$. If $U \neq \emptyset$, from Lemma 24, we have that $c'_1 \ldots c'_m$ is a sub-command of some if isunset(cnd) then $s_1$ else $s_2$. However, rule TR-IF-ISONSET ensures that $\mu_0 \neq N$ which is a contradiction (since $\mu_0 = N$). Hence $U = \emptyset$. Premise $\mu_0 \neq N \implies (\mu_0 = \mu_i \land K_{m}' = \emptyset)$ ensures that $K_1 = K_2 = \ldots = K_{m-1} = K_{m-1}$ from T-ENCLAVE, we thus have $pc, \mu_0, \Gamma_0, K_1, \Gamma, K \vdash \text{enclave}(j, c'_1 \ldots c'_m); \Gamma_{m-1}, K_{m-1}$.

Next we show that $pc, \mu_0, \Gamma_{i-1}, K_{i-1}, U \vdash \text{kill}(j); \Gamma_i, K_{i-1}' \cup K_{i-1}''$. Consider the case when processKill($K_i$) matches $K_i' \cup K_i''$. From premise $K_{i}' \cap K_{i}'' = \emptyset$ we have $K_{i}' \neq K_{i-1}'$. Also, premise $\mu_0 \neq N \implies (\mu_0 = \mu_i \land K_{m}' = \emptyset)$ ensures that $\mu_0 = N$. From T-KILL, we have $pc, \mu_0, \Gamma_{i-1}, K_{i-1}' \cup K_{i-1}'' \vdash \text{kill}(j); \Gamma_i, K_{i-1}' \cup K_{i-1}''$. Thus the output $\text{enclave}(j, c'_1 \ldots c'_m); \text{processKill}(K_{m-1})$ is well-typed.

For the remaining cases, no enclave or kill statements are inserted. From T-SEQ, we thus have $pc, \mu, \Gamma_0, K_0, U \vdash \text{cnd} : \Gamma, K_n$.

Case TR-IF-ELSE: Given $pc, G, U, e$ then $c_1$ else $c_2, G' \Rightarrow \mu, \Gamma, K, e$ then $c'_1$ else $c'_2, \Gamma'$, $\mu'$, $\Gamma'$. Applying induction hypothesis to the premises, we have $\mu, \Gamma \vdash_3 e : \text{int}_p$ and $pc, \mu', \Gamma, K, U \vdash c_i : \Gamma', K'$ for $i = \{1, 2\}$. From the remaining premises and T-IF-ELSE, we thus have $pc, \mu, \Gamma, K, U \vdash e$ then $c_1$ else $c'_2 : \Gamma', K'$.

Case TR-IF-ISONSET: Given $pc, G, U, \text{if} \text{isunset}(\text{cnd}) \text{then} c_1 \text{else} c_2, G' \Rightarrow \mu, \Gamma, K, \delta, \Gamma', K'$, $\mu'$, $\Gamma'$. Applying induction hypothesis to the premises, we have $\mu, \Gamma \vdash_3 e : \text{int}_p$ and $pc, \mu', \Gamma, K, U \vdash c_i : \Gamma', K'$ for $i = \{1, 2\}$. From T-IF-ISONSET, we thus have $pc, \mu, \Gamma, K, U \vdash \text{if} \text{isunset}(\text{cnd}) \text{then} c_1 \text{else} c_2 : \Gamma', K'$.

Case TR-WHILE: Given $pc, G, U, \text{while} \text{do} e, G, \Gamma, K, \delta, \text{while} \text{do} e', \Gamma, K$. Applying induction hypothesis to the premises, we have $\mu, \Gamma \vdash_3 e : \text{int}_p$ and $pc, \mu', \Gamma, K, U \vdash c_i : \Gamma', K'$ for $i = \{1, 2\}$. From the remaining premises and T-WHILE, we thus have $pc, \mu, \Gamma, K, U \vdash e \text{while} \text{do} c : \Gamma, K$.

Case TR-CALL: Given $pc, G, U, \text{call}(e), G_{\text{out}} \Rightarrow \mu, \Gamma, K, \delta, \text{call}(e'), G_{\text{out}}, G_{\text{out}}$. Applying induction hypothesis to the premise, we have $\mu, \Gamma \vdash_3 e : \text{Gamma}, K', U \vdash e' : \Gamma^+, K^+'$. From the remaining premises and T-CALL, we thus have $pc, \mu, \Gamma, K, U \vdash \text{call}(e') : G_{\text{out}}, K^+'$.

Case TR-SUB: Given $pc_2, G_2, U, c, G_2' \Rightarrow \mu, \Gamma_2, K, \delta, c', \Gamma_2', K'$. Applying induction hypothesis to the premise, we have $pc_1, \mu_1, K_1, U \vdash c : \Gamma_1', K'$. Also from translation of typing environment in Figure 10, we have $\vdash_3 \Gamma_i \text{ok}$ for $i = \{1, 2\}$. From the remaining premises and T-SUB, we thus have $pc_2, \mu_2, K_2, U \vdash e : \Gamma_2', K'$.