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Information Flow Control for Distributed Trusted Execution Environments

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Abstract—Distributed applications cannot assume that their security policies will be enforced on untrusted hosts. Trusted execution environments (TEEs) combined with cryptographic mechanisms enable execution of known code on an untrusted host and the exchange of confidential and authenticated messages with it. TEEs do not, however, establish the trustworthiness of code executing in a TEE. Thus, developing secure applications using TEEs requires specialized expertise and careful auditing.

This paper presents DFLATE, a core security calculus for distributed applications with TEEs. DFLATE offers high-level abstractions that reflect both the guarantees and limitations of the underlying security mechanisms they are based on. The accuracy of these abstractions is exhibited by asymmetry between confidentiality and integrity in our formal results: DFLATE enforces a strong form of noninterference for confidentiality, but only a weak form for integrity. This reflects the asymmetry of the security guarantees of a TEE: a malicious host cannot access secrets in the TEE or modify its contents, but they can suppress or manipulate the sequence of its inputs and outputs. Therefore DFLATE cannot protect against the suppression of high-integrity messages, but when these messages are delivered, their contents cannot have been influenced by an attacker.

Index Terms—information flow control, language-based security, trusted execution environment, enclaves, distributed systems, security

I. INTRODUCTION

Many applications rely on security checks in compilers and runtime systems to enforce security policies. In distributed decentralized settings (where applications are distributed, entities involved in the application may be mutually distrusting, and no single node is trusted by all entities), the effectiveness of such checks is limited: local security checks cannot ensure that a remote host will protect confidential information it receives. Encryption can ensure that an untrusted host cannot reveal secrets, but it also prevents the host from performing general computation on encrypted data. Lack of trust between entities may require data to be hosted separately from computations that use it, hurting performance. Worse, it is possible that no entity is sufficiently trusted to both access the data and compute the result, limiting what the application can do.

Trusted Execution Environments (TEEs) such as SGX [30, 4] and Sanctum [14] address some of these limitations with application enclaves. An enclave is a protected user-level process that is strongly isolated from the OS and other applications by trusted hardware. Remote nodes can verify the integrity of code running in an enclave using a remote attestation protocol. Once verified, the remote node knows that runtime security checks are still present in the code, and that static properties verified during compilation are still valid.

TEEs by themselves are insufficient to enforce security policies. For instance, inputs and outputs to TEEs must be correctly encrypted, signed, decrypted, and verified to protect against malicious hosts. Even with correct use of cryptography, the application must be written to ensure that it does not inappropriately reveal confidential information nor allow entities to inappropriately influence computations. Although previous work has combined techniques to enforce strong application-level confidentiality and integrity guarantees with correct-by-construction use of cryptography [29, 43, 41], no such previous work supports TEEs, and extending them to do so is nontrivial.

This work presents Distributed Flow-Limited Authorization for Trusted Execution (DFLATE), a core calculus for secure decentralized distributed applications. DFLATE extends the Flow-Limited Authorization Calculus (FLAC) [5] with distributed execution, communication channels, concurrency, and TEEs. DFLATE’s type system enforces confidentiality and integrity guarantees that are consistent with standard cryptographic mechanisms and TEE platforms.

To better understand how TEEs work, and the challenges in building secure applications that use them, consider an example of a simple distributed application, illustrated in Figure 1. Here, “Enclave” refers to code running in a TEE on Bob’s node. The only way for Alice to interact with the enclave is via Bob, whom Alice does not trust. To establish the authenticity of the enclave, Alice uses a remote attestation protocol. First, Alice requests a remote attestation from Bob (message 0), who requests a secure measurement of the enclave code from the TEE: a cryptographic hash of the loaded binary (message 1). This hash, as well as additional parameters for establishing a secure channel, is signed by a key that has been securely provisioned to the TEE (message 2). Next, Bob relays the signed message to Alice (message 3), who inspects the measurement to ensure the expected code is running, and verifies the signature to ensure it is from an authentic TEE.

Once the signature is verified, Alice uses the security parameters included in the message to establish a secure authenticated
channel to the enclave. Alice uses this channel to provide decryption and signing keys to the enclave (messages 4 and 5). Later, she can use these keys to exchange encrypted and signed inputs and outputs with the enclave (messages 6-9) without repeating the remote attestation protocol.

Omitting or improperly executing any of the above steps can undermine Alice’s security. If the remote attestation is omitted, Alice has no guarantee that the enclave code (and not some malicious version of it) is running nor that code execution is protected from Bob. If Alice fails to encrypt (or sign) inputs to the enclave, or uses keys that are accessible to Bob, then Bob can learn (or modify) the inputs. Similarly, if the enclave fails to properly encrypt and sign outputs, Bob may be able to read or modify them.

Fortunately, the security and correctness of the first three messages is mostly independent of the application. So a relatively simple (but trustworthy) library API or language extension can provide remote attestation capabilities to applications and eliminate programmer errors.

But even with remote attestation and proper encryption and authentication, Alice’s security may still be undermined. Although Bob cannot decrypt messages between Alice and the enclave, he does see each encrypted message when it is transmitted and may be able to infer secret information based on the sequence of exchanged messages. For example, the pseudocode below sends an encrypted and signed message \texttt{msg} from within an enclave over channel \texttt{ch} if \texttt{h} is true.

```plaintext
if h then send ch (enc (sign msg)) else ()
```

Because of the TEE and the cryptographic mechanisms, Bob cannot directly access \texttt{h} or \texttt{msg}, but he can infer the value of \texttt{h} based on whether a message is sent. The above code might also be problematic in terms of integrity: if Bob can influence the value of \texttt{h}, he can suppress the message. Similar code might permit Bob to replay messages or permute the message order.

**Information-flow control (IFC)** is well suited to protect against these kinds of vulnerabilities because it enables end-to-end semantic guarantees such as noninterference, which ensures an attacker cannot infer secret information from public outputs. However, existing IFC languages cannot precisely model the security guarantees and limitations of TEEs.

There are two key challenges to enforcing IFC in a decentralized distributed setting that employs encryption, signatures, and TEEs. The first challenge is to (symbolically) represent the security guarantees of the cryptographic mechanisms without abstracting away the power of the attacker to permute, suppress, or infer secrets from the message sequence. Security models of existing distributed IFC systems (Fabric [29] and DStar [43]) are insufficiently precise. Encryption and digital signatures allow secret or high-integrity messages to be sent over untrusted channels. For example, Alice could sign and send a message to the enclave over a channel controlled by Bob; if the enclave receives the message it knows (by verifying the signature) that it is from Alice, even though Bob could suppress the message. In Fabric and DStar, the only sound policy (that doesn’t ignore a potential attack) expresses that both Alice and Bob might have influenced the message. In other words, they are too coarse-grained to distinguish the attacker’s influence on control flow from its influence on data flow. Consequently, their enforcement mechanisms cannot determine if code respects the programmer’s intended policy.

This scenario arises in any nontrivial application using TEEs, since the main benefit of TEEs is to run computation on potentially malicious nodes. So IFC must be able to reason about protected data flowing through untrusted nodes.

The second challenge is to design high-level abstractions that accurately reflect the guarantees of TEEs in a decentralized distributed setting. Currently, developers integrate TEEs into their applications using low-level library APIs. Using these libraries correctly may require a different skill set from that needed for the rest of the application. A better approach would be to design high-level programming abstractions for TEEs that don’t burden the developer with low-level implementation details. Code expressed with these abstractions can be used to synthesize low-level implementations, shifting trust from application developers to the compiler.

Finding the right security abstraction for TEEs in decentralized settings is challenging. TEEs ensure that specific code is running securely, but, as discussed above, do not ensure the trustworthiness of the code. So different entities may trust different enclaves (perhaps based on who wrote the enclave code or analysis of the code). TEE mechanisms don’t hide the existence of messages to and from an enclave, nor guarantee message delivery. A suitable TEE abstraction must reflect these limitations on communication and allow entities to express their trust in specific TEEs and entities.

**DFLATE** addresses these challenges. DFLATE has sufficiently fine-grained information-flow control to distinguish (and usefully reason about) important TEE use cases. DFLATE provides language abstractions for TEEs, distribution, and security principals that can ensure security while enabling applications to benefit from the powerful features of TEEs.

DFLATE is the first language to enforce end-to-end information security for distributed applications with TEEs. We prove that well-typed DFLATE programs enjoy noninterference guarantees. Confidentiality noninterference [23] ensures that an attacker cannot infer secret information from public outputs. Integrity noninterference ensures that an attacker cannot influence high-integrity outputs by modifying low-integrity inputs.

Integrity is dual to confidentiality [10], and thus most systems that protect confidentiality noninterference also protect integrity noninterference. However, DFLATE provides asymmetric guarantees for confidentiality and integrity. This asymmetry reflects inherent limitations of TEEs. The confidentiality
and integrity of the contents of inputs and outputs to TEEs can be cryptographically protected, but neither the TEE itself nor cryptographic mechanisms can prevent the host of the TEE from suppressing or manipulating the sequence of inputs and outputs. Hence, we derive strong noninterference results for confidentiality, but weaker results for integrity that hold only when messages are not suppressed.

II. MOTIVATING THE DFLATE DESIGN

DFLATE is a high-level language designed to be implemented using cryptographic mechanisms and trusted execution environments. Designing an IFC model in this setting is subtly different than designing a general IFC model. In this section we motivate three design features of DFLATE that are informed by cryptography and TEEs.

A. Fine-grained policies for secure communication

Suppose Alice sends a message to Carol via Bob, who is only partially trusted by Alice and Carol. Figure 2(a) illustrates the scenario where no cryptographic mechanisms are used to enforce information security, similar to the model of Fabric and DStar. Sending message A1 to Carol is secure only if Alice permits Bob to learn the contents of A1 and Carol permits Bob to (potentially) modify the contents of A1 en route. Figure 2(b) illustrates the same scenario, but Alice additionally signs the message and encrypts it with Carol’s public key. In this case, Bob can neither learn nor modify the contents of A1. However, Bob does learn of the existence of A1. Furthermore, although Bob cannot modify A1, he could replace it with a previously signed message A2, or could choose to send no message at all.

Most existing IFC abstractions do not distinguish these two scenarios and instead enforce policies conservatively using checks similar to Figure 2(a). This lack of precision effectively ignores guarantees offered by cryptographic mechanisms for communication over untrusted channels.

DFLATE distinguishes the ability to disclose or modify messages sent over a channel from the ability to observe channel traffic and influence or suppress message sequences. In DFLATE, the security of a channel is specified using two policies. One policy governs the confidentiality and integrity of the contents of messages sent over the channel, and the other governs the confidentiality and integrity of contexts in which the channel may be used. A node may receive a message that it can’t read or modify; this can be enforced by signing and encrypting the message. A node should not send a message to an untrusted node in a secret context (even if the message is public), and should not rely on a message from an untrusted node in a high-integrity context (even if the message contents are trusted).

B. Decentralized and distributed trust management

DFLATE’s abstractions are based directly on the capabilities of the underlying cryptographic and TEE mechanisms, which allows stronger assumptions and finer-grained reasoning about what information flows and actions are possible than most previous IFC models. Two places where DFLATE’s design is influenced by the underlying mechanisms are clearance bounds and computation principals.

DFLATE’s type system associates a clearance bound [38] with every node, which restricts what data may be used and produced by computations on that node. Based on trust relationships between the node and other principals, the clearance bound reflects which cryptographic keys the node has access to, and thus models the ability of a node to digitally sign values and decrypt encrypted values. In Figure 2(b), Bob does not have access to Alice’s decryption key, so any computation that attempts to read and compute with Alice’s data would exceed Bob’s clearance. Similarly, Bob would be unable to produce a new message with Alice’s integrity using a DFLATE program, modeling Bob’s inability to access Alice’s signing key.

For each source-level DFLATE computation e that will execute in a TEE, DFLATE defines a unique computation principal t. Code running in a TEE is subject to clearance bounds of the computation principal rather than of the node executing the TEE. DFLATE permits principals to express their trust in code running in a TEE by expressing trust in the corresponding computation principal t. Therefore Alice can express trust in an enclave running on Bob’s node, allowing it to perform computation on her secrets even if Bob is not trusted to do so. DFLATE also provides protection in the other direction: if Bob doesn’t trust Alice or the enclave, Alice can’t use the enclave to leak Bob’s secrets or influence his data.

C. Observability of TEE interactions

TEEs introduce additional subtlety into information flow control design. TEEs provide guarantees similar to those of a trusted third party, but executing code in a TEE on an untrusted node is not equivalent to executing code on a trusted node.

Consider our previous examples, but where Bob executes application code in an enclave E (Figure 2(c)). Although the code executes within an enclave, Bob can still observe and manipulate incoming and outgoing messages, as in Figure 2(b).

Most distributed IFC approaches (e.g., [29, 43]) ignore an attacker’s ability to analyze traffic over communication channels. This is somewhat defensible for attackers with a limited view of the network, or when nodes use obfuscating techniques like TOR [16]. With TEEs, however, ignoring this ability is not as reasonable: in Figure 2(c), Bob is the only available conduit to E. Communicating over an observed untrusted channel is fundamental to the TEE abstraction. DFLATE ensures that programs capture the ability of a host to mediate communication with its enclaves, and enables reasoning about the security of these situations. For node-to-node communication, DFLATE makes similar assumptions to previous models: only the sender and the receiver observe the communication.

III. THE DFLATE LANGUAGE

A. FLAM principal algebra

Security policies in DFLATE are based on the Flow-Limited Authorization Model (FLAM) [6], a principal algebra and logic for reasoning simultaneously about authorization and information flow control policies. Entities in a distributed application (e.g., Alice, Bob, etc.) are represented by names in a set of

2Using LIO-style clearance bounds as a proxy for access to cryptographic keys was first introduced in CLIO [41].
(a) Security checks without cryptography.

(b) Security checks with cryptography.

(c) Security checks with an enclave on Bob.

Fig. 2: Information flow checks with and without cryptographic mechanisms.

primitive principals $\mathcal{N}$. The FLAM algebra provides operations for constructing composite principals from this base set. A FLAM principal refers to the authority of the entity (or entities) represented by that principal. A principal $p$’s authority consists of confidentiality authority, the authority necessary to learn $p$’s secrets, and integrity authority, the authority necessary to influence information trusted by $p$. Authority projections of the form $p^\pi$ where $\pi \in \{\to, \leftarrow\}$ allow us to represent the partial authority of a principal. For example, the principal $p^\to$ denotes a principal with only the confidentiality authority of $p$, and $p^\leftarrow$ denotes a principal with only the integrity authority of $p$.\(^3\) The combined authority of principals $p$ and $q$ is represented by the conjunction $p \land q$, and the selective authority of principals $p$ and $q$ (i.e., the individual authority of either $p$ or $q$) is represented by the disjunction $p \lor q$. The universally trusted principal (with the most authority) is represented by $\top$, and the universally distrusted principal (with the least authority) is $\bot$.

The complete set $\mathcal{P}$ of FLAM principals for any setting is given by the closure of the operations $\land$, $\lor$, $\to$, $\leftarrow$, and $\rightarrow$ over the set of primitive principals $\mathcal{N}$, extended with $\top$ and $\bot$. Principals in this set are related by a preorder $\succcurlyeq$, the “acts for” relation, which orders principals by increasing trust. The equivalence classes\(^4\) of $\succcurlyeq$ form a distributive lattice with $\top$ and $\bot$ as most and least trusted elements, and with $\land$ and $\lor$ as join and meet operations.

The trust ordering $\succcurlyeq$ also induces an ordering on principals specifying safe information flows. We write $p \subseteq q$ when information labeled $p$ may safely flow to principal $q$. The flows-to relation also forms a distributive lattice with $\bot \rightarrow \top$ (public and trusted) as the least restrictive element, and $\top \rightarrow \bot$ (secret and untrusted) as the most restrictive element. The flows-to relation and joins and meets in the information flow lattice are defined in terms of their authority lattice counterparts:

- $p \subseteq q \iff p^\to \succcurlyeq q^\to$ and $q^\leftarrow \succcurlyeq p^\leftarrow$
- $p \land q \equiv (p^\to \land q^\to) \land (p^\leftarrow \lor q^\leftarrow)$
- $p \lor q \equiv (p^\to \lor q^\to) \land (p^\leftarrow \land q^\leftarrow)$

Every principal $p$ is equivalent (under the trust ordering) to a principal in normal form, $q^\to \land r^\leftarrow$, i.e., the conjunction of a confidentiality authority and an integrity authority. The voice $\eta$ of a principal $p$, $\nabla(p)$, is the integrity authority necessary to act on the behalf of the principal. Formally, if $q^\to \land r^\leftarrow$ is the normal form of $p$, then $\nabla(p) = \nabla(q^\to \land r^\leftarrow) = q^\to \land r^\leftarrow$.

B. FLATE syntax and local semantics

The FLATE language is inspired by the Flow-Limited Authorization Calculus (FLAC)\(^5\). Like FLAC, DFLATE is a core calculus and secure programming model that enforces strong information security guarantees. DFLATE extends FLAC with distributed computation, communication, and TEEs, and the DFLATE type system is more compatible with implementations that use cryptographic enforcement mechanisms. This makes DFLATE a more suitable basis for the formal analysis of decentralized distributed applications, or as a core programming model for a general-purpose secure distributed programming language.

Figure 3 shows the DFLATE syntax. Principals are used both to specify the information flow policies on data and to represent the authority of the entities in the distributed application. Metavariables $p$, $q$, $\ell$, and $\tc$ range over principals. We assume the set of primitive principals $\mathcal{N}$ includes computation principals $t$ and nodes $n$, representing, respectively, code executed in a TEE and host machines. Nodes and computation

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\(^3\)One way to remember what each arrow means is to think of confidentiality as secrets “coming from” $p$, and integrity as information “accepted by” $p$.

\(^4\)Principals $p$ and $q$ are in the same equivalence class if and only if $p \succcurlyeq q$ and $q \succcurlyeq p$.

\(^5\)The FLAM syntax and local semantics was inspired by the Flow-Limited Authorization Calculus (FLAC)\(^5\).
principals represent the places where computation occurs, and we use metavariable \( pl \) to range over them.

Metavariables \( v \) and \( e \) range over values and expressions. (Shaded values and expressions are not part of the surface syntax but arise during evaluation.) DFLATE includes standard syntax for variables, tuples, projections of types, tagged unions, case expressions, function application, and type-abstraction application. Term and type abstractions have annotations (principal \( pc \), channel environment \( \Theta \), set of places \( P \), and delegation context \( II \)) that restrict how abstractions can be applied; we discuss this further when we present the type system. We explain non-standard parts of the syntax below, as they arise.

The operational semantics for DFLATE uses two judgments: one for sequential semantics and one for distributed semantics (see Section III-C). Sequential semantic judgment \( pl, D \vdash e \rightarrow e' \) indicates that at place \( pl \), under delegation sequence \( D \), expression \( e \) takes a small step to \( e' \). Figure 4 presents some of the inference rules for this judgment.\(^5\)

A delegation sequence is a sequence of delegations \( \langle p \succ q \rangle \), indicating that principal \( q \) has delegated its authority to principal \( p \). We assume that there is a well-known initial delegation sequence \( D_{init}. \) Expression assume \( \langle p \succ q \rangle \) in \( e \) adds delegation \( \langle p \succ q \rangle \) to the delegation sequence used to evaluate \( e \). This can be thought of as an annotation indicating that more information flows are permitted during the computation \( e \). However, note that the type system ensures that it is secure to add this delegation, i.e., that the delegation to add and the decision to add it have sufficiently high integrity. We use term \( e \) where \( v \) to record that delegation \( v \) holds for evaluation of \( e \). Rules DE-ASSUME and DE-WHERE show how these terms operate. (Runtime representation of the delegation sequence and where terms are needed only for proof purposes and do not need to be present in an implementation of DFLATE.)

The monadic unit term \( \eta_e e \) protects \( e \) at security level \( \ell \). This syntax is similar to that used by DCC [2] and FLAC [5], but the

\(^5\)All inference rules are given in Appendix A.
with subexpression $e$ that can be reduced. Evaluation contexts are standard and defined in Appendix A.

Rule PAR-STEP states that a distributed configuration takes a step whenever one of its processes takes a step. Note that the sequential evaluation of a process uses the initial delegation sequence $D_{init}$, although the process may use additional delegations via assume and where terms.

Processes communicate via channels. Channel endpoints are unidirectional: an endpoint can be used to send or receive values, but not both. Communication is synchronous: a send block until there is a matching receive, and a receive blocks until a message is available. Channels are not first-class and we ensure that a channel endpoint is used by at most one process. This restriction prevents certain races that are both difficult for programmers to reason about as well as potential covert channels. Indeed, even though the distributed semantics are non-deterministic, because of the careful management of channel endpoints, there can never be a race between two sends on the same channel or between two receives on the same channel. Thus our distributed semantics is confluent.

Rule PAR-SEND-RECV matches up a process that is sending on channel $v$ with a process that is receiving on $v$. For bookkeeping purposes in the proof, the value sent over the channel is a where term that includes all delegations in use by the sender. Rule DE-SEND ensures that delegations are included in the value before the communication occurs. We allow closures to be sent over channels, but the type system carefully ensures that the closure can not contain inappropriate channel endpoints nor can the closure contain TEE code.

Suppose node $n_1$ is sending a value $v$ over the channel $v$ and node $n_2$ is ready to receive it. Observe that the result of stepping $\text{send} \ ch \ v \ then \ e$ with rule PAR-SEND-RECV results in the expression $e;\square$ and $\text{recv} \ ch \ as \ x \ in \ e'$ becomes $e'[x \to v];\square$. The $;\square$ term is used to denote the end of the process: $e;\square$ reduces to an output value $v';\square$. While $v';\square$ is a valid expression, output values are not in the syntactic category of internal values $v$ in Figure 3. Our type system ensures that well-typed programs never use output values as internal values. This restriction merely ensures that the continuation of sending or receiving process is a subterm of the send or receive. It does not affect the expressiveness of the language, only the structure.

This design helps simplify type system constraints that track the influence of sent and received messages on control flow.

Term spawn @n (pl, ch1[pc1;τ1], ch2[pc2;τ2]).e1 then e2 spawns expression $e_1$ as a new process on node $n$ and continues as $e_2$ (rule PAR-SPAWN). Expressions $e_1$ and $e_2$ may refer to channel variables $ch_1$ and $ch_2$, which, when the process is spawned, will be replaced with fresh channels, enabling the parent and child processes to communicate. Channel type annotations $pc_1$, $\tau_1$, $pc_2$, and $\tau_2$ restrict how channels may be used. Place $pl$ indicates the intended endpoint of the newly created channel: if the newly spawned process is on the same host as the parent, then $pl$ may indicate a computation principal $t$ that is the intended endpoint. This enables a pattern where a parent process can spawn a child process that executes a TEE, and the parent process can communicate with the TEE.

IV. THREAT MODEL

The DFLATE type system statically enforces information-flow control policies on data processed by DFLATE programs. In order to understand various design choices in the type system, it is necessary to understand the attacker model.

We assume that some conjunction of principals (denoted $\mathcal{A}$ for “attacker”) are malicious. Since nodes are principals, this also permits us to express that nodes are compromised. Intuitively, the security guarantees that we will provide (Section VII) are based on the idea that “you can be hurt only by those you trust.” That is, if $\mathcal{A}$ is the malicious principal and $p$ is a “good principal” (i.e., a principal that doesn’t trust $\mathcal{A}$), then $\mathcal{A}$ cannot violate the security concerns of $p$. We assume all processes start execution with common trust assumptions, i.e., the initial delegation sequence $D_{init}$.

For confidentiality, we assume that a good principal provides confidential input to a program and that the attacker observes the output of the program (namely, the final value computed on a compromised node). For integrity, we assume that the attacker provides untrustworthy input to a program and that a good principal consumes the output of the program. We thus use an “input/output” observational model.

We also consider a stronger observational model for confidentiality, where the attacker is able to observe the execution trace on a compromised node. However, the attacker cannot observe the contents of a ciphertext for which it does not have a decryption key: the attacker cannot distinguish values $\eta_\ell \ v$ and $\eta_\ell \ v'$ (which represent values $v$ and $v'$ encrypted and signed by principal $\ell$) if the attacker does not have the authority to decrypt $\eta_\ell \ v$ and $\eta_\ell \ v'$. Similarly, the attacker cannot observe the contents of a TEE unless it has sufficient authority to access the keys of the corresponding computation principal.

We ignore covert channels, including timing, termination, memory accesses by TEEs, and speculative-execution channels. Orthogonal techniques (e.g., 44, 34, 28) can mitigate some of these concerns, and we expect some covert channels related to TEEs to be addressed in future TEE designs.

We assume that cryptographic mechanisms, TEE implementations, and the compiler and runtime system are correct. We assume that node-to-node communication is secure and unobserved by other principals, i.e., we do not consider network-level adversaries. Tools such as Tor [15] can be used to make it harder for network-level adversaries to observe the presence of node-to-node communication. We do, however, assume that communication with a TEE is observed by the host node. In DFLATE we use symbolic cryptography but do not treat keys as values in the language. We thus assume that an attacker has access to some set of signing and encryption keys based on trust relationships, but do not consider a Dolev-Yao-style attacker that can learn new keys from observations.

V. THE DFLATE SECURITY TYPE SYSTEM

DFLATE types (Figure 3) include unit, sums, products, functions, type functions, and type variables. (Functions and type functions have non-standard annotations that we describe below.) Delegation types are singleton types: each delegation type $(p \succ q)$ is inhabited by a single value $(p \succ q)$. Monad
type \( \ell \) says \( \tau \) protects an expression of type \( \tau \) at level \( \ell \); it is the type of values such as \( \eta_\ell \, v \) (where \( v \) has type \( \tau \)).

The grammar presented in Figure 3 splits types into two kinds: output types, which have the form \( \rhd \), and internal types of the form \( \hat{\tau} \). Output types represent expressions that cannot be consumed. For example, they cannot be passed as arguments to functions. Metavariable \( \tau \) is used when either an output or an internal type is permitted. The type system ensures that expressions with output types cannot be used as internal values. This distinction in types simplifies parts of the proof but does not limit the expressiveness of the language, just the structure of it.

Channels are not first-class values but do have types of the form \( \text{chan}_{pl_1 \leftarrow pl_2} \, pc \, \hat{\tau} \) and \( \text{chan}_{pl_1 \leftarrow pl_2} \, pc \, \tau \). These types specify channels that connect places \( pl_1 \) and \( pl_2 \) (either nodes or TEs) and may exchange values of type \( \hat{\tau} \) in contexts up to \( pc \). Recall that channels are uni-directional. The former type specifies a send channel, (indicated by the subscript \( pl_1 \leftarrow pl_2 \)) meaning \( pl_1 \) may use the channel to send values to \( pl_2 \), the latter specifies a receive channel (subscript \( pl_1 \leftarrow pl_2 \)) meaning \( pl_1 \) may use the channel to receive values from \( pl_2 \).

Well-typed programs do not always reduce to values, however. They may also become stuck waiting for communication: either a ready send with no corresponding recv or vice versa. To distinguish these stuck programs from those that the type system is designed to prevent, we refer to well-typed programs that become stuck waiting for communication as blocked programs.

Typing judgment \( \Pi; \Gamma; \Theta; \Delta \vdash e : \tau \) indicates that expression \( e \) has type \( \tau \). Delegation context \( \Pi \) contains a sequence of delegations that are valid just before executing \( e \). It is a conservative approximation of the delegation sequence \( D \) that is present at run time. The delegation context is extended by assume and where terms. Variable typing context \( \Gamma \) maps variables to types. Variables may have only internal types, so all mappings in \( \Gamma \) have the form \( x : \hat{\tau} \). Channel variable scope is maintained using the channel environment \( \Theta \). Principal \( pl \) indicates the place the term is typed at, either a node \( n \) or computation principal \( t \). Program counter level \( pc \) is an upper bound (in the information-flow ordering \( \sqsubseteq \) ) on the decision to execute \( e \), and also a lower bound on observable side-effects of \( e \).

The core DFLATE typing rules are presented in Figures 6 and 7 present some of the key DFLATE typing rules. Rules in Figure 6 are adapted from FLAC, and those in Figure 7 cover DFLATE’s distributed computation and TEE extensions. Premises of these rules are either typing judgments or judgments that specify required relationships between principals, or between principals and types.

Acts-for judgments have the form \( \Pi \vdash p \vdash q \) and require that \( p \) has at least as much authority as \( q \) in delegation context \( \Pi \). (Alternatively, that assuming the delegations in \( \Pi \), \( q \) trusts \( p \).) Recall that \( \sqsubseteq \) is defined in terms of \( \vdash \) so using the same rules we may also derive judgments of the form \( \Pi \vdash p \sqsubseteq q \). Intuitively, if \( \Pi \vdash p \sqsubseteq q \) information labeled with \( p \) can flow to information labeled \( q \), since (given the delegations in \( \Pi \) the confidentiality and integrity of \( q \) is at least as restrictive as that of \( p \).

Both of these judgments are simplified versions of the corresponding FLAC judgments [6], which we can use in DFLATE since delegations are reasoned about statically and thus do not provide an information channel. (FLAC could also benefit from this simplification.) Both of these forms are actually syntactic sugar for \( \Pi; \sqcap \vdash \top \vdash ; \vdash \top \vdash \vdash p \vdash q \) in the more general FLAC judgment form \( \Pi; pc; \xi \vdash p \vdash q \). The rules for deriving acts-for judgments, presented in Appendix A, are based on a fragment of the Flow-Limited Authorization Model logic [6]. Each delegation in the FLAM delegation context \( \Pi \) is labeled with a confidentiality and integrity policy. The logic establishes rules for how those delegations may be used to derive authorization judgments. The query label, \( pc \), protects the distributed authorization query by limiting communication to nodes that protect the confidentiality of \( pc \). The result label, \( \ell \), protects the confidentiality and integrity of the delegation context by limiting the delegations used to derive the judgment to those that flow to \( \ell \).

In DFLATE, authority is verified statically by the DFLATE compiler independently of any sensitive data. Therefore the query label, \( pc \), which protects the information in the acts-for query \( p \vdash q \), is considered public and trusted, or \( \sqcap \vdash \top \vdash \). Similarly, the result of that query is based only on trustworthy information the compiler itself has provided and processed, so the result is considered public and trusted too. The benefit of embedding DFLATE’s acts-for judgment in the FLAM logic is that we can rely on FLAM’s formal properties, which have been mechanically verified [7], in our proofs. In this technical report, we formalize this embedding rigorously, correcting some technical errors in the original FLAC [5] formalization.

Type protection judgments have the form \( \Pi \vdash e \vdash \hat{\ell} \leq \tau \), indicating that type \( \tau \) protects information labeled with \( \ell \). Intuitively, it means that the type system ensures that any information gained by using a value of type \( \tau \) will have a security level at least as restrictive as \( \ell \). The rules for deriving type protection judgments are based on a subset of FLAC’s rules. The primary rule is DP-LBL:

\[
\text{DP-LBL} \quad \Pi \vdash e \vdash \hat{\ell} \quad \Pi \vdash \ell \leq \hat{\ell}' \quad \Pi \vdash \ell \leq \hat{\ell}' \quad \text{says } \hat{\tau}
\]

This rule connects acts-for judgments to protected types. If \( \ell \) flows to \( \ell' \), then the type \( \ell' \) says \( \hat{\tau} \) protects level \( \ell \). Singleton types like unit and \( p \vdash q \) protect any level since the type itself encodes the value: observing the runtime value carries no information. However, the type \( \hat{\tau}_1 + \hat{\tau}_2 \) is not protected at any level since observing the value reveals the side of the sum the value is on, even if the sides have the same type. All protection rules are given in Figure 16 in Appendix A.

DFLATE’s type protection judgment is more restrictive than both FLAC’s and the protection rules in the Dependency Core Calculus [2, 1] (which FLAC’s are based on). The restrictiveness comes from the omission of three rules. One rule, DP-LBL1, permits a level to be protected by the inner type of a says type if the outer type does not protect it.

\[
\text{DP-LBL1} \quad \Pi \vdash \ell \leq \hat{\tau} \quad \Pi \vdash \ell \leq \hat{\ell}' \quad \text{says } \hat{\tau}
\]
This rule is not compatible with the cryptographic mechanisms DFLATE seeks to model: it makes nested says types commutative in the sense that \( p \) says \( q \) says \( \tau \) protects the same levels as \( q \) says \( p \) says \( \tau \). Commutativity undermines the expressiveness of integrity policies since a value of type \( \tau \) signed by \( q \) then \( p \) (and thus unmodified by \( p \)) cannot be statically distinguished from a value signed by \( p \) then \( q \) (and thus unmodified by \( q \)). It also complicates reasoning about confidentiality since encryption order is not reflected statically.

A value of type \( p \) says \( q \) says \( \tau \) may have been encrypted first with \( q \)’s public key then \( p \)’s, or first with \( p \)’s then \( q \)’s. Successfully decrypting the value requires distinguishing the correct order.

The other two rules we omit are DP-FUN and DP-TFUN, which state that abstractions protect a level if the pc and return type do.

\[
\begin{align*}
\text{[DP-FUN]} & \quad \Pi \vdash \ell \subseteq pc' \quad \Pi \vdash \ell \leq \tau_2 & \quad \Pi \vdash \ell \subseteq pc \quad \Pi \vdash \ell \leq \tau \\
\text{[DP-TFUN]} & \quad \Pi \vdash \ell \leq \tau_1 \quad \frac{pc', \Theta', P, \Pi}{\tau_2} \\
& \quad \Pi \vdash \ell \leq \forall X[pc', \Theta', P, \Pi], \tau
\end{align*}
\]

These rules are reasonable in a setting where an attacker cannot observe raw lambda values, and information can be gained from a function value only by applying it. In a distributed setting, however, functions can be sent over channels to potentially malicious hosts, who can directly examine the encoding of an abstraction and potentially learn information.

Every DFLATE typing rule contains a clearance premise \( \Pi \vdash pl \gg pc \) that requires place \( pl \) to act for \( pc \). This ensures a place cannot observe or use data exceeding its authority, as discussed in Section II-B.

For function type \( \tau \sim pc, \Theta, P, \Pi \), level \( pc \) is the latent effect of the function (i.e., a lower-bound on the observable side-effects when the function is invoked), \( \Theta \) is the channel environment the function expects, \( P \) is the delegation context the function expects, and \( P \) are the places at which the function make be invoked. Rule DT-LAM shows that the function body must be well-typed for the function’s \( pc \) and channel environment, for every place \( pl \in P \). Function application (rule DT-APP) may occur only if the \( pc \) at call site flows to the latent effect of the function, the call-site place is in \( P \), and the channel environment and delegation context of the caller is compatible with the function’s channel environment and delegation context. Note that any place can receive a lambda expression but only those within \( P \) are allowed to invoke it. Channels are not first class, and so the channel environment requirement ensures that the caller has the appropriate channels available and channel variables do not escape via closures. (Type abstraction and application is similar.)

Expression \( \eta e \) will evaluate \( e \) and then protect the result at level \( \ell \) (in implementation, by signing and encrypting it). It has type \( \ell \) says \( \tau \) (rule DT-UNITM) provided that \( e \) is well-typed and the program counter level \( pc \) flows to \( \ell \). Intuitively, this premise is required because program counter level \( pc \) is an upper bound on the decision to execute the statement and on the information available in this computational context (e.g., through variables). Thus, the result of \( e \) might be influenced by information at level \( pc \) and must be protected appropriately.

Clearance (\( \Pi \vdash pl \gg pc \)) ensures that place \( pl \) has appropriate integrity to sign the value. Suppose Alice wants to protect a value at Bob’s integrity by evaluating \( \eta_{\text{Bob}} v \). To type check, it must be the case that \( \Pi \vdash pc \subseteq \text{Bob} \). By clearance we have \( \Pi \vdash \text{Alice} \gg pc \), and thus \( \Pi \vdash \text{Alice} \gg \text{Bob}^{\tau} \), indicating Alice has access to Bob’s signing key. Note that a principal can create protected values that are more confidential than its clearance, e.g., Alice can encrypt values using Bob’s public encryption key without having access to Bob’s decryption key.

Rule DT-SEALED permits protected values to be on nodes that would not have the authority to create them. For instance, even if Alice does not trust Bob, sealed value \( \eta_{\text{Alice}} v \) is well-typed at Bob if \( v \) is well-typed. Sealed values reflect the security guarantees of cryptographic protection mechanisms: attackers cannot distinguish ciphertexts or forge signatures.

In \( \text{bind } x = e \in e' \), expression \( e \) evaluates to a protected value \( \eta_{\text{Alice}} v \), and \( x \) is bound to \( v \) in \( e' \). Rule DT-BINDM requires that the type of \( e' \) must protect \( pc \sqcup \ell \) and \( e' \) must type check at a more restrictive level \( pc \sqcup \ell \). Clearance (for expression \( e' \)) ensures that \( pc \sqcup \ell \) does not exceed the place’s authority: the place is trusted to compute on data protected at level \( \ell \). For example, if Bob evaluates \( \text{bind } x = \eta_{\text{Alice}} v \in e' \), for \( e' \) to type check it must be the case that \( \Pi \vdash \text{Bob} \gg pc \sqcup \text{Alice} \).

It follows that \( \Pi \vdash \text{Bob} \gg \text{Alice} \rightarrow \), indicating Bob has access to Alice’s decryption key.

DT-ASSUME ensures that when the delegation context is extended, there is sufficient integrity to do so. Specifically, when \( r \) delegates to \( q \), \( r \)’s security concerns may be compromised, so we require that \( pc \) acts for \( \nabla (r) \), the voice of \( r \). Premise \( \Pi \vdash \nabla (q^{-}) \gg \nabla (r^{-}) \) ensures robustness of the delegation, a desirable property from FLAM [6] that we also enforce.

Figure 7 shows the distributed and TEE typing rules. Rule DT-SPAWN limits the channel environment of newly spawned computations to the new channels created by the parent. Only place \( pl \) has access to the send endpoint of \( ch_1 \) and the receive endpoint of \( ch_2 \). Conversely, the newly created process on node \( n \) can use the receive endpoint of \( ch_1 \) and the receive endpoint of \( ch_2 \). Spawned processes \( e \) inherit the delegation context from the parent, but not the variable context. The program counter level of the spawned process \( e_n \), \( pc' \), is at least as restrictive as the \( pc \) of the parent process. This ensures that \( e_n \) does not inadvertently reveal that it was spawned. Premise \( \Pi \vdash pc' \leq \ell' \) ensures the output of the process protects the output of the spawning process. Thus if \( pc' \) is confidential (or untrusted) the output can only be used in confidential (or untrusted) contexts.

DT-SEND requires that channel \( ch \) is the send endpoint and that the expression has the correct type. The channel program counter level \( pc_{ch} \) is an upper bound on the confidentiality and integrity of the decision to send the message. This is distinct from the policy used to restrict what information can be sent in messages, which is expressed via type \( \tau \) (see Section II-A for discussion). After the message is sent, execution proceeds with \( e' \) which must type check at a program counter level that is at least as restrictive as \( pc_{ch} \). This ensures that information revealed by successfully sending a message is protected appropriately. In addition to the usual clearance premise, DT-SEND also has a channel clearance premise \( \Pi \vdash pl \gg pc_{ch} \) that
[DT-LAM]
\begin{align*}
&\forall pl \in P. \Pi', \Gamma; x: \tau_1; \Theta': pl'; pl \vdash e: \tau_2 \\
&\Pi; \Gamma; pl; pc \vdash l(x: \tau_1); \Theta', \Pi, \Pi'; e: \tau_1 \\
&\frac{pl' \in P, \Pi', \Gamma; x: \tau_1; \Theta': pl'; pl \vdash e: \tau_2}{\Pi; \Gamma; pl; pc \vdash \lambda(x: \tau_1)(pl' \rightarrow \Theta', \Pi, \Pi'); e: \tau_1}
\end{align*}

[DT-APP]
\begin{align*}
&\forall (p \geq q) \in \Pi'. \Pi \vdash p \geq q \\
&\Pi'; \Gamma; \Theta; pl; pc \vdash e: \tau_1 \quad pl \in P \\
&\Pi; \Gamma; pl; pc \vdash e': \tau_2 \\
&\Pi; \Gamma; pl; pc \vdash e: \tau_2
\end{align*}

[DT-IND]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash e: \tau_1 \\
&\Pi; \Gamma; pl; pc \vdash e': \tau_2 \\
&\Pi; \Gamma; pl; pc \vdash e: \tau_2
\end{align*}

[DT-UNIT]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash e: \tau \\
&\Pi; \Gamma; pl; pc \vdash \eta: \ell \quad e \text{ says } \ell \\
&\Pi; \Gamma; pl; pc \vdash \pi: \eta \quad e \text{ says } \ell
\end{align*}

[DT-ASSUME]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash e: (q \geq r) \\
&\Pi; \Gamma; pl; pc \vdash e': (q \geq r) \\
&\Pi; \Gamma; pl; pc \vdash e': (q \geq r)
\end{align*}

[DT-TEE]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash e: \tau \\
&\Pi; \Gamma; pl; pc \vdash e': \tau \\
&\Pi; \Gamma; pl; pc \vdash \eta; \ell
\end{align*}

[DT-SPAWN]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash \text{spawn } n (pl', ch_1, \tau_1), (pl', ch_2, \tau_2); e_n \text{ then } e: \tau \\
&\Pi; \Gamma; pl; pc \vdash \text{spawn } n (pl', ch_1, \tau_1), (pl', ch_2, \tau_2); e_n \text{ then } e: \tau
\end{align*}

[DT-SEND]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash e: \tau' \\
&\Pi; \Gamma; pl; pc \vdash e': \tau' \\
&\Pi; \Gamma; pl; pc \vdash send e \text{ then } e': \tau'
\end{align*}

[DT-RECEIVE]
\begin{align*}
&\Pi; \Gamma; pl; pc \vdash \text{recv } ch \text{ as } x \text{ in } e: \tau'
\end{align*}

Fig. 6: Core sequential typing rules

Fig. 7: Core distributed and TEE typing rules

ensures $p$ has sufficient authority to use the channel. Finally, notice that whether or not $e'$ has an output type $\tau'$, the send term has an output type, ensuring that the continuation-passing discipline is maintained. Rule DT-RECEIVE is similar to DT-SEND. Here, the channel direction is reversed, and the received value is mapped to variable $x$ in the continuation. The type of channel messages $\tau$ and the channel program counter level $pc_{ch}$ allow the sender and receive to co-ordinate on the security and contents of messages sent over the channel.

Expression $\text{TEE} e$ executes $e$ in a TEE. Rule DT-TEE requires $e$ to be closed (and so it cannot use variables to access data from the host node). The channel environment for the TEE is limited to endpoints for the TEE with a channel $pc$ that protects $pc$. This restriction ensures two properties. First, all messages into and out of a TEE pass through the TEE’s host, which reflects the operation of current TEE implementations. Second, the restriction to only channels with suitable channel $pc$’s ensures that any sends and receives the TEE perform also protect the $pc$ that launched the TEE. Without this second property, hosts could use TEEs as covert channels to send messages from restrictive contexts to less restrictive ones.

Expression $e$ executes with the integrity of $t$ and confidentiality $pc^{-}$. Rule DT-TEE differs from all other typing rules in that there is no relation between the integrity of the program counter where the TEE is executed $(pc^{-})$ and the integrity of the program counter within the TEE $(t^{-})$: this is a form of endorsement. Computation principal $t$ is unique for a given expression $e$ and the implementation of DFLATE can use remote attestation to ensure that the TEE is executing $e$, even if the host is untrusted. The typing rule reflects this guarantee by type checking $e$ at an integrity level unique to that expression. This ensures that the code $e$ is not altered (e.g., by malware) before the execution. Thus, principals that delegate trust to $t$ will consider the TEE trusted, but the TEE gains no additional authority over principals that do not delegate to $t$. The confidentiality level of the information revealed by the TEE is at least that of the host and thus expression $e$ is type checked with confidentiality $pc^{-}$.

A. Examples revisited

Figure 8 presents DFLATE code for the three scenarios in Figure 2. Each program applies function $f$ to a protected value from Alice (principal $a$) and outputs the result to Carol ($c$) using Bob ($b$) as an intermediate, protecting the output at level $a \cap c$, implying Alice and Carol can read it and both trust its integrity. Despite similar functionality, each program requires different trust relationships between Alice, Bob, and Carol.

Figure 8(a) is an implementation of Figure 2(a), where no cryptographic mechanisms are used. For this program to type check under some delegation context $\Pi$, it must be the case that Alice trusts Bob and Carol completely. That is, $\Pi \vdash b \geq a$ and $\Pi \vdash c \geq a$. Furthermore, Carol must trust Alice and Bob with her integrity, $\Pi \vdash a^c \geq c^c$ and $\Pi \vdash b^c \geq c^c$. To see why, first consider the $\text{send}$ in line 7. For this term to type check, it must be the case that $\Pi \vdash a \subseteq a \cup b \cup c$ since, by DT-BINDM, the $pc$ at this point is at least as restrictive as
needs to trust Bob’s integrity because of his power to suppress
and Bob does not learn the contents of this message. In other
in Figure 2(b), Alice signs and encrypts her message to Carol,
the continuity is at least
For this to hold, it must be the case that
requirement that Alice trust Bob with her secrets. When Carol
chosen. If the channel \(pc\) was \(a\) instead of \(a\), the clearance
implied by Figure 8(a).
Figure 8(c) is an implementation of Figure 8(b). Recall that
Bob can influence the incoming and outgoing messages to the
enclave \(E\) but the recipients (of messages from enclave) can
detect the modifications. Thus it suffices for Alice and Carol
to trust the enclave \(E\). The DFLATE program in Figure 8(c)
uses a TEE to further reduce the need for mutual trust among
Alice, Bob, and Carol. By running the computation in a TEE
(identified as \(t\)) hosted on potentially untrusted Bob’s node \(b\)
(lines 5 and 6), Alice no longer needs to delegate integrity to
Carol since Carol has no influence on the computation. Since
the enclave protects the result \((x)\) at line 9 on behalf of Alice
and Carol, they must each delegate integrity to \(t\) (Alice’s
integrity is also required for the assume at line 7).

After receiving \(y\) on channel \(ch_a\), the pc at line 9 is
\(a\), reflecting Bob’s influence relaying messages. In order to
bind \(y\) to \(x\), the enclave must have clearance to
read \(a\), so Alice must delegate her confidentiality to \(t\).
Since Bob is unable to modify Alice’s message, the enclave
endorses Bob’s influence by assuming \(b\) and allows the
result of \((x)\) to flow to Carol by assuming \(c\). These
assumptions allow the body of the bind to type check.
The below judgments show the assumptions needed inside the
TEE, \((\Pi_{tee})\) is the TEE’s delegation context at lines 8 and 9.

Introducing temporary delegations in a TEE using assume is
preferable to the delegation contexts required by 8(a) or 8(b)
since they are enabled only for the scope of the TEE, and
the TEE guarantees that the code is executed as-is.

6Note this would still be the case if a different channel \(pc\) for \(ch_b\) were
chosen. If the channel \(pc\) was \(a\) instead of \(a\), the clearance
requirement for the \(recv\) at line 5 would require \(\Pi \vdash b \Rightarrow a\) and \(a\), which implies \(\Pi \vdash b \Rightarrow a\).

7The absorption laws \((a \land b) \land a = a\) and \((a \land b) \lor a = a\) for all lattice
elements \(a\) and \(b\) are algebraic properties of all lattices.

Fig. 8: DFLATE code
VI. IMPLEMENTATION CONSIDERATIONS

In this section we discuss how DFLATE can be realized using existing cryptographic techniques and TEE mechanisms. Specifically, we identify security principals with public keys, and rely on a public key infrastructure to distribute private keys to appropriately authorized nodes. Our TEE abstraction is carefully designed to be implementable using Intel’s SGX and similar mechanisms; we describe how the remote attestation mechanism can be used by the DFLATE runtime to authenticate TEEs and provision the TEE with appropriate private keys.

A. Cryptography and Representation of Principals

We require that every primitive principal \( n \in N \) (which includes nodes and computation principals) is associated with a public/private key pair where the public key can be used for encryption and verifying signatures, and the private key can be used for decryption and signing.\(^8\) There are many possible cryptographic schemes that can be used, and we do not require any specific one. We do, however, require infrastructure to store and distribute keys, which we discuss below.

A conjunction or disjunction of principals is represented by a distinct key pair. That is, the cryptographic scheme does not need to support group encryption, group signatures, etc. Instead, our key infrastructure will provide appropriate access control for private keys of conjuncts and disjuncts of principals such as Alice ∨ Bob and Alice ∧ Bob ∨ Carol.

Computation principals \( t \in T \) are identified by a secure hash of (the bytecode representation of) the corresponding computation, and are associated with a key pair. We describe below how a TEE executing the code corresponding to computation principal \( t \) is provisioned with the key pair for \( t \).

Value \( \tau_p v \) represents value \( v \) encrypted and signed by \( p \).\(^9\) The value \( \tau_p v \) is implemented by first encrypting \( v \) (using the appropriate key for \( p^{-} \)), and then signing the result (using the appropriate key for \( p^{-} \)). Conversely, evaluation of \( \text{bind } x = \tau_p v \in e \) verifies then decrypts. We ensure that places (i.e., nodes and TEEs) that need to perform decryption and signing have access to the appropriate keys; see below.

Delegations \( (p 
triangledown q) \) are run-time values, and are implemented as a statement “\( q \) delegates to \( p \)” that is signed appropriately (i.e., signed by the principal \( \triangledown(q) \)).

We assume that node-to-node communication is secure and unobservable, which can be achieved using tools such as Tor [16] or Riffle [27].

B. TEE Implementation

Intel’s SGX is the most widely deployed TEE mechanism, although other TEE implementations exist (e.g., Sanctum [14]). Modulo security vulnerabilities\(^10\) and the need to trust Intel, SGX is suitable for implementing DFLATE’s TEE abstraction.

\(^8\)The public key will likely be a tuple of an encryption key and a verification key, and similarly for the private key.

\(^9\)By contrast, term \( \tau_p e \) will evaluate \( e \) and then encrypt and sign the result.

\(^10\)Recent security vulnerabilities discovered in SGX [40] appear to be implementation issues rather than fundamental concerns.

To start executing TEE\(^4\) \( e \), first an SGX enclave is created with the DFLATE runtime. SGX’s remote attestation mechanism can be used to prove that the enclave is running the DFLATE runtime. Once a remote party knows it is communicating with an instantiation of the DFLATE runtime, the DFLATE runtime can state that it is executing computation \( e \) whose hash is \( t \).

C. DFLATE Runtime

The DFLATE runtime system is responsible for executing DFLATE code, establishing communication channels between nodes, dynamically type-checking values (especially closures) that are received over channels, interacting with the SGX mechanisms and our key management infrastructure, and other tasks required to support execution of DFLATE programs.

The DFLATE runtime needs to be able to execute inside an SGX enclave. Current SGX SDK support is limited to C and C++, so the DFLATE runtime would be most easily implemented in C or C++. However, DFLATE code is represented as bytecode that is executed by the DFLATE runtime. This is necessitated by the ability to send closures over channels, but also simplifies our use of SGX remote attestation protocols.

D. Key Distribution

Computation at a place will need to encrypt, decrypt, sign, and verify data. While encryption and signature verification use the public part of a key pair, decryption and signing require that the place possess appropriate private keys. Fortunately, the clearance premises in the typing rules ensure that a well-typed program at place \( pl \) will need to perform decryptions and signatures only for principals \( p \) such that \( \Pi_{init} \vdash pl \triangledown (p) \) (where \( \Pi_{init} \) is the initial delegation context and \( \triangledown(p) \) is the authority required to act on behalf of \( p \)).

When an enclave is created for computation principal \( t \), the enclave does not initially have the private key for \( t \), nor for any other principals \( p \) such that \( \Pi_{init} \vdash t \triangledown (p) \). Thus the enclave must be provisioned with appropriate keys at run time.

To address this, we require a global key master component that can store key pairs and allow nodes and enclaves to acquire the private keys that they are authorized to have. Moreover, the key master creates key pairs as needed for conjuncts and disjuncts of principals. The key master can be implemented as a distributed service to reduce trust in any single entity.

Node \( n \) can request the private key for principal \( p \) from the key master by proving that it is \( n \) (i.e., that it possesses the private key for \( n \)), whereupon the key master will check that \( \Pi_{init} \vdash n \triangledown (p) \), and, if so, securely send \( n \) the private key for \( p \). Since we can conservatively approximate the principals occurring in a computation, node \( n \) could be provisioned with the appropriate private keys before execution, or the implementation could allow \( n \) to request private keys lazily during execution.\(^11\)

However, for a computation principal \( t \), the provisioning of private keys is slightly different, and must be performed at run time. First, the key master and the SGX enclave engage in a

\(^11\)Care must be taken to ensure that the decision to communicate with the key master does not reveal confidential information.
remote attestation protocol. Once the key master has proof that the enclave is running the DFLATE runtime, it establishes a secure channel with the enclave. The DFLATE runtime then informs the key master it is executing the code corresponding to computation principal \( t \), and requests private keys. Notably, this is the only place that the SGX remote attestation protocol is needed in our proposed DFLATE implementation: secure communication between a node and a TEE can be established using keys for DFLATE principals. Each enclave needs to run the remote attestation protocol only once, with the key master, in order to acquire keys for DFLATE principals.

VII. SECURITY GUARANTEE

DFLATE’s type system enforces information-flow policies expressed using the FLAM principal algebra, and thus enjoys noninterference-based security guarantees. DFLATE permits weakening, or downgrading, of policies.\(^{12}\) Dowgrading occurs by adding delegations (via assume terms) and by TEE execution (via endorsement of the TEE’s program counter level).

However, downgrading in DFLATE is carefully controlled and restricted: well-typed assume terms can only execute in contexts with sufficient integrity, and endorsement of TEEs reflect measurement and verification of code executing in a TEE. We thus expect that well-typed DFLATE programs satisfy a variety of expressive noninterference-based security guarantees, based on controlled downgrading (e.g., \([12, 25, 8, 9, 13, 3]\)), suitably adapted to be consistent with our threat model IV.

To demonstrate that DFLATE does indeed enjoy noninterference-based properties, we state and prove two variants of noninterference. The first (Theorem 1) uses a “batch-job” model, and holds for confidentiality and integrity. In a batch-job model, inputs are provided at the beginning of execution, and outputs are provided if and when the program terminates \([31]\). For our purposes, we regard the input as being data on one node (thus modeling that node possessing confidential information, or that node containing untrustworthy data) and the output as the final result on a specific node. Even though the execution of a DFLATE program involves nodes interacting with each other over channels, the batch model ensures that ultimate result of the program is appropriately secure.

The second (Theorem 2) uses a stronger observational model where an attacker observes the internal state of a compromised node, but holds only for confidentiality. It does not hold for integrity, due to asymmetry in security guarantees inherent in distributed decentralized applications that use TEEs.

A. Batch-Job Noninterference

We state noninterference with respect to a security level \( H \). Intuitively, for confidentiality, inputs labeled \( H \) (or a more restrictive security level) are regarded as confidential inputs, and we are concerned with ensuring that no information about them is revealed in outputs observable by an attacker, i.e., an entity that can observe outputs at level \( \ell \) where it is not the case that \( H \) can flow to \( \ell \). For integrity, inputs labeled \( H \) are regarded as low-integrity and we want to ensure they do not influence high-integrity outputs (i.e., outputs at level \( \ell \)).

Since we are stating noninterference, we are concerned only with executions where there is no downgrading from \( H \) (or above) to \( \ell \). However, we do not want to rule out all downgrading, as delegations and TEEs are central to DFLATE’s expressiveness. Instead, we assume that for a given process \( \langle n_k, e_k \rangle \) in a well-typed distributed configuration \( D \), we have a delegation approximation \( \hat{\Pi}_k^\tau \) that over-approximates delegations that the process may make during execution.\(^{13}\) See Appendix B for a formal definition.

Suppose all processes in \( D = \langle n_1, e_1 \rangle \parallel \cdots \parallel \langle n_m, e_m \rangle \) are well-typed, and the \( i \)th process takes an input value protected by \( H \) (i.e., it has a free variable of type \( H \) says \( \tau \)) and the \( j \)th process produces a value of type \( \ell \) says bool\( \Box \). Moreover, suppose the delegation approximations for the processes ensure that they never downgrade from \( H \) (i.e., \( \hat{\Pi}_k^\tau \) permits the same flows from \( H \) as \( \Pi_{init} \)). If we have two executions of \( D \) where the input to the \( i \)th process is replaced with different values, then the result of the \( j \)th process will be the same. We state this formally. Proofs of theorems are in Appendix C.

**Theorem 1** (Batch-Job Noninterference). Let \( H \) and \( \ell \) be security levels such that \( \Pi_{init} \not\models H \sqsubseteq \ell \). Let \( D = \langle n_1, e_1 \rangle \parallel \cdots \parallel \langle n_m, e_m \rangle \) such that for all \( k \in 1..m \) we have

\[
\Pi_{init}; \Gamma_k; \Theta_k; n_k; pc_k \vdash e_k : \tau_k,
\]

where \( \tau_j = \ell \) says bool\( \Box \) and \( x : H \) says \( \tau \in \Gamma_i \).

Assume that no process downgrades from \( H \), i.e., \( \forall k \in 1..m \). \( \forall \ell' \). \( \Pi_{init} \models H \sqsubseteq \ell' \iff \hat{\Pi}_k^\tau \models H \sqsubseteq \ell' \). For all \( v_1 \) and \( v_2 \), and all \( z \in \{1, 2\} \) such that

\[
\Pi; \Gamma_i; \Theta_i; n_i; PC_i \vdash v_z : H \text{ says } \tau,
\]

let \( D_z = \langle n_1, e_1 \rangle \parallel \cdots \parallel \langle n_i, e_i(v_z/x) \rangle \parallel \cdots \parallel \langle n_m, e_m \rangle \).

If \( D_1 \Rightarrow^* \langle n_1, e_1' \rangle \parallel \cdots \parallel \langle n_i, v_j' \rangle \parallel \cdots \parallel \langle n_m, e_m' \rangle \) and \( D_2 \Rightarrow^* \langle n_1, e_1'' \rangle \parallel \cdots \parallel \langle n_j, v_j'' \rangle \parallel \cdots \parallel \langle n_m, e_m'' \rangle \) then \( v_j' = v_j'' \).

**B. Noninterference for Stronger Observational Model**

We also prove a stronger confidentiality noninterference result for an attacker that is able to observe the execution of a process at a compromised node. Intuitively, the attacker sees the sequence of expressions (with stuttering removed, since we ignore timing channels) but cannot see the contents of protected values or TEEs for which the node does not have the decryption key. Recall (from Section VI) that node \( n \) has access to keys for all principals \( p \) such that \( \Pi_{init} \models n \gg \nabla(p) \). Thus, the attacker cannot observe the contents of protected value \( \pi_p \) if \( \Pi_{init} \not\models n \gg \nabla(\pi_p) \), nor see the contents of a TEE for computation principal \( t \) if \( \Pi_{init} \not\models n \gg \nabla(t) \).

The formal definitions of the process trace of node \( n \) and equivalence of process traces are provided in Appendix B. We say that two process traces are equivalent to node \( n \) if \( n \) is unable to distinguish them.

\(^{12}\) Weakening confidentiality is called declassification \([37]\); weakening integrity is called endorsement \([11]\).

\(^{13}\) A straightforward static analysis can be used to compute delegation approximations, but any over-approximation suffices for the security condition.
Our stronger noninterference result is similar to the result of Theorem 1, but holds only for confidentiality.

**Theorem 2** (Compromised-node Noninterference). Let $H^-$ and $n_1$ be security levels such that $\Pi_{\text{init}} \nvdash n_j \supseteq H^-$. Let $D = \langle n_1, e_1 \rangle \cdots \langle n_m, e_m \rangle$ such that for all $k \in 1..m$ we have

$$\Pi_{\text{init}}; \Gamma_k; \Theta_k; n_k; pc_k \vdash e_k : n_k,$$

where $x : H^- \text{ says } \tau \in \Gamma_i$.

Assume that no process downgrades from $H^-$, i.e., $\forall k \in 1..m, \forall \ell. \Pi_{\text{init}} \nvdash H^- \subseteq \ell \equiv \Pi_{\text{conf}} \nvdash H^- \subseteq \ell$. For all $v_1$ and $v_2$, and all $z \in \{1, 2\}$ such that

$$\Pi; \Gamma_i; \Theta_i; n_i; pc_i \vdash v_z : H^- \text{ says } \tau,$$

let $D_z = \langle n_i, e_1 \rangle \cdots \langle n_i, e_i(v_z/x) \rangle \cdots \langle n_m, e_m \rangle$. Then for all executions $D_1 \rightarrow \cdots \rightarrow D'_1$ and $D_2 \rightarrow \cdots \rightarrow D'_2$ the process traces of node $n$ are equivalent.

An equivalent of Theorem 2 does not hold for integrity. To see why, consider the following well-typed program that is running on node $n_2$ and outputs $\eta_2 \text{ true}$ for all inputs $x$.

$$\lambda_{\text{true}} (\text{bind } u = x \text{ in } (\lambda_{\text{unit}} u))$$

where

$$\lambda_{\text{true}} = \lambda (\underline{\cdot})[\ell, \emptyset, P, \Pi_\lambda]. \eta_2 \text{ true}$$

and

$$\lambda_{\text{unit}} = \lambda (\underline{\cdot}: \text{bool})[\ell, \emptyset, P, \Pi_\lambda]. ()$$

Let the input $x$ be controlled by an adversary (say $H^-$). Then a high integrity observer (say $\ell^-$) can observe the following traces when given the inputs $H^- \text{ says } \tau$ and $H^- \text{ says } \text{false}$.

Trace on $S_1$

1. $(n_j, \lambda_{\text{true}} (\lambda_{\text{unit}} \text{ true}))$
2. $(n_j, \lambda_{\text{true}} (\lambda_{\text{unit}} \text{ false}))$
3. $(n_j, \eta_2 \text{ true})$

Trace on $S_2$

$$\begin{cases}
\langle n_1, \text{inj}_1(z), \text{send } ch () \text{ then } () \rangle & \text{case of } u \\
\langle n_1, \text{inj}_2(z), \text{recv } ch' \text{ as } y \text{ in } y \rangle & \text{in } v
\end{cases}$$

On the left, the attacker node binds low-integrity input $x$ (of type $H^- \text{ says } \text{bool}$) to $u$ and branches on the value, sending on channel $ch$ in one branch and receiving on channel $ch'$ in the other. On the right, a high-integrity node is engaged in communication with $n_1$ and terminates after receiving a message. For inputs $x = \eta_2 \text{ true}$, process $n_2$ terminates, but for $x = \eta_2 \text{ false}$ it blocks, thus distinguishing the executions.

The weaker integrity result validates our goal of faithfully expressing the power of attackers to suppress messages without eclipsing the guarantees provided by the cryptographic mechanisms and TEEs. DFLATE cannot protect against the suppression of high-integrity messages, but for all programs that result in high-integrity messages, Theorem 1 guarantees their contents have not been influenced by an attacker.

### VIII. Related Work

#### A. Enclaves and Information Flow

Gollamudi and Chong [25] use enclaves to enforce information flow policies against low-level attackers that can inject arbitrary code into non-enclave parts of a program. DFLATE uses enclaves to enforce confidentiality and integrity against low-level attackers in a distributed setting. Our current noninterference results model passive attackers; we leave more powerful attacker models for future work.

In CFLOW, Fournet et al. [20] compile a sequential imperative program into a distributed program, preserving its security properties using cryptographic techniques. A straightforward security type system enforces noninterference. Fournet and Planul [18] extend CFLOW to use Trusted Platform Modules (TPM) and remote attestation to minimize the TCB while preserving noninterference. DFLATE programs are explicitly distributed at the source level via spawn, send, and recv terms. CFLOW’s communication channels are always public and untrusted whereas DFLATE channels specify separate policies for the presence of a message and its contents. CFLOW’s TPM is trusted and has a fixed integrity level, but TEEs in DFLATE have distinct integrity and confidentiality levels, allowing TEEs to be trusted than their host.

Subramanyan et al. [39] provide a formal foundation for the remote execution of enclaves and use it to prove that two remote enclave executions emit observationally equivalent traces if the attacker provides the same inputs in both executions. DFLATE uses the high-level guarantees of TEEs and proves end-to-end semantic guarantees (noninterference) of distributed applications using enclaves.

#### B. Communication Channels and Cryptography

Zdancewic et al. [42] securely partition a program into subprograms that communicate to simulate the original program. The resulting distributed program prevents read channels, which leak information when a remote read request occurs for a secret reason. DFLATE’s channel pc annotations protect against similar leaks.

Fournet and Rezk [19] use a security type system to enforce the correct use of cryptographic primitives for controlled downgrading. Compiling DFLATE to this language would ensure DFLATE’s monadic abstractions are implemented securely.

Wysteria [36] is a language for writing secure multiparty computation protocols. Wires in Wysteria express the idea of data ownership and are comparable to the monadic unit types in DFLATE. However, because Wysteria models communication implicitly through variable binding, it does not detect insecure flows that arise due to explicit communication.

Gazeau et al. [21] enforce confidentiality (but not integrity) of the client data in the cloud. Like our assumptions regarding access to cryptographic keys, their security guarantee relies on honest nodes denying access to attacker nodes.

Fabric [29] and DStar [43] use static and dynamic mechanisms to enforce IFC for distributed programs. They use cryptographic protocols to establish secure channels between nodes, but unlike DFLATE, do not allow high-integrity or secret data to flow through untrusted hosts.
Our channel design is similar to Rafnsson et al. [35], who also distinguish the presence of a message from the contents of a message. DFLATE channel policies are decentralized in that the security of a channel is relative to each principal rather than a centralized security lattice.

IX. CONCLUSION

DFLATE offers high-level security abstractions for decentralized, distributed applications that use cryptography and trusted execution environments. These abstractions accurately reflect the strengths and limitations of these mechanisms without exposing low-level implementation details. DFLATE is suitable for formal analysis of decentralized distributed applications and as a core programming model for a general-purpose secure distributed programming language. We have formalized DFLATE's semantics and shown that the type system enforces two variants of noninterference: the stronger variant holds only for confidentiality, reflecting the asymmetry in the security guarantees of the underlying mechanisms.

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REFERENCES


\( n \in \mathcal{N} \) (primitive principals) | \( t \in \mathcal{T} \) (enclaves)
\( x \in \mathcal{V} \) (variables)
\( \nu \in \mathcal{C} \) (channel values)
\( p, \ell, pc \ ::= n \mid T \mid \bot \mid p \cdot \mid p \cdot \cdot \mid p \land p \mid p \lor p \mid t \)
\( \tau ::= \hat{\top} \mid \hat{\bot} \)
\( \hat{\top} ::= p > p \mid \text{unit} \mid \hat{\top} + \hat{\top} \mid \hat{\top} \times \hat{\top} \)
\( e ::= \hat{\top} \mid \hat{\bot} \mid \hat{\top} \cdot \mid \hat{\top} \cdot \cdot \mid \hat{\top} \land \hat{\top} \mid \hat{\top} \lor \hat{\top} \mid T \mid v \mid e \cdot e \mid (e, e) \mid \eta \mid e \mid e^\hat{\top} \mid \text{proj}_1 e \mid \text{proj}_2 e \mid \text{send} ch v \mid \text{runTEE}\ 's \ s \mid \text{recv} ch \mid \text{spawn} \ (v, \nu, v) \mid \text{case} c \mid \text{assume} \ e \mid \text{bind} x = e \mid \text{assume} s \mid \text{case} c \ | \text{assume} s \mid \text{send} ch s \mid \text{recv} ch \mid \text{runTEE}\ 's \ E \)

Fig. 9: Complete DFLATE syntax

\[ E ::= [\cdot] \mid E \cdot e \cdot v \cdot E \mid (E, e) \mid (e, E) \mid \text{proj}_1 E \mid \text{proj}_2 E \mid \eta \mid E \mid \text{bind} x = E \cdot e \mid E \tau \mid \text{assume} E \cdot e \mid \text{send} v \cdot E \mid \text{case} c \mid \text{assume} E \cdot e \mid \text{send} v \cdot E \mid \text{case} c \mid \text{assume} E \cdot e \mid \text{send} v \cdot E \mid E \cdot e \mid E \cdot e \cdot E \mid E \cdot e \cdot E \mid E \cdot e \cdot E \mid E \cdot e \cdot E \]

Fig. 10: Evaluation Contexts (E). Define \( T[e] \) as substituting hole (in \( T \)) with expression \( e \).

APPENDIX A

We present the full syntax (Figure 9), evaluation contexts (Figure 10), evaluation rules (Figures 11, 12, 13, 14), robust assumptions (Figure 15), type protection rules (Figure 16), typing rules (Figures 17, 18, 19).

APPENDIX B

Definition B.1 (Delegation Approximation Contains All Delegations). Let \( \mathcal{S} \) be the well-typed distributed system

\[ \Pi_{init}; \Gamma; \Theta; p_1; p_c \vdash e_1: \tau_1 \mid \cdots \mid \Pi_{init}; \Gamma_m; \Theta_m; p_m; p_c \vdash e_m: \tau_m \]

Then the\( \Pi_D \) of the process \( \langle p_1, e_1 \rangle \) is the union of the set of all delegations in the initial delegation

Also let the process executing on node \( p_i \) evaluate to a value. That is,

\[ \langle p_1, e_1 \rangle \mid \cdots \mid \langle p_i, e_i \rangle \mid \cdots \mid \langle p_m, e_m \rangle \]

\[ \Longrightarrow^* \]

\[ \langle p_1, e'_1 \rangle \mid \cdots \mid \langle p_i, v \rangle \mid \cdots \mid \langle p_j, e'_n \rangle \]

Fig. 11: DFLATE evaluation step for a single process
Fig. 12: DFLATE evaluation step for case terms

\[ \begin{align*}
\text{[DE-CASE]} & \quad p_l, D \vdash e \rightarrow e' \\
\text{[DE-CASE1]} & \quad p_l, D \vdash (\text{case in}_{j_1}(x). e_1 | in_{j_2}(x). e_2) \rightarrow \text{case e'} \text{ of in}_{j_1}(x). e_1 | in_{j_2}(x). e_2 \\
\text{[DE-CASE2]} & \quad p_l, D \vdash (\text{case in}_{j_1}(v) \text{ of in}_{j_2}(x). e_1 \mid in_{j_2}(x). e_2) \rightarrow e_1[v/x] \\
\end{align*} \]

Fig. 13: Propagation of where terms

\[ \begin{align*}
\text{[PAR-STEP]} & \quad p_l, \Pi_{\text{init}} \vdash e_i \rightarrow e_i' \quad i \in \{1 \ldots m\} \\
\text{[PAR-SPAWN]} & \quad \langle p_{l_i}, e_i \rangle \ldots \langle p_{l_m}, e_m \rangle \Rightarrow \langle p_{l_1}, e_1 \rangle \ldots \langle p_{l_m}, e_m \rangle \\
\text{[PAR-SEND-RECV]} & \quad \langle p_{l_i}, e_i \rangle \ldots \langle p_{l_m}, e_m \rangle \Rightarrow \langle p_{l_j}, e_j \rangle \ldots \langle p_{l_m}, e_m \rangle \\
\end{align*} \]

Fig. 14: DFLATE distributed semantics

\[ \begin{align*}
\text{[DP-UNIT]} & \quad \Pi \vdash \ell \leq \text{unit} \\
\text{[DP-HALT]} & \quad \Pi \vdash \ell \leq \text{halt} \\
\text{[DP-PAIR]} & \quad \Pi \vdash \ell \leq \tau_1 \times \tau_2 \\
\text{[DP-UNPAIR]} & \quad \Pi \vdash \ell \leq \ell' \; \text{says} \; \tau \\
\text{[DP-ASSUME]} & \quad \Pi \vdash \ell \leq \text{assumed} \\
\text{[DP-TRANS]} & \quad \Pi \vdash \ell \leq \text{transformed} \\
\end{align*} \]

Fig. 15: FLAC Robust Assumption

\[ \begin{align*}
\text{[R-STATIC]} & \quad \ell \vdash p \geq \ell' \\
\text{[R-ASSUME]} & \quad \ell ; p \vdash \ell' \geq \ell \\
\text{[R-CONJ]} & \quad \ell ; p \vdash \ell' \geq \ell \wedge q \\
\text{[R-DISJ]} & \quad \ell ; p \vdash \ell' \geq \ell \vee q \\
\text{[R-TRANS]} & \quad \ell ; p \vdash \ell' \geq \ell \circ q \\
\text{[R-WEAKEN]} & \quad \ell ; p \vdash \ell' \geq q \\
\end{align*} \]

Fig. 16: Type protection levels

context II and those that are assumed during the evaluation of the process \( \langle p_{l_i}, e_i \rangle \).

**Proposition 1.** Let S be the well-typed distributed system from Definition B.1. If \( \Pi^D \) is the delegation approximation of the process \( \langle p_{l_i}, e_i \rangle \), then the following hold.

1) \[
\exists \langle p_{l_1}, e_1 \rangle \ldots \langle p_{l_m}, e_m \rangle \Rightarrow^* \langle p_{l_1}, e_1 \rangle \ldots \langle p_{l_m}, e_m \rangle
\]

2) \[
\forall \ell, \ell' \Pi \vdash \ell \leq \ell' \Rightarrow \Pi^D \vdash \ell \leq \ell'
\]

3) \[
\forall \ell, \ell' \Pi \vdash \ell \leq \ell' \Rightarrow \Pi \not\vdash \ell \leq \ell'
\]
We define the trace of a process using the observation function.

**Definition B.3 (Process Trace of a Node).** Let $\mathcal{S}$ be the well-typed distributed system as follows.

$$(p_1,e_1) \cdots (p_i,e_i) \cdots (p_j,e_j) \cdots (p_m,e_m)$$

Then process trace of node $p_j$ with respect to a delegation context $\Pi$ and an attacker at level $\ell$, denoted as $Tr(S, p_j, \Pi, \ell)$, is the sequence of the expressions reduced on that node as observed by the attacker. That is, $O((p_j, e_j), \Pi, \ell)$, ... , $O((p_j, e_j^k), \Pi, \ell)$ where $\ell$ is the trace concatenation operator.

Note that a trace may contain consecutive duplicates because it remains the same in those steps when other processes are scheduled to evaluate. The definition of trace duplication is provided in Appendix C. We assume that the attacker is associated with the node $p_j$. The trace is computed as follows:

$$Tr(S, p_j, \hat{\Pi}^D, p_j) = O((p_j, e_j), \hat{\Pi}^D, p_j), \ldots, O((p_j, e_j^k), \hat{\Pi}^D, p_j)$$

where $\hat{\Pi}^D$ is the delegation approximation of the process running on node $p_j$. Intuitively, this is an approximation of the actual trace observed by an attacker during the execution of a process on the node $p_j$. Note that this is safe because it assumes the largest set of delegations that an attacker can influence. Two traces are equivalent if they are equal after applying the observation function and removing the duplicates.

**Definition B.4 (Trace Equivalence).** Two traces, $Tr_1$ and $Tr_2$, are equivalent, denoted as $Tr_1 \approx Tr_2$, if they are equal after removing the duplicates. That is

$$Tr_1 \approx Tr_2 \iff U(Tr_1) = U(Tr_2)$$

We use these formal definitions to instantiate a stronger confidentiality attacker. Our noninterference result against this attacker generalizes the results stated in Theorem 1. Informally, it states that the attacker with limited confidentiality authority cannot distinguish between traces from executions differing in secret inputs. Theorem 2 formally states the noninterference against this attacker.

---

**Fig. 17:** DFLATE typing rules for standard terms

**Definition B.2 (Observation Function).** We show $O(e, \Pi, A)$ for interesting cases; for the remaining terms, the function is homomorphically applied to the subexpressions of $e$. 

<table>
<thead>
<tr>
<th>e</th>
<th>$O(e, \Pi, A)$</th>
<th>Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\eta_\ell v, \Pi, A$</td>
<td>$\eta_\ell O(v, \Pi, A)$</td>
<td>$\Pi \vdash A \gg \ell$</td>
</tr>
<tr>
<td>$\eta_\ell$</td>
<td>$\eta_\ell$</td>
<td>$\Pi \not\vdash A \gg \ell$</td>
</tr>
<tr>
<td>$e, \Pi, A$</td>
<td>$TEE^\ell O(e', \Pi, A)$</td>
<td>$\Pi \not\vdash A \gg \ell$</td>
</tr>
<tr>
<td>$e, \Pi, A$</td>
<td>$TEE^\ell$</td>
<td>$\Pi \not\vdash A \gg \ell$</td>
</tr>
<tr>
<td>$e, \Pi, A$</td>
<td>$runTEE^\ell O(e', \Pi, A)$</td>
<td>$\Pi \not\vdash A \gg \ell$</td>
</tr>
<tr>
<td>$e, \Pi, A$</td>
<td>$runTEE^\ell$</td>
<td>$\Pi \not\vdash A \gg \ell$</td>
</tr>
</tbody>
</table>

The observation function erases all terms protected at levels that do not flow to the level of the attacker. Similarly, it also erases the terms running inside a TEE that has less integrity and more confidentiality than the attacker. This precisely models that an attacker, with restricted access to cryptographic keys, cannot distinguish ciphertexts. We lift the observation function to a process executing on node $n$ as below.

$$O(n, e, \Pi, A) = \langle n, O(e, \Pi, A) \rangle$$
Fig. 18: DFLATE typing rules for secure operations

Fig. 19: DFLATE typing rules for spawn and communication primitives
Fig. 20: DFLATE Evaluation extensions

APPENDIX C
DFLATE NONINTERFERENCE PROOFS

We use Pottier and Simonet [33] technique to prove noninterference. At a high-level, the techniques involves extend the calculus to have bracketed expressions and values that represent two different executions. Proving noninterference for DFLATE program entails proving that the individual projections of the final bracketed values are equal for inputs that are confidential and have less integrity.

Figure 20 shows evaluation extensions. Nesting of bracketed expressions isn’t allowed. Figure 22 defines projection. Typing extensions shown in Figure 23.

\[
\hat{v} ::= \ldots | (\hat{v} | \hat{v}) \\
e ::= \ldots | (e | e)
\]
$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle$$

$$i \in \{1 \ldots m\} \quad e'_j = (e'_{i_1} \mid e'_{i_2}) \quad j = \begin{cases} n & \text{if } n \neq m \\ \in \{1 \ldots m\} & \text{otherwise} \end{cases}$$

$$e'_j = \begin{cases} e'_j & \text{if } n = m \text{ and } j = i \\ e_j & \text{if } n \neq m \\ (e'_{i_1} \mid e'_{i_2}) & \text{if } n = m, j \neq i \text{ and } e_j = (e'_{i_1} \mid e'_{i_2}) \\ T[e'_n] & \text{if } n = m, j \neq i \text{ and } e_j = T[e] \end{cases}$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle$$

$$i \in \{1 \ldots m\} \quad e'_j = (\langle e_{i_1} \mid e'_{i_2} \rangle) \quad j = \begin{cases} n & \text{if } n \neq m \\ \in \{1 \ldots m\} & \text{otherwise} \end{cases}$$

$$e'_j = \begin{cases} e'_j & \text{if } n = m \text{ and } j = i \\ e_j & \text{if } n \neq m \\ (e_{i_1} \mid e'_{i_2}) & \text{if } n = m, j \neq i \text{ and } e_j = (e_{i_1} \mid e_{i_2}) \\ T[e'_n] & \text{if } n = m, j \neq i \text{ and } e_j = T[e] \end{cases}$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle$$

$$e_i = T[(\text{spawn } @ q (pl.chvet [pc_1 ; \tau], ch.vet [pc_2 ; \tau]), e_{eq} \text{ then } e_{aq} \mid e)]$$

$$e'_i = T[(e'_{aq} \mid e_{eq})]$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle \parallel \langle q, (\bullet \mid e_{eq} \mid e_{aq}) \rangle$$

$$e_i = T[(\text{recv } \nu \text{ as } x \text{ in } e_{aq})]$$

$$e'_i = T[(e'_{aq} \mid e_{eq})]$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle \parallel \langle q, (\bullet \mid e_{eq} \mid e_{aq}) \rangle$$

$$e_i = T[(\text{send } \nu \text{ as } x \text{ in } e_{aq})]$$

$$e'_i = T[(e'_{aq} \mid \text{send } \nu \text{ as } x \text{ in } e_{aq})]$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle \parallel \langle q, (\bullet \mid e_{eq} \mid e_{aq}) \rangle$$

$$e_i = T[(\text{recv } \nu \text{ as } x \text{ in } e_{aq})]$$

$$e'_i = T[(e'_{aq} \mid \text{recv } \nu \text{ as } x \text{ in } e_{aq})]$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle \parallel \langle q, (\bullet \mid e_{eq} \mid e_{aq}) \rangle$$

$$e_i = T[(\text{recv } \nu \text{ as } x \text{ in } e_{aq})]$$

$$e'_i = T[(e'_{aq} \mid \text{recv } \nu \text{ as } x \text{ in } e_{aq})]$$

$$\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \implies \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_n, e'_m \rangle \parallel \langle q, (\bullet \mid e_{eq} \mid e_{aq}) \rangle$$

$$e_i = T[(\text{send } \nu \text{ as } x \text{ in } e_{aq})]$$

$$e'_i = T[(e'_{aq} \mid \text{send } \nu \text{ as } x \text{ in } e_{aq})]$$

Fig. 21: DFLATE Evaluation extensions for system
Fig. 22: Projection for bracketed expressions. • denotes empty expression.

Fig. 23: DFLATE Typing Extensions
A. Adequacy

We prove that the bracketed semantics are adequate – sound and complete. By soundness, we mean that given a step in the bracketed execution, then at least one of the left or right projections take one or more steps such that they are in relation with the bracketed execution. By completeness, we mean that given a left and right execution, we can construct a corresponding bracketed execution. We now prove the soundness of the bracketed semantics.

**Lemma 1** (Soundness for Single Thread). If $p, D \vdash e \rightarrow e'$ then $p, D \vdash [e]_k \rightarrow^* [e']_k$ for some $k \in \{1, 2\}$.

**Proof.** Inducting on the step relation. Interesting cases are described below.

**Case BD-STEP:** Since $p, D \vdash (e_1 | e_2) \rightarrow (e'_1 | e'_2)$, we have that $p, D \vdash e_k \rightarrow e'_k$. Since $e_k = [e]_k$ and $e'_k = [e']_k$, we have that $p, D \vdash [e]_k \rightarrow [e']_k$.

**Case BD-APP:** First we consider the case when there is no where term surrounding the function. That is, given $p, D \vdash (\lambda(x: \tau_1)[p_{c_1}, \Theta_1, \mathcal{P}_1, \Pi_1].e_1 | \lambda(x: \tau_2)[p_{c_2}, \Theta_2, \mathcal{P}_2, \Pi_2].e_2) \nu \rightarrow (e_1[x \mapsto [v_1]] | e_2[x \mapsto [v_2]])$. For $k \in \{1, 2\}$, we have to prove that $p, D \vdash \lambda(x: \tau_k)[p_{c_k}, \Theta_k, \mathcal{P}_k, \Pi_k].e_k [v]_k \rightarrow e_k[x \mapsto [v]_k]$

This is straightforward from DE-APP. Thus both left and right executions take a step.

Next, we consider the case in which the function is surrounded by where terms. Without loss of generality, let $p, D \vdash ((\lambda(x: \tau_1)[p_{c_1}, \Theta_1, \mathcal{P}_1, \Pi_1].e_1)$ where $e'_1 \ | \lambda(x: \tau_2)[p_{c_2}, \Theta_2, \mathcal{P}_2, \Pi_2].e_2) \nu \rightarrow ((e_1[x \mapsto [v_1]])$ where $e'_1 \ | e_2[x \mapsto [v_2]])$

We have to prove that $p, D \vdash (\lambda(x: \tau_1)[p_{c_1}, \Theta_1, \mathcal{P}_1, \Pi_1].e_1 [v_1]) \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_k) [v_1] \rightarrow (p_1, [e_1]_k) \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_k) [v_2] \rightarrow (p_1, [e_1]_k) \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_k)$ for $k \in \{1, 2\}$.

**Proof.** We induct on the system step relation.

**Case PAR-STEP:** From the premises of PAR-STEP, we have that $p_i, D_{init} \vdash e_i \rightarrow e'_i$. Invoking soundness for single process (Lemma 1), we have that $p_i, D_{init} \vdash [e_i]_k \rightarrow [e'_i]_k$ for some $i \in \{1 \ldots m\}$ and for all $k \in \{1, 2\}$. Thus, we have $\langle p_1, [e_1]_k \rangle \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_k) \rightarrow \langle p_1, [e_1]_k \rangle \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_k)$.

**Case PAR-SPAWN:** Straightforward from the projection of premises.

**Case PAR-SEND-REC:** Straightforward from the projection of premises.

**Case BD-CONS:** If $n = m$ and $j = i$, the case is a straightforward application of Lemma 1 (soundness of single process).

If $n = m$ and $j \neq i$, then from the premise, we have that $\langle p_i, [e_i]_1 \rangle \ | \ ... \ | \ (p_j, [e_j]_1) \rightarrow \langle p_i, [e'_i]_1 \rangle \ | \ ... \ | \ (p_j, [e'_j]_1)$ where $[e_i]_1 = e_i$. Similarly, we have that $\langle p_i, [e_i]_2 \rangle \ | \ ... \ | \ (p_j, [e_j]_2) \rightarrow \langle p_i, [e'_i]_2 \rangle \ | \ ... \ | \ (p_j, [e'_j]_2)$ where $[e_i]_2 = e_i$.

Remaining terms are unchanged.

If $n \neq m$ then remaining terms unchanged, we have that $\langle p_i, [e_i]_1 \rangle \rightarrow \langle p_i, [e'_i]_1 \rangle \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_1) \rightarrow \langle p_i, [e_i]_2 \rangle \ | \ ... \ | \ ... \ | \ (p_n, [e_n]_2)$. Hence proved.

**Case BD-ASSUME:** Proof similar to above case.

**Case BD-UNPAIR:** Proof similar to the last case ($n \neq m$) of BD-PAR-STEP-L.

**Case BD-SPAWN-L:** Proof similar to the last case ($n \neq m$) of BD-PAR-STEP-L except that we now reason for taking the step on the right execution.

**Case BD-SPAWN-R:** Remaining terms unchanged, we have that $\langle p_i, T_i[\text{send } \nu \text{ as } x \in e_k] \rightarrow (p_i, T_i[e_{sk}] \ | \ (p_j, T_i[e_n[x \mapsto v]])$. The right project of the process running on $p_i$ and $p_j$ remains unchanged. Hence proved.

□
Case BD-PAR-SEND-RECV-R: Similar to above case but the right projection takes a step and left projection remains unchanged.
Case BD-PAR-SEND-RECV-S: Similar to the case BD-PAR-SEND-RECV-L.
Case BD-PAR-RECV-SEND-R: Similar to the case BD-PAR-SEND-RECV-L.

Before proving completeness, we prove if the bracketed term is stuck then either left or right execution are stuck.

Lemma 3 (Stuck Expressions for Single Thread). If \((p,e)\) gets stuck then \((p,[e]_i)\) is stuck for some \(i \in \{1,2\}\).

Proof. We prove by induction on the structure of \(e\) that if \((p,e)\) gets stuck then \((p,[e]_k)\) is stuck for some \(i \in \{1,2\}\).
Case \(\hat{v}\): By assumption, \(e\) is not stuck.
Case \(x\): No reduction for a variable. Hence \([x]_i\) is stuck as well.
Case \(e\) \(\hat{e}\): Since BD-APP and DE-APP are not applicable, it follows that \(e\) is not of the form \((\lambda x : \tau)[pc',\Theta',\mathcal{P},\Pi_{\lambda},e_1] | (\lambda x : \tau)[pc',\Theta',\mathcal{P},\Pi_{\lambda},e_2]\) or \((\lambda x : \tau)[pc',\Theta',\mathcal{P},\Pi_{\lambda},e]\). Thus \([e]_i\) is stuck as well.
Case \(\eta e\): Since \(e\) is stuck, applying induction hypothesis it follows that \([e]_i\) is also stuck and so \([\eta e]_i\) is also stuck.
Case \(\text{proj}, e, \text{inj}_i\): Similar to the above case.
Case \(\tau\): Since BD-TAPP and DE-TAPP are not applicable, it follows that \(e\) is not of the form \((v_1 | v_2)\) or \(\Lambda X[pc,\Theta,\mathcal{P},\Pi]\).
Thus \([e]_i\) \(\tau\) is stuck as well.
Case \(e\) \(\text{of inj}_i(x), e_1 | \text{inj}_j(x), e_2\): Since BD-CASE and DE-CASE are not applicable, it follows that \(e\) is not of the form \((\text{inj}_i v_1 | \text{inj}_j v_2)\) or \(\text{inj}_j v\). It follows that \([e]_i\) \(\text{of inj}_i(x), e_1 | \text{inj}_j(x), e_2\) is also stuck.
Case \(\text{bind} x = v \in e'\): Similar to the above case.
Case \(\text{assume} e \in e'\): Similar to the above case.
Case \(\text{TEE} e\): Cannot get stuck.
Case \(\text{runTEE} e\): Applying induction to \(e\), we have that \(\text{runTEE} e\) is stuck for some \(i \in \{1,2\}\). \(\text{runTEE} e\) Case \(\text{runTEE} \hat{v}\): Cannot get stuck.
Case \(\text{Bracket} e = (e_1 | e_2).\) The only way this gets stuck is if both \(e_1\) and \(e_2\) get stuck. This is a contradiction.
Case Other: Remaining expressions do not take a step (single thread step relation \(\rightarrow\)).

\[
\text{Lemma 4 (Stuck Expressions for System). If } \langle p_1,e_1 \rangle \| \cdots \| \langle p_n,e_n \rangle \text{ gets stuck then the configuration } \langle p_1,[e_1]_k \rangle \| \cdots \| \langle p_n,[e_n]_k \rangle \text{ for some } k \in \{1,2\}.
\]

Proof. Induction on the structure of the system configuration.
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[\text{send} \nu v \text{ then } e_{sk}] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{recv} \nu \text{ as } x \text{ in } e]\) on any other process, otherwise it would have taken a step using PAR-SEND-RECV. Thus it is stuck for all \(k \in \{1,2\}\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): Similar to above case.
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[\text{send} \nu v \text{ then } e_{sk}] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{recv} \nu \text{ as } x \text{ in } e | e'']\) on any other process, otherwise it would have taken a step using BD-PAR-SEND-RECV-L. Thus it is stuck for \(k = 1\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): Similar to above case.
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): Similar to above cases but \(k = 2\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[\text{recv} \nu v \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{recv} \nu \text{ as } x \text{ in } e | e']\), otherwise it would have taken a step using BD-PAR-SEND-RECV-R. Thus \(k = 1\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{recv} \nu \text{ as } x \text{ in } e] \text{ or } (e'' | T[\text{recv} \nu \text{ as } x | e'']\), otherwise it would have taken a step using BD-PAR-SEND-RECV-R. Thus \(k = 2\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{send} \nu v \text{ then } e_{sk}] \) or \(T[\text{send} \nu v \text{ then } e_{sk}] \), otherwise it would have taken a step using BD-PAR-SEND-RECV-L. Thus \(k = 1\).
Case \(\langle p_1,e_1 \rangle \| \cdots \| \langle p_i,T[e' \text{ as } x \text{ in } e] \rangle \| \cdots \| \langle p_n,e_n \rangle \): There is no matching \(T[\text{send} \nu v \text{ then } e_{sk}] \), otherwise it would have taken a step using BD-PAR-SEND-RECV-R. Thus \(k = 2\).
Case \(\text{Spawn} \): Spawn always takes a step and cannot be stuck.
Case Other: We have that all processes are stuck. Invoking Lemma 3 on each process, we have that each process is stuck for some \(k \in \{1,2\}.

We now prove the completeness of the bracketed semantics.

\[
\text{Lemma 5 (Completeness). If } \langle p_1,[e_1]_k \rangle \| \cdots \| \langle p_i,[e_i]_k \rangle \| \cdots \| \langle p_n,[e_n]_k \rangle \Rightarrow^* \langle p_1,e'_1 \rangle \| \cdots \| \langle p_i,e'_i \rangle \| \cdots \| \langle p_n,e'_n \rangle \text{ for all } k \in \{1,2\} \text{ then there exists a } \hat{v}_1 \text{ such that } \langle p_1,e_1 \rangle \| \cdots \| \langle p_i,e_i \rangle \| \cdots \| \langle p_n,e_n \rangle \Rightarrow^* \langle p_1,e'_1 \rangle \| \cdots \| \langle p_i,e'_i \rangle \| \cdots \| \langle p_n,e'_n \rangle \text{.}
\]

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Proof. From the rules in Figure 20, the brackets are always propagated outside, so they can only be applied finite number of times. If the system is stuck, then from Lemma 4, we have that either left or right executions are stuck. This is contradicting our assumption. Thus the system will not get stuck. Hence it must terminate to some value.

B. Proofs for Non-Interference

Before proving Theorem 1 and Theorem 2, we prove few supporting lemmas required for proving the type preservation.

Lemma 6 (Closed Values in Send). Let \( \Pi; \Theta; p; p'c_1 \vdash e_1: \tau_1 \parallel \cdots \parallel \Pi; \Theta; p; p'c_1 \vdash e_i: \tau_i \parallel \cdots \parallel \Pi; \Theta; p; p'c_1 \vdash e_m: \tau_m \). If \( \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i \rangle \parallel \cdots \parallel \langle p_n, e_m \rangle \Rightarrow^* \langle p_1, e_1' \rangle \parallel \cdots \parallel \langle p_i, T[\overrightarrow{\text{send}} \nu v \text{ then } e_{an}] \rangle \parallel \cdots \parallel \langle p_n, e_m' \rangle \) then \( \emptyset; \emptyset; p'_i; p'_c_i \vdash v: \tau \).

Proof. The only case where unbound variables appear is in closures. However, the top level program is closed and so all variables are \( \beta \)-substituted before getting reduced to \( v \).

Lemma 7 (Clearance). Let \( \Pi; \Gamma; \Theta, p; pc \vdash e: \tau \). Then \( \Pi \not\vdash p \bowtie pc \)

Proof. Proof is by straight forward induction on the typing judgment.

Lemma 8 (Robust Assumption). If \( \Pi \vdash pc \bowtie \daleth \langle b \rangle \), then \( \Pi \vdash \langle a \bowtie b \rangle \bowtie pc \bowtie \daleth \langle b \rangle \) for any \( a, b \in \mathcal{L} \).

Lemma 9 (Robust Protection). If \( \Pi \vdash pc \bowtie \tau \), then \( \Pi \vdash \langle a \bowtie b \rangle \vdash pc \bowtie \tau \) for any \( a, b \in \mathcal{L} \).

Lemma 10 (\( \Pi \) Extension). If \( \Pi; \Gamma; \Theta; p; pc \vdash e: \tau \) then \( \Pi; \Gamma; \Theta; p; pc \vdash e: \tau \) for any \( p, q \in \mathcal{L} \).

Proof. Proof is by induction on the derivation of the typing judgment.

Case DT-VAR: Straightforward from the corresponding typing judgment.

Case DT-UNIT: Straightforward from the corresponding typing judgment.

Case DT-DEL: Straightforward from the corresponding typing judgment.

Case DT-LAM: Straightforward from the corresponding typing judgment.

Case DT-APP: Straightforward from the corresponding typing judgment.

Case DT-TAPP: Straightforward from the corresponding typing judgment.

Case DT-PAIR: Straightforward from the corresponding typing judgment.

Case DT-UNPAIR: Straightforward from the corresponding typing judgment.

Case DT-INJ: Straightforward from the corresponding typing judgment.

Case DT-CASE: Straightforward from the corresponding typing judgment.

Case DT-UNITM: Straightforward from the corresponding typing judgment.

Case DT-BINDM: Straightforward from the corresponding typing judgment.

Case DT-ASSUME: Given \( \Pi; \Gamma; \Theta; p; pc \vdash \text{ assume } e \text{ in } e' : \tau \). From the premises,

\[
\begin{align*}
\Pi; \Gamma; \Theta; p; pc \vdash: \langle a \bowtie b \rangle \\
\Pi \vdash pc \bowtie \daleth \langle b \rangle \\
\Pi \vdash \daleth \langle a^{-} \rangle \bowtie \daleth \langle b^{-} \rangle \\
\Pi \vdash: \langle a \bowtie b \rangle ; \Gamma; \Theta; p; pc \vdash e' : \tau
\end{align*}
\]

Applying inductive step to (1) and (4), we have \( \Pi \vdash: \langle a \bowtie b \rangle ; \langle p \bowtie q \rangle ; \Gamma; \Theta; p; pc \vdash e' : \tau \) and \( \Pi \vdash: \langle p \bowtie q \rangle ; \Gamma; \Theta; p; pc \vdash e : \langle a \bowtie b \rangle \). Applying Lemma 8 to premises (C-B) and (C-B) we have \( \Pi \vdash: \langle p \bowtie q \rangle \vdash pc \bowtie \daleth \langle b \rangle \) and \( \Pi \vdash: \langle p \bowtie q \rangle \vdash: \daleth \langle a^{-} \rangle \bowtie \daleth \langle b^{-} \rangle \).

Hence proved.

Case DT-WHERE: Similar to the proof of case DT-ASSUME.

Case DT-BRACKET: Applying induction to the premises of DT-BRACKET gives us that \( \Pi \vdash: \langle a \bowtie b \rangle ; \langle p \bowtie q \rangle ; \Gamma; \Theta; p; pc \vdash \langle e_1 \mid e_2 \rangle : \tau \).

Case DT-BRACKET-WHERE: Similar to above case.

Case DT-BRACKET-VALUES: Applying induction to premises, we have the conclusion.

Case DT-SPAWN: Applying induction to premises, we have the conclusion.

Case DT-SEND: Applying induction to premises, we have the conclusion.

Case DT-SENDDDEL: Applying induction to premises, we have the conclusion.

Case DT-RECEIVE: Applying induction to premises, we have the conclusion.

Case DT-TEE: Applying induction to premises, we have the conclusion.

Case DT-TEEG: Applying induction to premises, we have the conclusion.

Case DT-HALT: Applying induction to premises, we have the conclusion.

Case DT-HALT2: Applying induction to premises, we have the conclusion.
The following lemma is required to prove the type preservation. It says that a expression $e$ well-typed at $pc$ is still well-typed at a reduced $pc'$ that satisfies the clearance.

**Lemma 11 (PC Reduction).** Let $\Pi; \Gamma; \Theta; p; pc \vdash e : \tau$. For all $pc, pc'$, such that $\Pi \vdash pc' \subseteq pc$ and $\Pi \vdash p \models pc'$, then $\Pi; \Gamma; \Theta; p; pc' \vdash e : \tau$ holds.

**Proof.** Proof is by induction on the derivation of the typing judgment.

**Case DT-VAR:** Straightforward from the corresponding typing judgment.

**Case DT-VAR:** Straightforward from the corresponding typing judgment.

**Case DT-UNIT:** Straightforward from the corresponding typing judgment.

**Case DT-DEL:** Straightforward from the corresponding typing judgment.

**Case DT-CHANNEL:** Straightforward from the corresponding typing judgment.

**Case DT-LAM:** Straightforward from the corresponding typing judgment.

**Case DT-APP:** Given, $\Pi; \Gamma; \Theta; p; pc \vdash e : \tau$. From DT-App, we have

\[
\Pi; \Gamma; \Theta; p; pc \vdash e : \tau_1 \\
\Pi; \Gamma; \Theta; p; pc \vdash e' : \tau_2 \\
\Pi \vdash pc \subseteq pc'' \\
\Pi \vdash p \models pc \\
\Theta \models_{\text{dom}(\sigma') = \Theta'} \\
\forall (p \models q) \in \Pi'. \Pi \vdash p \models q
\]

Since $\Pi \vdash p \models pc'$, Applying induction to the premises we have $\Pi; \Gamma; \Theta; p; pc' \vdash e : \tau_1, pc'' \vdash \tau_2$ and $\Pi; \Gamma; \Theta; p; pc' \vdash e' : \tau_1$. From R-TRANS, we have $\Pi \vdash pc' \subseteq pc''$. Hence we have all the premises.

**Case DT-TAP:** Similar to DT-App case.

**Case DT-PAIR:** Straightforward from the corresponding typing judgment.

**Case DT-PAIR:** Straightforward from the corresponding typing judgment.

**Case DT-INJ:** Straightforward from the corresponding typing judgment.

**Case DT-CASE:** Straightforward from the corresponding typing judgment.

**Case DT-UNITMSEALED:** Straightforward from the corresponding typing judgment.

**Case DT-BINDM:** Before applying induction to the premises, we have to prove that $\Pi \vdash p \models pc' \sqcup \ell$. Applying clearance (Lemma 7) to the premise $\Pi; \Gamma; x : \tau' ; \Theta; p; pc \sqcup \ell \vdash e' : \tau$, we have that $\Pi \vdash p \models pc \sqcup \ell$ and this implies $\Pi \vdash pc' \sqcup \ell \subseteq pc \sqcup \ell$. Hence we can apply induction to the premise and the rest of the proof os straight forward.

**Case DT-ASSUME:** Given, $\Pi; \Gamma; \Theta; p; pc \vdash \text{assume } e \text{ in } e' : \tau$. From DT-ASSUME, we have

\[
\Pi; \Gamma; \Theta; pl; pc \vdash e : (p \models q) \\
\Pi \vdash pc \models q \\
\Pi \vdash \nabla(p^{-}) \models \nabla(q^{-}) \\
\Pi \cdot (p \models q); \Gamma; \Theta; p; pc \vdash e' : \tau \\
\Pi \vdash p \models pc
\]

Applying inductive hypothesis to (11) and (14) we have $\Pi; \Gamma; \Theta; p; pc' \vdash e : (p \models q)$. Applying inductive hypothesis to (14), we have $\Pi \cdot (p \models q); \Gamma; \Theta; p; pc' \vdash e' : \tau$. Since $\Pi \vdash pc' \subseteq pc$, we have $\Pi \vdash pc' \models \nabla(q)$. Combining, we have all the premises for DT-ASSUME and thus

\[
\Pi; \Gamma; \Theta; p; pc' \vdash \text{assume } e \text{ in } e' : \tau
\]

**Case DT-WHERE:** Given, $\Pi; \Gamma; \Theta; p; pc \vdash e \text{ where } v : \tau$. From DT-WHERE, we have

\[
\Pi; \Gamma; \Theta; pl; pc \vdash v : (p \models q) \\
\Pi \vdash pl \models q \\
\Pi \vdash \nabla(p^{-}) \models \nabla(q^{-}) \\
\Pi \cdot (p \models q); \Gamma; \Theta; p; pc \vdash e : \tau
\]

Applying inductive hypothesis to (16) and (19) (and weakening delegation context (Lemma 10)), we have $\Pi; \Gamma; \Theta; p; pc' \vdash v : (p \models q)$ and $\Pi \cdot (p \models q); \Gamma; \Theta; p; pc' \vdash e : \tau$. Using R-TRANS on (17), we have $\Pi \vdash pc' \models \nabla(q)$ and so $\Pi \vdash pc' \models \nabla(q)$. 

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Combining, we have all the premises for T-WHERE and thus
\[
\Pi; \Gamma; \Theta; \rho; p, \rho' \vdash e \quad \text{where } \nu : \tau
\]

Case DT-TEE: Given \(\Pi; \Gamma; \Theta; \rho; p, \rho' \vdash \text{TEE}^t e : \tau\). From the premise, we have
\[
\Pi; \emptyset; \Theta; t; t^- \land \rho^- \vdash e : \tau
\]
Before applying induction to the premises, we have to prove that \(\Pi \vdash t \succ t^- \land \rho'^- \). From clearance (Lemma 7, we have that \(\Pi \vdash t \succ t^- \land \rho^-\). Combined with \(\Pi \vdash \rho' \subseteq \rho\), we have that
\[
\Pi \vdash t \succ t^- \land \rho'^-
\]
We now use straightforward induction.

Case DT-TEEG: Similar to above case.

Case DT-HALT: Applying induction to premises, we have the conclusion.

Case DT-HALT2: Applying induction to premises, we have the conclusion.

Case DT-RECEIVE: Given \(\Pi; \Gamma; \Theta; pl; \rho \vdash \text{recv ch as } x \text{ in } e' : \tau\). We therefore have the premises
\[
\Pi; \Gamma; \Theta; pl; \rho \vdash ch : \text{chan}_{p \rightarrow q} \rho_{\text{ch}} \tau
\]
\[
\Pi; \Gamma, x : \tau; \Theta; p; \rho'' \vdash e : \tau'
\]
\[
\Pi \vdash \rho' \subseteq \rho''
\]
\[
\Pi \vdash \rho_{\text{ch}} \subseteq \rho''
\]
\[
\Pi \vdash \rho \subseteq \rho_{\text{ch}}
\]
\[
\Pi \vdash p \succ \rho
\]
\[
\Pi \vdash \rho' \subseteq \rho_{\text{ch}}
\]
From DT-CHAN, we have
\[
\Pi; \Gamma; \Theta; p; \rho' \vdash \text{recv ch as } x \text{ in } e' : \tau
\]
Using R-TRANS, from (22) and (24), we have
\[
\Pi \vdash \rho' \subseteq \rho''
\]
and
\[
\Pi \vdash \rho' \subseteq \rho_{\text{ch}}
\]
Thus \(\Pi; \Gamma; \Theta; p; \rho' \vdash \text{recv ch as } x \text{ in } e' : \tau\)

Case DT-SEND: Proof similar to above case

Case DT-SENDDEL: Proof similar to above case except that values can be well-typed under empty delegation set.

Case DT-SPAWN: Given \(\Pi; \Gamma; \Theta; p; \rho \vdash \text{spawn } @q (pl[ch_1[pc_1] ; \tau_1], ch_2[pc_2] ; \tau_2)] e_1 \text{ then } e_2 : \tau\). From the premises, we have
\[
\Pi; \Gamma; \Theta[\text{ch}_1 \mapsto \text{chan}_{p \rightarrow q} \text{pc}_1 \tau_1, \text{ch}_2 \mapsto \text{chan}_{p \rightarrow q} \text{pc}_2 \tau_2]; p, \rho \vdash e_2 : \tau
\]
\[
\Pi; \emptyset[\text{ch}_1 \mapsto \text{chan}_{q \rightarrow p} \text{pc}_1 \tau_1, \text{ch}_2 \mapsto \text{chan}_{q \rightarrow p} \text{pc}_2 \tau_2]; q; \rho \vdash e_1 : \text{unit}\Pi \vdash \rho \subseteq \rho
\]
\[
\Pi \vdash p \succ \rho
\]
\[
\Pi \vdash q \succ \rho
\]

Straightforward application of induction hypothesis to the premises.

Case DT-BRACKET: The premise \(\Pi \vdash H^\tau \cup \rho \subseteq \rho''\) implies \(\Pi \vdash H^\tau \cup \rho' \subseteq \rho''\).

Case DT-BRACKET-VALUES: Applying induction to the premises gives the required conclusion.

Case DT-BRACKET-WHERE: Similar to above case.

Lemma 12 (Weakening Channel Environment for Values). If \(\Pi; \Gamma; \Theta; p; \rho \vdash \nu : \tau\) then \(\Pi; \Gamma; \Theta'; p; \rho \vdash \nu : \tau\) for any \(\Theta'\).

Proof. Proof is by induction on the structure of the values.

Case Var: Trivial
Case Unit: Trivial
Case Delegation: Trivial
Case inj: Apply induction to the premise of DT-UNITMSEALED.
Case inj: Apply induction to the premise of DT-INJ.
Case Lambda: Trivial since functions carry their own channel environment.

Case TLambda: Same as above.

Case Where: Apply induction to the premise of DT-INJ.

Case \( \vdash \Box \): Apply induction to the premise of DT-HALT and DT-HALT2.

Case (\( \ddot{\nu}_1 \) where \( \ddot{\nu}_1' \) | \( \ddot{\nu}_2 \) where \( \ddot{\nu}_2' \)): From DT-BRACKET-WHERE, we have that

\[
\Pi \vdash H^\pi \leq \tau
\]

(31)

\[
\Pi; \Gamma; \Theta; p; pc \vdash \ddot{\nu}_1 \quad \Pi \vdash \ddot{\nu}_1' : \tau
\]

(32)

\[
\Pi; \Gamma; \Theta; p; pc \vdash \ddot{\nu}_2 \quad \Pi \vdash \ddot{\nu}_2' : \tau
\]

(33)

Applying induction to the premises, we have that

\[
\Pi \vdash H^\pi \leq \tau
\]

(34)

\[
\Pi; \Gamma; \Theta'; p; pc \vdash \ddot{\nu}_1 \quad \Pi \vdash \ddot{\nu}_1' : \tau
\]

(35)

\[
\Pi; \Gamma; \Theta'; p; pc \vdash \ddot{\nu}_2 \quad \Pi \vdash \ddot{\nu}_2' : \tau
\]

(36)

Thus, from DT-BRACKET-WHERE, we have \( \Pi; \Gamma; \Theta; p; pc \vdash (\ddot{\nu}_1 \mid \ddot{\nu}_2) : \tau \)

Case (\( \ddot{\nu}_1 \) where \( \ddot{\nu}_1' \) | \( \ddot{\nu}_2 \) where \( \ddot{\nu}_2' \)): Similar to above case.

Case (\( \ddot{\nu}_1 \mid \ddot{\nu}_2 \) where \( \ddot{\nu}_2' \)): Similar to above case.

Case (\( \ddot{\nu}_1 \mid \ddot{\nu}_2 \)): From DT-BRACKET-VALUES, we have that

\[
\Pi \vdash H^\pi \leq \tau
\]

(37)

\[
\Pi; \Gamma; \Theta; p; pc \vdash \ddot{\nu}_1 : \tau
\]

(38)

\[
\Pi; \Gamma; \Theta; p; pc \vdash \ddot{\nu}_1 : \tau
\]

(39)

Applying induction to the premises, we have that

\[
\Pi \vdash H^\pi \leq \tau
\]

(40)

\[
\Pi; \Gamma; \Theta'; p; pc \vdash \ddot{\nu}_1 : \tau
\]

(41)

\[
\Pi; \Gamma; \Theta'; p; pc \vdash \ddot{\nu}_1 : \tau
\]

(42)

Thus \( \Pi; \Gamma; \Theta'; p; pc \vdash (\ddot{\nu}_1 \mid \ddot{\nu}_2) : \tau \).

\[\square\]

Lemma 13 (Non-Bracket Values PC). Let \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i : \tau_i \mid \cdots \mid \Pi_m; \Gamma_m; \Theta_m; p_m; pc_m \vdash e_m : \tau_m \). If \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash \ddot{\nu} : \tau \) for some \( i = \{1 \ldots m\} \) such that \( \ddot{\nu} \) is not a bracket then \( \Pi'; \Gamma'; \Theta'; p'; pc' \vdash \ddot{\nu} : \tau \) for any \( pc' \) such that \( \Pi_i \vdash p \gg pc' \).

Proof. Recall that \( pc = \prod_i T_i \cap \bigcap_i pc_i \). Proof is by induction on the values. Also, the condition \( \Pi_i \vdash p_i \gg pc' \) ensures that the proof is straight forward for all values except functions and where terms. We prove important cases.

Case Function: Given \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash \lambda(x: \ddot{\nu})[pc''', \Theta'', P, \Pi_i] \vdash e : \tau \). We have to prove that:

\[
\Pi_i; \Gamma_i; \Theta_i; p'; pc' \vdash \lambda(x: \ddot{\nu})[pc''', \Theta'', P, \Pi_i] \vdash e \quad \Pi_i; \Gamma_i; \Theta_i; p'; pc' \vdash e : \tau
\]

This is straightforward as there is no dependency on \( pc' \) to type check the function body \( e \).

Case Where: Given \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash \ddot{\nu} \) where \( p \gg q \) : \( \tau_i \). We have to prove that:

\[
\Pi_i; \Gamma_i; \Theta_i; p'; pc' \vdash \ddot{\nu} \) where \( p \gg q \) : \( \tau_i \)

From DT-WHERE, we have that \( \Pi_i \vdash pc \gg \nu(q) \). Note that \( pc \) is constant for a given distributed configuration and does not change with the \( pc' \). We are already given that \( \Pi_i \vdash p' \gg pc' \). Applying induction to the remaining premises, we have all premises for DT-WHERE.

\[\square\]

Lemma 14 (Variable Substitution). If \( \Pi; \Gamma; x : \tau' ; \Theta; p; pc \vdash e : \tau \) and \( \Pi; \Gamma; \Theta; p; pc \vdash \ddot{\nu} : \tau' \), then \( \Pi; \Gamma; \Theta; p; pc \vdash e[x \mapsto \ddot{\nu}] : \tau \).

Proof. Proof is by the induction on the structure of the expression \( e \). Interesting case is bracket. Given \( \Pi; \Gamma; x : \tau'; \Theta; p; pc \vdash (e_1 \mid e_2) : \tau \). We have to prove that

\[
\Pi; \Gamma; \Theta; p; pc \vdash (e_1[x \mapsto [\ddot{\nu}_1]] \mid e_2[x \mapsto [\ddot{\nu}_2]]) : \tau
\]
We first describe the case DT-BRACKET. The proof for the cases DT-BRACKET-VALUES and DT-BRACKET-WHERE are similar. From DT-BRACKET, we have that

\[
\Pi; \Gamma, x : \tau' ; \Theta ; p; pc' \vdash e_1 : \tau
\]  
\[\Pi; \Gamma, x : \tau' ; \Theta ; p; pc' \vdash e_2 : \tau
\]

\[H^n \sqsubseteq pc' \quad \Pi \vdash H^n \leq \tau
\]

Applying clearance (Lemma 7), we have that \(\Pi \vdash p \gg pc'\). Depending on whether \(\hat{\circ}\) is a bracket value, we have two cases

**Case** \(\hat{\circ} = (\hat{\circ}_1 \mid \hat{\circ}_2)\): From DT-BRACKET-VALUES (or from DT-BRACKET-WHERE), we have that

\[
\Pi; \Gamma, x : \tau' ; \Theta ; p; pc' \vdash e_1[x \mapsto \hat{\circ}_1] : \tau
\]

\[
\Pi; \Gamma, x : \tau' ; \Theta ; p; pc' \vdash e_2[x \mapsto \hat{\circ}_2] : \tau
\]

and thus

\[
\Pi; \Gamma, x : \tau' ; \Theta ; p; pc \vdash (e_1[x \mapsto \hat{\circ}_1] \mid e_2[x \mapsto \hat{\circ}_2]) : \tau
\]

**Case** \(\hat{\circ} \neq (\hat{\circ}_1 \mid \hat{\circ}_2)\): Since non-bracketed values can be typed under any \(pc\) which acts for the place under which value is typed (Lemma 13), we have that \(\Pi; \Gamma, \Theta; p; pc' \vdash \tau'\). Applying induction to the premises (43) and (44), we get

\[
\Pi; \Gamma, \Theta; p; pc' \vdash e_1[x \mapsto \hat{\circ}_1] : \tau
\]

\[
\Pi; \Gamma, \Theta; p; pc' \vdash e_2[x \mapsto \hat{\circ}_2] : \tau
\]

and thus

\[
\Pi; \Gamma, \Theta; p; pc \vdash (e_1[x \mapsto \hat{\circ}_1] \mid e_2[x \mapsto \hat{\circ}_2]) : \tau
\]

**Lemma 15** (Projection Preserves Types). If \(\Pi; \Gamma, \Theta; p; pc \vdash e : \tau, \text{ then } \Pi; \Gamma, \Theta; p; pc \vdash [e]_i : \tau\) for \(i = \{1, 2\}\).

**Proof.** Proof is by induction on the typing derivation. Interesting case is DT-BRACKET. Applying Lemma 11 to the expression typing judgments, we have the proof.

**Lemma 16** (Type Substitution Preserves Well-Typedness). Let \(\tau'\) be well-formed in \(\Gamma, X, \Gamma'\). If \(\Pi; \Gamma, X, \Gamma'; \Theta; p; pc \vdash e : \tau\) then \(\Pi; \Gamma, \Gamma'[X \mapsto \tau']; \Theta; p; pc \vdash e[X \mapsto \tau'] : \tau[X \mapsto \tau']\).

**Proof.** Proof is by induction on the typing derivation \(\Pi; \Gamma, X, \Gamma', \Theta; p; pc \vdash e : \tau\).

**Case DT-Var:** Depending on whether \(\tau\) uses \(X\) two cases arise. Either \(\Pi; \Gamma, X, \Gamma', \Theta; p; pc \vdash x : \tau, \text{ or } \Pi; \Gamma, X, x : \tau, \Gamma'; \Theta; p; pc \vdash x : \tau\). In the former case, since \(\Gamma, x : \tau\) does not use \(X\), we have that \(\tau[X \mapsto \tau']\) is same as \(\tau\).

In the latter case, we have that \(\Pi; \Gamma, x : \tau, \tau[X \mapsto \tau'], \Theta; p; pc \vdash x : \tau[X \mapsto \tau']\).

**Case DT-Unit:** Straightforward from the corresponding typing judgment.

**Case DT-Del:** Straightforward from the corresponding typing judgment.

**Case DT-Lam:** Given \(\Pi; \Gamma, X, \Gamma', \Theta; p; pc \vdash \lambda(x : \tau_1)(pc', \Theta', \Pi') : e : \tau_1 \xrightarrow{\text{pc'}, \Theta', \Pi', \Pi\lambda, \tau_2}[X \mapsto \tau']\). From DT-LAM \(\Pi; \Gamma, X, \Gamma', x : \tau_1; \Theta; p; pc' \vdash x : \tau_2\). Applying induction hypothesis to the premise, we have \(\Pi; \Gamma, \Gamma'[X \mapsto \tau'], x : \tau_1[X \mapsto \tau'] ; \Theta; p; pc' \vdash e[X \mapsto \tau'] : \tau_2[X \mapsto \tau']\). From DT-LAM, we thus have \(\Pi; \Gamma, \Gamma'[X \mapsto \tau']; \Theta; p; pc \vdash \lambda(x : \tau_1)(pc', \Theta', \Pi') : e[X \mapsto \tau'] : \tau_1[X \mapsto \tau'] \xrightarrow{pc', \Theta', \Pi', \Pi\lambda, \tau_2}[X \mapsto \tau']\).

**Case DT-App:** Given \(\Pi; \Gamma, X, \Gamma', \Theta; p; pc \vdash e : \tau\). Applying induction hypothesis to the premises, we have

\[
\Pi; \Gamma, X, \Gamma'[X \mapsto \tau'] ; \Theta; p; pc \vdash e[X \mapsto \tau'] : \tau_1[X \mapsto \tau'] \xrightarrow{\text{pc'}} \tau_2[X \mapsto \tau']
\]

\[
\Pi; \Gamma, X, \Gamma'[X \mapsto \tau'] ; \Theta; p; pc \vdash e'[X \mapsto \tau'] : \tau_1[X \mapsto \tau']
\]

From DT-APP, we thus have \(\Pi; \Gamma, X, \Gamma'[X \mapsto \tau'] ; \Theta; p; pc \vdash e[X \mapsto \tau'] e'[X \mapsto \tau'] : \tau_2[X \mapsto \tau']\)
Case DT-TLam: Given $\Pi;\Gamma;\Theta; p; pc \vdash \Lambda Y[pc', \Theta', \mathcal{P}, \Pi']. e: \forall Y[pc', \Theta', \mathcal{P}, \Pi']. \tau$. Applying induction hypothesis to the premise of DT-TLAM we have

$$\Pi;\Gamma, (\Gamma', Y)[X \mapsto \tau'];\Theta; p; pc \vdash e[X \mapsto \tau'] : \tau[X \mapsto \tau']$$

So from DT-TLAM, we have

$$\Pi;\Gamma, \Gamma'[X \mapsto \tau'];\Theta; p; pc \vdash \Lambda Y[pc', \Theta', \mathcal{P}, \Pi']. e[X \mapsto \tau'] : \forall Y[pc', \Theta', \mathcal{P}, \Pi']. \tau[X \mapsto \tau']$$

Case DT-TApp: Given $\Pi;\Gamma, \Gamma'[X \mapsto \tau']\Theta; p; pc \vdash \tau'' : \tau[Y \mapsto \tau'']$. Applying induction hypothesis to the premises of T-TAPP, we have $\Pi;\Gamma, \Gamma'[X \mapsto \tau']\Theta; p; pc \vdash e[X \mapsto \tau'] : \forall Y[pc', \Theta', \mathcal{P}, \Pi']. \tau[X \mapsto \tau']$. From DT-TAPP, we thus have $\Pi;\Gamma, \Gamma'[X \mapsto \tau']\Theta; p; pc \vdash (e[X \mapsto \tau']) \tau''[X \mapsto \tau'] : \tau[X \mapsto \tau'][Y \mapsto \tau'']$.

Case DT-Pair: Straightforward application of induction hypothesis to the premises.

Case DT-UnPair: Straightforward application of induction hypothesis to the premises.

Case DT-Inj: Straightforward application of induction hypothesis to the premises.

Case DT-Case: Straightforward application of induction hypothesis to the premises.

Case DT-UnitM: Straightforward application of induction hypothesis to the premises.

Case DT-BindM: Straightforward application of induction hypothesis to the premises.

Case DT-Assume: Straightforward application of induction hypothesis to the premises.

Case DT-Where: Straightforward application of induction hypothesis to the premises.

Case DT-TEE: Straightforward application of induction hypothesis to the premises.

Case DT-TEEG: Straightforward application of induction hypothesis to the premises.

Case DT-Halt: Straightforward application of induction hypothesis to the premises.

Case DT-Halt2: Straightforward application of induction hypothesis to the premises.

Case DT-Spawn: Straightforward application of induction hypothesis to the premises.

Case DT-SendDel: Straightforward application of induction hypothesis to the premises.

Case DT-Send: Straightforward application of induction hypothesis to the premises.

Case DT-Receive: Straightforward application of induction hypothesis to the premises.

Case DT-BRACKET: Straightforward application of induction hypothesis to the premises.

Case DT-BRACKET-WHERE: Straightforward application of induction hypothesis to the premises.

Case DT-BRACKET-VALUES: Straightforward application of induction hypothesis to the premises.

\[ \square \]

Lemma 17 (Non-Bracket values sent over a channel). Let $v$ be the well-typed non-bracket value sent over a channel. That is, for some $\Pi, p, pc$. $\Pi; \emptyset; p; pc \vdash v : \tau$. Then $\forall \Pi', p', pc'$. $\Pi' \vdash p' \triangleright pc'$. $\Pi'; \emptyset; p'; pc' \vdash v : \tau$.

Proof. Induction on the structure of values.

Case Unit: Given that $\Pi' \vdash q \triangleright pc'$. From DT-UNIT, we thus have $\Pi'; \emptyset; q; pc \vdash () : \text{unit}$.

Case Pair: Given $\Pi; \emptyset; p; pc \vdash \langle v_1, v_2 \rangle : \tau_1 \times \tau_2$ for all $\Pi$ such that $\Pi \vdash p \triangleright pc$. Inverting DT-PAIR, we have $\Pi; \emptyset; q; pc \vdash v_1 : \tau_1$. Also given that $\Pi' \vdash p' \triangleright pc'$. Applying induction hypothesis, we have $\Pi'; \emptyset; p'; pc' \vdash v_1 : \tau_1$. From DT-PAIR, we thus have all premises to prove $\Pi'; \emptyset; q; pc' \vdash \langle v_1, v_2 \rangle : \tau_1 \times \tau_2$.

Case Del: Similar to the above case.

Case Inj: Similar to the above case.

Case Seal: Similar to the above case.

Case Where: Given $\Pi; \emptyset; pl; pc \vdash v$ where $v' : \tau$. Inverting DT-WHERE, we have:

$$\Pi; \Gamma; pl; pc \vdash v' : (p \triangleright q)$$

(56)

$$\Pi; (p \triangleright q); \Gamma; pl; pc \vdash v : \tau$$

(57)

$$\Pi \vdash \triangleright pl \triangleright pc$$

(58)

$$\Pi \vdash \triangleright pl \triangleright \triangleright q$$

(59)

$$\Pi \vdash pl \triangleright pc$$

(60)

We have to prove that

$$\Pi'; \emptyset; p' ; pc' \vdash v \text{ where } v' : \tau$$

Applying induction hypothesis to the first 2 premises, we have:

$$\Pi'; \Gamma; p'; pc' \vdash v' : (p \triangleright q)$$

(61)

$$\Pi' ; (p \triangleright q); \Gamma; p'; pc' \vdash v : \tau$$

(62)

(63)
We now argue that the following holds.

$$\Pi' \vdash \nabla (p^-) \Rightarrow \nabla (q^-)$$

Recall that the value $v$ sent over a channel is closed over required delegations and so $\emptyset \vdash \nabla (p^-) \Rightarrow \nabla (q^-)$ holds. Extending delegation context, we have the above premise. Hence the proof.

**Case Lam:** Given $\Pi; \emptyset; p; \pi \vdash \lambda (x : \tau) [\pi e', \Theta''', \eta, \Pi', \lambda]$, $e \vdash p \Rightarrow \pi e'$ for all $\Pi$ such that $\Pi \vdash p \Rightarrow \pi$. Inverting DT-LAM, we have

$$\forall p' \in \pi \Pi', x : \tau; \Theta''', p' : \pi e' \vdash e : \tau$$

We are also given that $\Pi' \vdash q \Rightarrow \pi e'$. Combining this with above, from DT-LAM, we thus have all premises to prove $\Pi' ; \emptyset; q; \pi e' \vdash \lambda (x : \tau) [\pi e'', \Theta''', \pi, \Pi', \lambda], e \vdash p \Rightarrow \pi e''$.

**Case TLam:** Similar to above.

□

Using the above lemmas, we are now ready to prove type preservation for a process.

**Lemma 18** (Type Preservation for a Process). Let $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash e_j : e_j$ for every $j \in \{1, \ldots, m\}$. If $p_j, D_j \vdash e_j \rightarrow e'_j$ such that for every $(p \gg q) \in \Pi_j$, $(p \gg q) \in D_j$ then $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash e'_j : \tau_j$

Proof. We prove by induction on the step relation $p_j, D_j \vdash e_j \rightarrow e'_j$. We only prove interesting cases. Remaining cases (DE-App', DE-Tapp', DE-Case', DE-UniM', DE-BindM', DE-AsseM', DE-Send') follow from straightforward application of induction hypothesis to the premises.

**Case DE-App:** Given $e_j = (\lambda (x : \tau) [\pi e', \Theta, \Pi, \lambda]; e) \ v$ and $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash (\lambda (x : \tau) [\pi e', \Theta, \Pi, \lambda]; e) \ v : \tau_2$. From the premises we have,

$$\Pi_j; \Gamma_j; x : \tau'; \Theta_j; p_j; \pi_j \vdash e : \tau_j$$

Since $\Pi_j \vdash p_j \gg \pi_j$, invoking PC reduction (Lemma 11, we have that $\Pi_j; \Gamma_j; x : \tau'; \Theta_j; p_j; \pi_j \vdash e : \tau_j$.

We have $e'_j = e [x/v]$. Invoking Lemma 14, we have $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash e [x/v] : \tau_j$.

**Case DE-Tapp:** Similar to above case.

**Case DE-Case1:** Given

$$e_j = \text{case}(\text{inj}_j v) \ of \ \text{inj}_j (x) \ e_j 1 \ | \ \text{inj}_j (x) \ e_j 2$$

and $e'_j = e_j [x/v]$. Also,

$$\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash \text{case}(\text{inj}_j v) \ of \ \text{inj}_j (x) \ e_j 1 \ | \ \text{inj}_j (x) \ e_j 2 : \tau.\\text{From the premises we have,}$$

$$\Pi_j; \Gamma_j; x : \tau'; \Theta_j; p_j; \pi_j \vdash e : \tau'$$

$$\Pi_j; \Gamma_j; x : \tau'; \Theta_j; p_j; \pi_j \vdash e : \tau_j.\\text{From DT-Inj, we have}$$

Invoking Lemma 14, we have $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash e [x/v] : \tau_j$.

**Case DE-Case2:** Similar to above.

**Case DE-UniM:** Given $e_j = \eta_k v$ and $e'_j = \eta_k v$. Also, $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash \eta_k v : \ell \ \text{says} \ \tau.$ From the premises it follows that $\Pi_j; \Gamma_j; \Theta_j; p_j; \pi_j \vdash \eta_k v : \ell \ \text{says} \ \tau.\\text{From the premises we have the following:}$

$$\Pi; \Gamma; p; \pi \vdash \eta_k v : \ell \ \text{says} \ \tau'$$

$$\Pi; \Gamma; p; \pi \vdash \eta_k v : \ell \ \text{says} \ \tau$$

We have to prove that $\Pi; \Gamma; p; \pi \vdash \eta_k v : \ell \ \text{says} \ \tau$. Since we have that $\Pi \vdash p \gg \pi$, applying PC reduction (Lemma 11) to the premise (65), we have $\Pi; \Gamma; x : \tau'; \Theta; p; \pi \vdash e' : \tau$.

Invoking Lemma 14, we thus have $\Pi; \Gamma; p; \pi \vdash e' [x/v] : \tau$.

**Case DE-AsseM:** Given $e = \text{assume} v \ in \ e'$ and $e' = e' [x/v]$ Also, $\Pi; \Gamma; \Theta; p; \pi \vdash \text{assume} v \ in \ e' : \tau.\\text{From the premises, we have the following:}$

$$\Pi; \Gamma; p; \pi \vdash (p \gg q)$$

$$\Pi; (p \gg q); \Gamma; \Theta; p; \pi \vdash e' : \tau$$

$$\Pi \vdash p \gg \pi$$

$$\Pi \vdash \nabla (p^+) \gg \nabla (q^-)$$

We have $\Pi \vdash p \gg \pi$.
We need to prove:  
\[ \Pi; \Gamma; \Theta; p; \text{pc} \vdash e' \text{ where } v : \tau \]

Comparing with the given premises, we have all except (80).

\[ \Pi; \Gamma; \Theta; p; \text{pc} \vdash e : \tau \]

Since \text{pc} grows monotonically both inside and outside TEEs, we have that \text{pc} has more integrity than \text{pc}. And so, if \[ \Pi \vdash \text{pc} \gg \nabla(q) \] then \[ \Pi \vdash \text{pc} \gg \nabla(q) \]. We now argue that if \[ \Pi \vdash \text{pc} \gg \nabla(q) \] then \[ \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(q) \]. The argument is similar to proving delegation invariance in FLAC. We sketch the high-level details here: every delegation added to \( \Pi_{\text{inst}} \) requires that \text{pc} is the voice of the delegator. That is, if \( \Pi = \Pi_{\text{inst}} : (r \gg s) \), then it must be the case that the assign expression added the delegation after checking \( \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(s) \) (for some \text{pc}) which implies \( \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(s) \). Now let \( \Pi' = \Pi : (u \gg v) \). Following similar argument, we have that \( \Pi \vdash \text{pc} \gg \nabla(v) \). The derivation has two cases: \( \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(v) \) or \( \Pi_{\text{inst}} : (u \gg v) \vdash \text{pc} \gg \nabla(v) \). For the latter case, notice that \( \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(v) \) and \( \Pi_{\text{inst}} : (r \gg s) \vdash \text{pc} \gg \nabla(v) \), hence it must be the case that \( \Pi_{\text{inst}} \vdash s \gg \nabla(v) \). Using transitivity, we now have that \( \Pi_{\text{inst}} \vdash \text{pc} \gg \nabla(v) \). Thus, using induction on the delegation context it can be proved that if \( \Pi \vdash \text{pc} \gg \nabla(q) \) then \( \Pi \vdash \text{pc} \gg \nabla(q) \). This gives us the premise (80). Hence proved.

**Case DE-WHERE:** Given \( e = e \) where \( v \) and \( e' = e' \) where \( v \). Also, \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash e \) where \( v : \tau \). Let \( D \) be such that for every \( (p \gg q) \in \Pi, (p \gg q) \in D \). From DT-WHERE, we have that

\[ \Pi; \Gamma; \Theta; p; \text{pc} \vdash e : \tau \]

From DT-WHERE, we have \( p, D \cdot v \vdash e \rightarrow e' \). So, we have that \( v \in D \cdot v \) and \( v \in \Pi \cdot (a \gg b) \). Applying inductive to the premise (83), we have that \( \Pi : (a \gg b) ; \Gamma; \Theta; p; \text{pc} \vdash e : \tau \) and so \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash e' \) where \( v : \tau \)

**Case DE-TEE:** Given \( e = \text{TEE}^t \) and \( e' = \text{runTEE}^t \). Also given \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{TEE}^t \) and \( \text{runTEE}^t \). From DT-TEE, we trivially have that \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{runTEE}^t \); \( e : \text{unit}; \Box \). From the premises of DT-TEE, we have

\[ \Pi; \emptyset; \Theta; t; t^\tau; \text{pc}^\tau \vdash e : \tau \]

From the premises of DE-TEE, we have

\[ t, D \vdash e \rightarrow e' \]

Applying induction hypothesis to the premise, we have

\[ \Pi; \emptyset; \Theta; t; t^\tau; \text{pc}^\tau \vdash e' : \tau \]

**Hence** \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{runTEE}^t \); \( e' : \tau \)

**Case DE-TEE:** Given \( e = \text{TEE}^t \) and \( e' = \Box; \emptyset \). Also given \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{TEE}^t \) and \( \Box; \emptyset \). From DT-UNIT and DT-HALT, we have that \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \Box; \emptyset \); \( \text{unit}; \emptyset \). Thus \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash e' : \text{unit}; \emptyset \).

**Case DE-SEND:** Given \( p, D \vdash (p, \text{send } v \rightarrow v' \text{ then } e_{\text{sn}}) \rightarrow (p, \text{send } v \rightarrow v' \text{ then } e_{\text{sn}}; \Box) \) where \( v' = \text{export}_\text{del}(D, v) \). Also, \( \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{send } v \rightarrow v' \text{ then } e_{\text{sn}}; \Box \). We need to prove that

\[ \Pi; \Gamma; \Theta; p; \text{pc} \vdash \text{send } v \rightarrow v' \text{ then } e_{\text{sn}}; \Box \]
Inverting the premises of DT-SEND, we have that
\[
\Pi; \Gamma; \Theta; pl; pc \vdash ch : \text{chan}_{p \to q} \quad (86)
\]
\[
\Pi; \Gamma; \Theta; pl; pc \vdash v : \tau \\
\Pi; \Gamma; \Theta; pl; pc \vdash v : \tau \\
\Pi; \Gamma; p; pc' \vdash e_{\kappa} : \tau' \\
\tau' \in \{ \tau', \tau' \} \\
\Pi \vdash pc \subseteq pc_{ch} \\
\Pi \vdash pc_{ch} \subseteq pc' \\
\Pi \vdash pc' \leq \tau' \\
\Pi \vdash p \succ pc \\
\Pi \vdash p \succ pc_{ch} \\
\]

To prove what we want, we have all the required premises except
\[
\Pi; \emptyset; \emptyset; p; pc \vdash v' : \tau
\]
Since \text{export}_{D}(D, v) closes over required delegations, \(v'\) requires no further assumptions on delegations. We still need to prove that the variable typing context can be empty. Because of substitution semantics, if we start with a closed program then an expression that gets evaluated to a value is closed as well (Lemma 6). Hence proved.

**Case DE-HALT**: Applying induction hypothesis to the premise, we have the conclusion.

**Case DE-HALT'**: Given \(e = v; \square; \square\) and \(e' = v; \square\). From DT-HALT, the type of \(v; \square\) is \(\tau; \square\). From DT-HALT2, the type of \(e\) is thus \(\tau; \square\). We already have that the type of \(e'\) is also \(\tau; \square\). Hence proved.

**Case DW-APP**: Given \(e = (v \text{ where } \langle p \succ q \rangle)\ \text{and} \ e' = (v \hspace{1em} v') \text{ where } \langle p \succ q \rangle\). We have to prove that
\[
\Pi; \Gamma; \Theta; PC ; pl \vdash (v \hspace{1em} v') \text{ where } \langle p \succ q \rangle : \tau
\]
From DT-APP we have:
\[
\Pi; \Gamma; \Theta; pl; pc \vdash v \text{ where } \langle p \succ q \rangle : \tau_1 \xrightarrow{\text{pc', \Theta', p, \Pi_3}} \tau \\
\Pi; \Gamma; \Theta; pl; pc \vdash v' : \tau_1 \\
\Pi \vdash pc \subseteq pc' \\
\Pi \vdash pl \in \mathcal{P} \\
\Theta \mid \text{dom}(\Theta') = \Theta' \\
\forall (p \succ q) \in \Pi'. \Pi \vdash p \succ q \\
\Pi \vdash pl \succ pc \\
\]
Rule DT-WHERE gives us the following:
\[
\Pi; \Gamma; \Theta; p; pc \vdash \langle p \succ q \rangle : \langle p \succ q \rangle \\
\Pi \vdash pc \succ \nabla(q) \\
\Pi \vdash \nabla(p^{-}) \succ \nabla(q^{-}) \\
\Pi \cdot \langle p \succ q \rangle; \Gamma; \Theta; p; pc \vdash v : \tau_1 \xrightarrow{\text{pc', \Theta', p, \Pi_3}} \tau \\
\]
We now want to show that \(e'\) is well typed via DT-WHERE. The key premise is to show that the subexpression \((v \hspace{1em} v')\) is well-typed via DT-APP. That is,
\[
\Pi \cdot \langle p \succ q \rangle; \Gamma; \Theta; p; pc \vdash v' : \tau \\
\]
Applying Lemma 10 (extending assumptions for terms) to (96) and applying Lemma 8 to (97), as well as using premises from (106) to (101), we have
\[
\Pi \cdot \langle p \succ q \rangle; \Gamma; \Theta; p; pc \vdash v' : \tau_1 \\
\Pi \cdot \langle p \succ q \rangle \vdash pc \subseteq pc' pl \in \mathcal{P} \\
\Theta \mid \text{dom}(\Theta') = \Theta' \\
\forall (a \succ b) \in \Pi'. \Pi \cdot \langle p \succ q \rangle \vdash a \succ b \\
\Pi \cdot \langle p \succ q \rangle \vdash pl \succ pc \\
\]
\[
33
\]
Combining with (105), we have (106). Remaining clauses, that is, (102), (103), and (104), give:

\[ \Pi; \Gamma; \Theta; p; pc \vdash (v \ v') \text{ where } (p \gg q) : \tau \]

**Case DW-TAPP:** Given \( e = (v \text{ where } (p \gg q)) \) and \( e' = (v \tau') \text{ where } (p \gg q) \). We have to prove that

\[ \Pi; \Gamma; \Theta; p; pc \vdash (v \tau') \text{ where } (p \gg q) : \tau[X \mapsto \tau'] \]

From DT-TAPP we have:

\[ \Pi; \Gamma; \Theta; p; pc \vdash v \text{ where } (p \gg q) : \forall X[pc', \Theta', \mathcal{P}, \Pi']. \tau \]

\[ \Pi \vdash pc \sqsubseteq pc' \]  

(112)  

(113)

Rule DT-WHERE gives us the following:

\[ \Pi; \Gamma; \Theta; p; pc \vdash (p \gg q) : (p \gg q) \]

\[ \Pi \vdash pc \gg \nabla(q) \]  

\[ \Pi \vdash \nabla(p^-) \gg \nabla(q^-) \]  

\[ \Pi \cdot (p \gg q); \Gamma; \Theta; p; pc \vdash v: \forall X[pc', \Theta', \mathcal{P}, \Pi']. \tau \]

(114)  

(115)  

(116)  

(117)

We now want to show that \( e' \) is well-typed via DT-WHERE. The key premise is to show that the subexpression \((v \tau')\) is well-typed via DT-TAPP. That is,

\[ \Pi \cdot (p \gg q); \Gamma; \Theta; p; pc \vdash \tau[X \mapsto \tau'] \]

(118)

Applying Lemma 10 (extending assumptions for terms) to (113), we have:

\[ \Pi \cdot (p \gg q) \vdash pc \sqsubseteq pc' \]

(119)

Combining with (117), we have (118). Remaining clauses, that is, (114), (115), and (116), give

\[ \Pi; \Gamma; \Theta; p; pc \vdash (v \tau') \text{ where } (p \gg q) : \tau[X \mapsto \tau'] \]

**Case DW-UNPAIR:** Given \( e = \text{proj}_i((v_1, v_2) \text{ where } (p \gg q)) \) and \( e' = (\text{proj}_i(v_1, v_2)) \text{ where } (p \gg q) \). We have to prove that

\[ \Pi; \Gamma; \Theta; p; pc \vdash (\text{proj}_i(v_1, v_2)) \text{ where } (p \gg q) : \tau_i \]

From DT-UNPAIR, we have:

\[ \Pi; \Gamma; \Theta; p; pc \vdash (v_1, v_2) \text{ where } (p \gg q) : \tau_1 \times \tau_2 \]

(120)

From (120) and DT-WHERE, we have:

\[ \Pi; \Gamma; \Theta; p; pc \vdash (p \gg q) : (p \gg q) \]

\[ \Pi \vdash pc \gg \nabla(q) \]

\[ \Pi \vdash \nabla(p^-) \gg \nabla(q^-) \]

\[ \Pi \cdot (p \gg q); \Gamma; \Theta; p; pc \vdash v_1, v_2 : \tau_1 \times \tau_2 \]

(121)  

(122)  

(123)  

(124)

From (124) and DT-UNPAIR, we have: \( \Pi \cdot (p \gg q); \Gamma; \Theta; p; pc \vdash \text{proj}_i(v_1, v_2) : \tau_i \). Combining with remaining premises ((121) to (124)) we have \( \Pi; \Gamma; \Theta; p; pc \vdash \text{proj}_i((v_1, v_2)) \text{ where } (p \gg q) : \tau_i \)

**Case DW-CASE:** Given \( e = \text{case } (v \text{ where } (p \gg q)) \text{ of } \text{inj}_1(x).e_1 | \text{inj}_2(x).e_2 \text{ and } e' = (\text{case } v \text{ of } \text{inj}_1(x).e_1 | \text{inj}_2(x).e_2) \text{ where } (p \gg q) \). We have to prove that

\[ \Pi; \Gamma; \Theta; p; pc \vdash (\text{case } v \text{ of } \text{inj}_1(x).e_1 | \text{inj}_2(x).e_2) \text{ where } (p \gg q) : \tau \]

From DT-CASE we have:

\[ \Pi; \Gamma; \Theta; p; pc \vdash v \text{ where } (p \gg q) : \tau_1 + \tau_2 \]

\[ \Pi \vdash pc \sqsubseteq \ell \leq \tau \]

(125)  

(126)  

(127)

\[ \Pi; \Gamma; x: \tau_1; \Theta; p; pc \sqsubseteq \ell \vdash e_1 : \tau \]

(128)

\[ \Pi; \Gamma; x: \tau_2; \Theta; p; pc \sqsubseteq \ell \vdash e_2 : \tau \]

(129)
From (125) and rule DT-WHERE, we get the following:

\[ \Pi; \Gamma; \Theta; p; v \vdash (p \Rightarrow q) : (p \Rightarrow q) \]  
\[ \Pi \vdash p \Rightarrow q \]  
\[ \Pi \vdash \nabla^{-1} q \]  

The key premise to prove is that:

\[ \Pi \cdot (p \Rightarrow q); \Gamma; \Theta; p; v \vdash \ell \text{ says } \tau \]

which follows from (133) and extending delegations in equations (126) to (129) (Lemma 10). Combining with remaining premises, we have

\[ \Pi; \Gamma; \Theta; p; v \vdash \ell \text{ says } \tau \]

**Case DW-UNITM**: Given \( e = \eta_e (v \text{ where } (p \Rightarrow q)) \) and \( e' = (\eta_e v) \text{ where } (p \Rightarrow q) \). We have to prove that

\[ \Pi; \Gamma; \Theta; p; \eta_e v \vdash \ell \text{ says } \tau \]

From DT-UNITM we have:

\[ \Pi; \Gamma; \Theta; p; v \vdash \ell \text{ says } \tau \]

From DT-WHERE, we have:

\[ \Pi; \Gamma; \Theta; p; v \vdash (p \Rightarrow q) : (p \Rightarrow q) \]  
\[ \Pi \vdash p \Rightarrow q \]  
\[ \Pi \vdash \nabla^{-1} q \]  

Combining with other premises ((135) to (138)), we have \( \Pi; \Gamma; \Theta; p; \eta_e v \vdash (p \Rightarrow q) : \ell \text{ says } \tau \).

**Case DW-BINDM**: Given \( e = \text{bind } x = (v \text{ where } (p \Rightarrow q)) \) in \( e \) and \( e' = (\text{bind } x = v \text{ in } e) \) where \( (p \Rightarrow q) \). We have to prove that

\[ \Pi; \Gamma; \Theta; p; v \vdash \ell \text{ says } \tau \]

From DT-BINDM we have:

\[ \Pi; \Gamma; \Theta; p; v \vdash (v \text{ where } (p \Rightarrow q)) : \ell \text{ says } \tau' \]  
\[ \Pi; \Gamma; x : \tau'; \Theta; p; v \vdash : \tau \]  
\[ \Pi \vdash \tau \subseteq \tau' \]

From (139) and rule DT-WHERE, we get the following:

\[ \Pi; \Gamma; \Theta; p; v \vdash (p \Rightarrow q) : (p \Rightarrow q) \]  
\[ \Pi \vdash p \Rightarrow q \]  
\[ \Pi \vdash \nabla^{-1} q \]  

We now want to show that \( e' \) is well typed via DT-WHERE. That is, we need the following premises,

\[ \Pi; \Gamma; \Theta; p; v \vdash \text{bind } x = v \text{ in } e : \tau \]  
\[ \Pi; \Gamma; \Theta; p; v \vdash (p \Rightarrow q) : (p \Rightarrow q) \]  
\[ \Pi \vdash p \Rightarrow q \]  

Extending the delegation context (Lemma 10) in the premises (140), (141) and from (145) we have (146).
We already have (147) from (142); (148) from (143); (149) from (144). Combining, we have

\[ \Pi; \Gamma; \Theta; p; pc \vdash (\text{bind } x = v \text{ in } e) \text{ where } \langle p \triangleright q \rangle : \tau \]

**Case DW-ASSUME:** Given \( e = \text{assume } v \text{ where } \langle a \triangleright b \rangle \text{ in } e \) and \( e' = \text{assume } v \text{ in } e \text{ where } \langle a \triangleright b \rangle \). From DT-ASSUME, we have

\[ \Pi; \Gamma; \Theta; p; pc \vdash v \text{ where } \langle a \triangleright b \rangle : (r \triangleright s) \]

(150)

\[ \Pi \cdot \langle r \triangleright s \rangle; \Gamma; \Theta; p; pc \vdash e : \tau \]

(151)

\[ \Pi \vdash pc \triangleright \nabla(s) \]

(152)

\[ \Pi \vdash \nabla(r^-) \triangleright \nabla(s^-) \]

(153)

Expanding the first premise using DT-WHERE, we have

\[ \Pi; \Gamma; \Theta; p; pc \vdash \langle a \triangleright b \rangle : (a \triangleright b) \]

(154)

\[ \Pi \cdot \langle a \triangleright b \rangle; \Gamma; \Theta; p; pc \vdash v : (r \triangleright s) \]

(155)

\[ \Pi \vdash pc \triangleright \nabla(b) \]

(156)

\[ \Pi \vdash \nabla(a^-) \triangleright \nabla(b^-) \]

(157)

We want to show

\[ \Pi; \Gamma; \Theta; p; pc \vdash \langle a \triangleright b \rangle : (a \triangleright b) \]

(158)

\[ \Pi \cdot \langle a \triangleright b \rangle; \Gamma; \Theta; p; pc \vdash \text{assume } v \text{ in } e : \tau \]

(159)

\[ \Pi \vdash pc \triangleright \nabla(b) \]

(160)

\[ \Pi \vdash \nabla(a^-) \triangleright \nabla(b^-) \]

(161)

Extending delegation context (Lemma 10) in the premises (151), (152), (157) and combining with topmost premise we have the (159). Remaining premises follow from (154), (156) and (157).

**Case DW-TEEG:** Given \( e = \text{runTEE}^\dagger (\hat{v} \text{ where } \langle a \triangleright b \rangle) \) and \( e' = (\text{runTEE}^\dagger \hat{v}) \text{ where } \langle a \triangleright b \rangle \). Also given \( \Pi; \Gamma; \Theta; p; pc \vdash e : \text{unit} \). From DT-TEEG, we have

\[ \Pi; \emptyset; \Theta'; t; t^- \land pc^- \vdash \hat{v} \text{ where } \langle a \triangleright b \rangle : \tau \]

\( \tau \in \{ \text{unit}, \text{unit} \} \)

(162)

(163)

\[ C = \{ ch | \Theta(ch) = \text{chan}_{t=p} pc' \triangleright \land \Pi \vdash pc \sqsubseteq pc' \} \]

(164)

(165)

(166)

Expanding the first premise using DT-WHERE, we have

\[ \Pi; \emptyset; \Theta'; t; pc^- \land t^- \vdash \hat{v} \text{ where } \langle a \triangleright b \rangle : \tau \]

(167)

(168)

(169)

(170)

(171)

To prove \( \Pi; \Gamma; \Theta; p; pc \vdash e' : \text{unit} \), we need the following premises:

\[ \Pi; \Gamma; \Theta; p; pc \vdash \langle a \triangleright b \rangle : (a \triangleright b) \]

(172)

(173)

(174)

(175)

(176)

(172) follows from (166) using DT-UNIT. To prove (173), we need the following premises

\[ \Pi; \emptyset; \Theta''; t; t^- \land pc^- \vdash \hat{v} : \tau \]

(177)

\[ C' = \{ ch | \Theta'(ch) = \text{chan}_{t=p} pc' \triangleright \land \Pi \cdot \langle a \triangleright b \rangle \vdash pc \sqsubseteq pc' \} \]

(178)

(179)

(180)
Invoking lemma that values do not depend on the channel environment (Lemma 12) on premise (168), we have (177). From premises (178) to (180) we thus have the premise (173). Weakening the delegation context in (165), we have (180). Premises (174) and (175) follow from (169) and (170). Hence proved.

Case DW-HALT: Given \( e = p, D \vdash (\hat{v} \text{ where } \langle a \gg b \rangle); \square \) and \( e' = p, D \vdash (\hat{v}; \square \text{ where } \langle a \gg b \rangle) \). Straight forward proof.

Case BD-STEP: Given \( e = (e_1 | e_2) \) and \( e' = (e_1' | e_2') \). Also \( \Pi; \Gamma; \Theta; \rho; \pi \vdash (e_1 | e_2) : \tau \). We have to prove

\[
\Pi; \Gamma; \Theta; \rho; \pi \vdash (e_1' | e_2') : \tau
\]  

Without loss of generality, let \( i = 1 \). Thus from the premises of BD-STEP, we have \( e_1 \rightarrow e_1' \) and \( e_2' = e_2 \). Since, values cannot take a step, inverting the well-typedness of bracket is only possible through DT-BRACKET and not through DT-BRACKET-VALUES or DT-BRACKET-WHERE. From DT-BRACKET, we have

\[
\Pi; \Gamma; \Theta; \rho; \pi \vdash (e_1' | e_2') : \tau
\]  

Since (181) holds, applying induction to the premise \( e_1 \rightarrow e_1' \), we have that \( \Pi; \Gamma; \Theta; \rho; \pi \vdash e_1' : \tau \). Combining with remaining premises ((182) to (184)) we thus have that \( \Pi; \Gamma; \Theta; \rho; \pi \vdash (e_1' | e_2') : \tau \).

Case BD-APP: Invalid case as DP-FUN is removed and so a bracket of function values is no longer well-typed.

Case BD-TAPP: Invalid case as DP-TFUn is removed and so a bracket of type abstraction values is no longer well-typed.

Case BD-UNPAIR: Given \( e = \text{proj}_i, \langle (v_{11}, v_{12}) | (v_{21}, v_{22}) \rangle \) and \( e' = (v_{1i} | v_{2i}) \) Also \( \Pi; \Gamma; \Theta; \rho \vdash \text{proj}_i (\langle (v_{11}, v_{12}) | (v_{21}, v_{22}) \rangle) : \tau_i \). We have to prove

\[
\Pi; \Gamma; \Theta; \rho \vdash (v_{1i} | v_{2i}) : \tau_i
\]  

From DT-UNPAIR, we have:

\[
\Pi; \Gamma; \Theta; \rho \vdash (v_{11}, v_{12}) | (v_{21}, v_{22}) : \tau_1 \times \tau_2
\]  

Since they are already values, they can be inverted using either DT-BRACKET-VALUES or DT-BRACKET-WHERE. We first use the former. Proof for the latter is similar.

\[
\Pi; \Gamma; \Theta; \rho \vdash (v_{11}, v_{12}) : \tau_1 \times \tau_2
\]  

\[
\Pi; \Gamma; \Theta; \rho \vdash (v_{21}, v_{22}) : \tau_1 \times \tau_2
\]  

\[
\Pi \vdash H^\pi \leq \tau_1 \times \tau_2
\]  

From (186), (187) and DT-UNPAIR, we have \( \Pi; \Gamma; \Theta; \rho \vdash v_{1i} : \tau_i \) and \( \Pi; \Gamma; \Theta; \rho \vdash v_{2i} : \tau_i \) for \( i = \{1, 2\} \). From (188) and DP-PAIR, we have \( \Pi \vdash H^\pi \leq \tau_i \). Combining with other premises, from DT-BRACKET-VALUES, we have \( \Pi; \Gamma; \Theta; \rho \vdash (v_{11} | v_{21}) : \tau_i \).

Case BD-BINDM1: Invalid case as the given bracket expression isn’t well-typed.

Case BD-BINDM3: Given \( e = \text{bind} \ x = (\eta_\ell, v_1 | \eta_\ell, v_2) \) in \( e \). Here \( e' = ([e]_1[x \mapsto v_1] | [e]_2[x \mapsto v_2]) \). Also \( \Pi; \Gamma; \Theta; \rho \vdash \text{bind} \ x = (\eta_{\ell_1}, v_1 | \eta_{\ell_2}, v_2) \) in \( e ; \tau \). From DT-BINDM, we have

\[
\Pi; \Gamma; \Theta; \rho \vdash (\eta_{\ell_1}, v_1 | \eta_{\ell_2}, v_2) : \ell \text{ says } \tau'
\]  

\[
\Pi; \Gamma; x : \tau' ; \Theta; \rho ; \pi \sqcup \ell \vdash e : \tau
\]  

\[
\Pi \vdash \pi \sqcup \ell \leq \tau
\]  

\[
\Pi \vdash p \gg \pi
\]  

From (189) and DT-BRACKET-VALUES, we have

\[
\Pi; \Gamma; \Theta; \rho \vdash \eta_{\ell_1}, v_1 : \ell \text{ says } \tau'
\]  

\[
\Pi; \Gamma; \Theta; \rho \vdash \eta_{\ell_2}, v_2 : \ell \text{ says } \tau'
\]  

\[
\Pi \vdash H^\pi \sqsubseteq \ell
\]  

We have to prove that

\[
\Pi; \Gamma; \Theta; \rho ; \pi \vdash ([e]_1[x \mapsto v_1] | [e]_2[x \mapsto v_2]) : \tau
\]
For some \( \hat{\pi} \) we need the following premises to satisfy DT-BRACKET:

\[
\Pi; \Gamma; \Theta; p; \hat{\pi} \vdash e[x \mapsto v_1]: \tau \\
\Pi; \Gamma; \Theta; p; \hat{\pi} \vdash e[x \mapsto v_2]: \tau \\
\Pi \vdash (H^\pi \sqcup \pi) \subseteq \hat{\pi} \\
\Pi \vdash H^\pi \leq \tau
\]

A natural choice for \( \hat{\pi} \) is \( \pi \sqcup \ell \). From (190)

\[
\Pi; \Gamma, x: \tau'; \Theta; p; \hat{\pi} \vdash e: \tau
\]

Invoking clearance lemma (Lemma 7) on the premise (190) we have that

\[
\Pi \vdash p \succ \pi \sqcup \ell
\]

From the lemma that non-bracketed values can be typed under any \( \pi \) such that they are bound by the place’s authority (Lemma 13), we have

\[
\Pi; \Gamma; \Theta; p; \hat{\pi} \vdash v_1: \tau'
\]

Lemma 15 (bracket projection) and Lemma 14 (substitution), we have (196) and (197). From (195) and (191) we have (199).

Thus we have all premises necessary to prove \( \Pi; \Gamma; \Theta; p; \pi \vdash (\lambda e_1 [e_1[x \mapsto v_1] \mid e_2[x \mapsto v_2]) : \tau. \)

**Case BD-CASE:** Does not occur. Not well-typed.

**Case BD-ASSUME:** Does not occur. Not well-typed.

\[
\square
\]

Before we prove type preservation for the distributed system, we show that holes in the evaluation contexts can be typed under a suitable \( \pi \).

We express this using the relation.

\[
\Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \Rightarrow \Pi'; \Gamma'; \Theta'; p'; \pi' \vdash e: \tau'
\]

Figure 24 defines the relation using inference rules. The following lemma proves that every hole is well-typed under some \( \pi \).

**Lemma 19** (Hole has a Suitable \( \pi \)). If \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \) then \( \exists \Pi', \Gamma', \pi', \pi' \tau' \) such that \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \Rightarrow \Pi'; \Gamma'; \Theta'; \pi'; \pi' \tau' \) and \( \Pi' \vdash \dom(\pi') = \Pi. \)

**Proof.**

The following two lemmas prove that channel environment can be extended for a well-typed expression and context.

**Lemma 20** (Weakening Channel Environment for Expressions). If \( \Pi; \Gamma; \Theta; p; \pi \vdash e: \tau \) then \( \Pi; \Gamma; \Theta'; p; \pi \vdash e: \tau \) such that \( \Theta' |_{\dom(\Theta)} = \Theta. \)

**Proof.** Straightforward induction on the typing judgement.

**Lemma 21** (Weakening Channel Env for Contexts). If \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \) then for all \( \Theta' \) such that \( \Theta' |_{\dom(\Theta)} = \Theta. \)

\[
\Pi; \Gamma; \Theta'; p; \pi \vdash T[e]: \tau.
\]

**Proof.** Straightforward invocation of Lemma 20.

**Lemma 22** (Hole Substitution). Let \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \) and \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e]: \tau \Rightarrow \Pi'; \Gamma'; \Theta'; p'; \pi' \vdash e: \tau'. \) If \( \Pi'; \Gamma'; \Theta'; p'; \pi' \vdash e': \tau' \) then \( \Pi; \Gamma; \Theta; p; \pi \vdash T[e']: \tau \).

**Proof.** Proof is by induction on the structure of \( W. \)

**Lemma 23** (Hole Substitution by Weakening Channel Env). Let \( \Pi; \Gamma; \Theta; p; \pi \vdash T[\text{spawn} @ q (pl, ch_1[pc_1; \tau_1, \ ch_2[pc_2; \tau_2]), \ e_1 \text{ then } e_2]: \tau \) Then \( \Pi; \Gamma; \Theta'; p; \pi \vdash T[e_2]: \tau \) where

\[
\Theta' = \Theta[\text{chan}_{p \rightarrow q} \ p c_1 \ \tau_1, \ \text{chan}_{p \rightarrow q} \ p c_2 \ \tau_2]
\]

**Proof.** Proof is by induction on the structure of \( W. \) (This is actually \( E \) since \( \text{spawn} \) doesn’t occur inside TEE).

**Case \( \text{[ \_ \_ \_ ]} \)** Given \( \Pi; \Gamma; \Theta; p; \pi \vdash [\text{spawn} @ q (pl, ch_1[pc_1; \tau_1, \ ch_2[pc_2; \tau_2]), \ e_1 \text{ then } e_2]: \tau \). Inverting using DT-HOLE and then using DT-SPAWN, we have that:

\[
\Pi; \Gamma; \Theta'; p; \pi \vdash e_2: \tau
\]

Using DT-HOLE, we have \( \Pi; \Gamma; \Theta'; p; \pi \vdash [e_2]: \tau \).

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Fig. 24: Extracting holes from well-typed contexts

Case \( e \): Given \( \Pi; \Gamma; p; pc \vdash T[\text{spawn } @ q (pl, ch1[pc1; \tau_1], ch2[pc2; \tau_2])], e_1 \text{ then } e_2 \vdash e; \tau \). Inverting using DT-APP, we have that:

\[
\Pi; \Gamma; \Theta; p; pc \vdash T[\text{spawn } @ q (pl, ch1[pc1; \tau_1], ch2[pc2; \tau_2])], e_1 \text{ then } e_2 \vdash \tau \vdash \Pi; \Theta; p; pc \vdash e; \tau
\]

Applying induction hypothesis to (201), we have

\[
\Pi; \Gamma; \Theta; p; pc \vdash T[e_2] : \tau_1 \vdash \Pi; \Theta; p; pc \vdash e; \tau_1
\]
The condition $\Theta' |_{\text{dom}(\Theta')} = \Theta''$ still holds. Thus from DT-APP, we have

$$\Pi; \Gamma; \Theta'; p; pc \vdash T[e] : \tau$$

**Case** $v : W$: Straight forward application of induction hypothesis.

**Case** $\langle W, e \rangle$: Straight forward application of induction hypothesis.

**Case** $\langle v, W \rangle$: Straight forward application of induction hypothesis.

**Case** $\text{proj}_1$ $W$: Straight forward application of induction hypothesis.

**Case** $\text{inj}_1 W$: Straight forward application of induction hypothesis.

**Case** $\eta_p W$: Straight forward application of induction hypothesis.

**Case** $\text{bind} x = W \text{ in } e$: Straight forward application of induction hypothesis.

**Case** $W \tau$: Straight forward application of induction hypothesis.

**Case** $\text{assume } W \text{ in } e$: Straight forward application of induction hypothesis.

**Case** $\text{send } \nu W \text{ then } e_{rk}$: Straight forward application of induction hypothesis.

**Case** $\text{case } W \text{ of } \text{inj}_1(x), e_1 | \text{inj}_2(x), e_2$: Straight forward application of induction hypothesis.

**Case** $W \text{ where } v$: Straight forward application of induction hypothesis.

**Case** $\text{runTEE'} W$: Syntactically not possible.

\[ \square \]

**Lemma 24** (High Contexts without TEEs have High Holes). Let $\Pi; \Gamma; \Theta; p; pc \vdash E[e] : \tau \triangleright \Pi'; \Gamma'; \Theta'; p'; pc' \vdash e : \tau'$ and $T$ be the set of TEE principals in $W$. If $\exists pc_H$, we have

1. $\Pi \vdash H^\pi \cup pc \subseteq pc_H$
2. $\Pi; \Gamma; \Theta; p; pc_H \vdash T[e] : \tau$
3. $\Pi \vdash H^\pi \leq \tau$

then $\exists pc'_H$ such that

1. $\Pi' \vdash H^{\pi'} \cup pc' \subseteq pc'_H$
2. $\Pi'; \Gamma'; \Theta'; p'; pc'_H \vdash e : \tau'$
3. $\Pi' \vdash H^{\pi'} \leq \tau'$

**Proof.** Proof by induction on the relation $\Pi; \Gamma; \Theta; p; pc \vdash E[e] : \tau \triangleright \Pi'; \Gamma'; \Theta'; p'; pc' \vdash e : \tau'$.

**Case** $\text{T-HOLE}$: Straightforward as $pc' = pc$. Choose $pc'_H = pc_H$.

**Case** $\text{T-LAM}$: Straightforward application of induction hypothesis.

**Case** $\text{T-APP}$: Straightforward application of induction hypothesis.

**Case** $\text{T-ARG}$: Straightforward application of induction hypothesis.

**Case** $\text{T-PAIR-LEFT}$: Straightforward application of induction hypothesis.

**Case** $\text{T-PAIR-RIGHT}$: Straightforward application of induction hypothesis.

**Case** $\text{T-INJ}$: Straightforward application of induction hypothesis.

**Case** $\text{T-UNITM}$: Straightforward application of induction hypothesis.

**Case** $\text{T-RUNTEE}$: Does not occur syntactically.

**Case** $\text{T-BINDM}$: Straightforward application of induction hypothesis.

**Case** $\text{T-ASSUME}$: Straightforward application of induction hypothesis.

**Case** $\text{T-SEND}$: Straightforward application of induction hypothesis.

**Case** $\text{T-CASE}$: Straightforward application of induction hypothesis.

**Case** $\text{T-WHERE}$: Applying induction to the premises, we have

\[
\begin{align*}
\Pi \cdot \langle r \triangleright s \rangle & \vdash H^\pi \cup pc \subseteq pc_H \land \\
\Pi \cdot \langle r \triangleright s \rangle \cdot \Gamma; \Theta; p; pc_H & \vdash E[e] : \tau \land \\
\Pi \cdot \langle r \triangleright s \rangle & \vdash H^\pi \leq \tau \\
\implies \\
\exists pc'_H. \Phi & \vdash H^{\pi'} \cup pc' \subseteq pc'_H \land \\
\Phi' \cdot \Gamma'; \Theta'; p'; pc'_H & \vdash e : \tau' \land \\
\Pi & \vdash H^{\pi'} \leq \tau' 
\end{align*}
\]

We are given that

\[
\begin{align*}
\Pi & \vdash H^\pi \cup pc \subseteq pc_H \land \\
\Pi & \vdash H^\pi \leq \tau
\end{align*}
\]

(203)
This means the delegation \( r \succ s \) is not necessary to derive the required flows-to elation. Hence proved the case.

We are now ready to prove the type preservation of a step in the system.

**Lemma 25** (Type Preservation for the Distributed System). Let \( \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, e'_i \rangle \parallel \cdots \parallel \langle p_m, e'_m \rangle \) such that \( n \geq m \) and \( k \in \{1, \ldots, m\} \). Also let,

\[
\forall \ell', \forall \{i, j\} \subseteq \{1, \ldots, m\}, \Pi_i \vdash H^\pi \subseteq \ell' \iff \Pi_j \vdash H^\pi \subseteq \ell'
\]

\[
\forall k \in \{1, \ldots, m\}, \forall \ell'. \Pi_k \vdash H^\pi \subseteq \ell' \iff \Pi_k \vdash H^\pi \subseteq \ell'\]

Then the following hold.

1. \( \Pi_k; \Gamma_k; \Theta_k; p_k; pc_k \vdash e'_k : \tau_k \) where \( \Theta_k \subseteq \Theta' \)
2. If \( n = m + 1 \), then \( \Pi_n; \emptyset; \emptyset; p_n; pc_n \vdash e'_n : \tau_n \) for some \( \Pi_n, \Theta_n', pc_n \) and \( \tau_n \)
3. If \( n = m + 1 \), then \( \forall \ell'. \Pi_n \vdash H^\pi \subseteq \ell' \iff \Pi_n \vdash H^\pi \subseteq \ell' \)
4. \( \forall \ell', \forall \{i, j\} \subseteq \{1, \ldots, n\}, \Pi_i \vdash H^\pi \subseteq \ell' \iff \Pi_j \vdash H^\pi \subseteq \ell' \)

**Proof.** Proof is by induction on the system step relation (\( \implies \)).

**PAR-STEP:** Given

\[
\Pi_1; \Gamma_1; \Theta_1; p_1; pc_1 \vdash e_1 : \tau_1 \parallel \cdots \parallel \Pi_m; \Gamma_m; \Theta_m; p_m; pc_m \vdash e_m : \tau_m
\]

From the premises of DT-SYS, we have that \( \Pi_k; \Gamma_k; \Theta_k; p_k; pc_k \vdash e_k : \tau_k \) for every \( k \in \{1, \ldots, m\} \).

Also given, \( \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_m, e'_m \rangle \). From the premises we have that \( p_1; \emptyset; e_1 \rightarrow e'_1 \) for some \( i = \{1, \ldots, m\} \). Thus we have \( n = m \) and \( e'_k = e_k \) for every \( k \in \{1, \ldots, m\} \setminus \{i\} \).

For \( k = i \), invoking type preservation for single process (Lemma 18) gives us that \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e'_i : \tau_i \). Thus the new configuration is well-typed by DT-SYS.

a) **Proof for remaining results:** Since \( n = m \), we trivially have the states resulted in 3 and 4.

**PAR-SPAWN:** Given

\[
\Pi_1; \Gamma_1; \Theta_1; p_1; pc_1 \vdash e_1 : \tau_1 \parallel \cdots \parallel \Pi_m; \Gamma_m; \Theta_m; p_m; pc_m \vdash e_m : \tau_m
\]

From the premises of DT-SYS, we have that \( \Pi_k; \Gamma_k; \Theta_k; p_k; pc_k \vdash e_k : \tau_k \) for every \( k \in \{1, \ldots, m\} \). Also given

\[
\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, T[spawn \; q \; (pl, ch_r[pc_r]; \tau_r], ch_s[pc_s]; \tau_s) \; e \; then \; e' \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, T[e'[\nu_r/\text{ch}_r]\nu_s/\text{ch}_s] \parallel \cdots \parallel \langle p_m, e_m \rangle \parallel \langle q, e[\nu_r/\text{ch}_r]\nu_s/\text{ch}_s \rangle \]

Here we have \( n \neq m \) and \( (e'_i, e'_n) = (T[e'], e) \). From DT-SPAWN, we have that \( \Pi_i; \emptyset; [ch_1 \rightarrow \text{chan}_q-p, pc_1 \tau_1, ch_2 \rightarrow \text{chan}_q-p pc_2 \tau_2]; q; pc' \vdash e : \tau' \) and thus \( pc_n = pc \). Let \( \Theta'_i = \Theta_i[ch_s \rightarrow \text{chan}_p-q pc_3 \tau_3, ch_r \rightarrow \text{chan}_p-q pc_4 \tau_4] \). It remains to show that

\[
\Pi_i; \Gamma_i; \Theta'_i; p_i; pc_i \vdash T[e']: \tau_i
\]

Invoking Lemma 19, we have \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[spawn \; q \; (pl, ch_r[pc_r]; \tau_r], ch_s[pc_s]; \tau_s) \; e \; then \; e' \; \tau_i \) \( \Pi_i; \Gamma_i; \Theta'_i; p'_i; pc' \vdash T[spawn \; q \; (pl, ch_r[pc_r]; \tau_r], ch_s[pc_s]; \tau_s) \; e \; then \; e' \; \tau' \). From DT-SPAWN, we have \( \Pi'_i; \Gamma'_i; \Theta'_i; p'_i; pc'_i \vdash e' \; \tau' \). Invoking hole substitution (Lemma 23), we thus have \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[e']: \tau_i \).

b) **Proof for remaining results:** Since \( n = m + 1 \), we have that \( \Pi_n = \Pi_i \) and so \( \Pi_n \subseteq \Pi_i \). Thus we have the results stated in condition 3 and 4.

**PAR-SEND-RECV:** Here \( n = m \). Without loss of generality, let \( (e_i, e_j) = (T[send \; \nu \; v \; then \; e_{\kappa_n}], T'[recv \; \nu \; as \; x \; in \; e]) \).

Then \( (e'_i, e'_j) = (T[e_{\kappa_n} \parallel \nu], T'[\nu \; \rightarrow \; v \; \parallel \nu]) \).

We thus have \( \Pi_k; \Gamma_k; \Theta_k; p_k; pc_k \vdash e'_k : \tau_k \) where \( \Theta_k = \Theta_k \) and \( e_k = e'_k \) for all \( k \in \{1, \ldots, m\} \setminus \{i, j\} \).

Given \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[send \; \nu \; then \; e_{\kappa_n}] : \tau_i \) \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[recv \; \nu \; as \; x \; in \; e] : \tau_j \). Since holes can be typed under some \( pc'_k \) (Lemma 19), we have that \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[send \; \nu \; \parallel \nu \; then \; e_{\kappa_n}] : \tau_i \parallel \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[recv \; \nu \; as \; x \; in \; e] : \tau_j \parallel \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T[recv \; \nu \; as \; x \; in \; e] : \tau_j \).

From DT-SENDDEL, we have that \( \Pi'_i; \emptyset; \emptyset; p'_i; pc'_i \vdash \tau \) for some \( \Pi'_i \) such that \( \Pi'_i \subseteq \Pi_i \) and \( \Pi'_i \parallel p'_i \parallel pc'_i \).

Since non-bracket values sent over a channel can be typed on other places such that \( \Pi'_j \parallel p'_j \parallel pc'_j \parallel \hat{v}_1 = \hat{v}_2 \). (Lemma 17), we have \( \Pi'_j; \emptyset; \emptyset; p'_j; pc'_j \vdash \hat{v}_1 \parallel \hat{v}_2 \; \tau \).

If \( \hat{v} = (\hat{v}_1 \parallel \hat{v}_2) \), a bracket value, then there are 2 cases.

**Invert using DT-BRACKET-VALUES:** We have that \( \Pi'_i \parallel H^\pi \leq \tau \). From the given conditions on the upward closure of \( H^\pi \), we have that \( \Pi'_j \parallel H^\pi \leq \tau \). Thus from DT-BRACKET-VALUES, we have \( \Pi'_j; \emptyset; \emptyset; p'_j; pc'_j \vdash (\hat{v}_1 \parallel \hat{v}_2); \tau \).

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**Invert using DT-BRACKET-WHERE:** We already have that \((\hat{v}_1 \mid \hat{v}_2)\) is well-typed on node \(p_i\). That is, \(\Pi_i' \vdash \hat{v}_1 : \tau\). And so,

\[
\Pi_i' \vdash \hat{v}_1 \mid \hat{v}_2 \quad \text{where} \quad \Pi_i' \vdash \hat{v}_1 : \tau \quad \text{(204)}
\]

\[
\Pi_i' \vdash \hat{v}_1 \quad \text{and} \quad \Pi_i' \vdash \hat{v}_2 : \tau \quad \text{(205)}
\]

\[
\Pi_i' \vdash H^\tau \leq \tau \quad \text{(206)}
\]

We need to prove the following:

\[
\Pi_j' \vdash \hat{v}_1 \quad \text{where} \quad \Pi_j' \vdash \hat{v}_1 : \tau \quad \text{(207)}
\]

\[
\Pi_j' \vdash \hat{v}_2 \quad \text{where} \quad \Pi_j' \vdash \hat{v}_2 : \tau \quad \text{(208)}
\]

\[
\Pi_j' \vdash H^\tau \leq \tau \quad \text{(209)}
\]

From the given conditions on the upward closure of \(H^\tau\), we have that \(\Pi_j' \vdash H^\tau \leq \tau\). Thus from DT-BRACKET-VALUES, we have \(\Pi_j' \vdash \hat{v}_1 \mid \hat{v}_2 \vdash \hat{v} : \tau\) (weakening delegation and variable contexts, channel environment by invoking Lemma 12, Lemma 10).

From DT-RECV, we have \(\Pi_j' ; \Gamma_j' ; x : \tau ; \Theta_j ; p_j ; pc_j' \vdash e_n : \tau_j\). Applying PC reduction (Lemma 11), we have that \(\Pi_j' ; \Gamma_j' ; x : \tau ; \Theta_j ; p_j ; pc_j' \vdash e_n : \tau_j\). Invoking hole substitution (Lemma 14), we have that \(\Pi_j' ; \Gamma_j' ; \Theta_j ; p_j ; pc_j' \vdash e_n[x \mapsto v]; \Box : \tau_j\). Invoking hole substitution (Lemma 22), on \(\Pi_j ; \Gamma_j ; \Theta_j ; p_j ; pc_j \vdash T[\text{recv } v \text{ as } x \in e; \Box] : \tau_j\), we have

\[
\Pi_j ; \Gamma_j ; \Theta_j ; p_j ; pc_j \vdash e_j' : \tau_j
\]

From DT-SENDDEL, we also have that \(\Pi_i' ; \Gamma_i' ; \Theta_i ; p_i ; pc_i' \vdash e_{sn} : \tau_i\). Applying PC reduction (Lemma 11), we have that \(\Pi_i' ; \Gamma_i' ; \Theta_i ; p_i ; pc_i' \vdash e_{sn} : \tau_i\). Invoking hole substitution (Lemma 22), on \(\Pi_i ; \Gamma_i ; \Theta_i ; p_i ; pc_i \vdash T[\text{send } v \text{ then } e_{sn}] : \tau_i\), we have

\[
\Pi_i ; \Gamma_i ; \Theta_i ; p_i ; pc_i \vdash e_i' : \tau_i
\]

c) **Proof for remaining results:** Since \(n = m\), we trivially have the results stated in 3 and 4.

**BD-PAR-STEP-L:** Given

\[
\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, (e_i1 \mid e_i2) \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \Rightarrow \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, e'_i \rangle \parallel \cdots \parallel \langle p_n, e'_n \rangle
\]

Let \(e_i = (e_{i1} \mid e_{i2})\). From the premises of BD-PAR-STEP, we have

\[
\langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_{i1} \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \Rightarrow \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, e'_{i1} \rangle \parallel \cdots \parallel \langle p_n, e'_{n} \rangle
\]

Also from the remaining premises we have

\[
i \in \{1 \cdots m\}
\]

\[
e_i' = (e_{i1}' \mid e_{i2}')
\]

\[
j = \begin{cases} n & \text{if } n \neq m \\ \in \{1 \cdots m\} & \text{otherwise} \end{cases}
\]

\[
e_i' = \begin{cases} e_i' & \text{if } n = m \text{ and } j = i \\ e_i' & \text{if } n \neq m \text{ and } j \neq i \\ \end{cases}
\]

\[
e_j' = \begin{cases} (e_{j1}' \mid e_{j2}') & \text{if } n = m, j \neq i \text{ and } e_j = (e_{j1} \mid e_{j2}) \\ T[e_{j1}' \mid e] & \text{if } n = m, j \neq i \text{ and } e_j = T[e] \end{cases}
\]

From DT-SYS, we have that

\[
\Pi_i ; \Gamma_i ; \Theta_i ; p_i ; pc_i \vdash e_i : \tau_i
\]

\[
\Pi_j ; \Gamma_j ; \Theta_j ; p_j ; pc_j \vdash e_j : \tau_j
\]

We have four cases.

**Case** \(n = m\) and \(j = i\) : Trivial case. Invoking type preservation for single process (Lemma 18), we get the conclusion.
d) Proof for remaining results: Since $n = m$, we trivially have the results stated in 3 and 4.

Case $n \neq m$ and $j \neq i : e_i$ is a spawn expression. That is, we have

$$
e_i = (T_i[sp\,e \triangleright q (pl, ch_r[pc_r ; \tau_r], ch_s[pc_s ; \tau_s]). e \triangleright e_n] \mid e_2)$$

$$
e'_i = (T_i[e_n] \mid e_2)$$

$$
e'_j = e'_n = (e \triangleright \bullet)$$

$$
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i : \tau_i
$$

(214)

(215)

Note that TEE’s do not have any spawn statement. Thus $T_i$ is actually $E_i$. That is,

$$
e_i = (E_i[sp\,e \triangleright q (pl, ch_r[pc_r ; \tau_r], ch_s[pc_s ; \tau_s]). e \triangleright e_n] \mid e_2)$$

$$
e'_i = (E_i[e_n] \mid e_2)$$

$$
e'_j = e'_n = (e \triangleright \bullet)$$

$$
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i : \tau_i
$$

(216)

(217)

From the DT-BRACKET (since it is an expression, we cannot invert using DT-BRACKET-VALUES or DT-BRACKET-WHERE), we have that $e_i$ is protected for some $pc'_i$ and so:

$$
\Pi_i \parallel (H^\pi \sqcup pc_i) \sqsubseteq pc'_i
$$

(218)

$$
\Pi_i; \Gamma_i; \Theta_i; p_i; pc'_i \vdash T_i[sp\,e \triangleright q (pl, ch_r[pc_r ; \tau_r], ch_s[pc_s ; \tau_s]). e \triangleright e_n] : \tau_i
$$

(219)

$$
\Pi_i; \Gamma_i; \Theta_i; p_i; pc'_i \vdash e_2 : \tau_i
$$

(220)

$$
\Pi_i \parallel H^\pi \leq \tau_i
$$

(221)

Invoking Lemma 19 on (219), we have $\Pi_i; \Gamma_i; \Theta_i; p_i; pc'_i \vdash e_1 : \tau_i \triangleright \Pi_i'; \Gamma_i'; \Theta_i'; p_i'; pc'_i \vdash sp\,e \triangleright q (pl, ch_r[pc_r ; \tau_r], ch_s[pc_s ; \tau_s]). e \triangleright e_n : \tau_i$. Inverting the above expression using DT-SPAWN, we have:

$$
\Pi_i'; \emptyset; \emptyset[ch_r \triangleright chan_{p \leftarrow q} pc_r \tau_r, \triangleright ch_s \triangleright chan_{p' \leftarrow \neg q} pc_s \tau_s]; q; pc'' \vdash e : \tau_n
$$

(222)

$$
\Pi_i'; \Gamma_i'; \Theta_i'[ch_r \triangleright chan_{p \leftarrow \neg q} pc_r \tau_r, \triangleright ch_s \triangleright chan_{p' \leftarrow q} pc_s \tau_s]; p_i'; pc' \vdash e : \tau_n
$$

(223)

$$
\Pi \parallel pc'' \sqsubseteq pc''
$$

(224)

$$
\Pi \parallel p_i'' \triangleright pc''
$$

(225)

$$
\Pi \parallel pc'' \leq \tau_n
$$

(226)

Invoking hole substitution (Lemma 23) with (223) we have

$$
\Pi_i; \Gamma_i; \Theta'_i; p_i; pc'_i \vdash T_i[e_n] : \tau_i
$$

(227)

where $\Theta'_i = \Theta_i[ch_r \triangleright chan_{p \leftarrow q} pc_r \tau_r, \triangleright ch_s \triangleright chan_{p' \leftarrow q} pc_s \tau_s]$. Weakening channel environment on (223) using Lemma 20, we have Thus, from DT-BRACKET, we have that

$$
\Pi_i; \Gamma_i; \Theta'_i; p_i; pc'_i \vdash (T_i[e_n] \mid e_2) : \tau_i
$$

We still need to show that for some $\Pi_n$, $\Gamma_n$, $\Theta_n$, $pc_n$ and $\tau_n$, the following holds.

$$
\Pi_n; \Gamma_n; \Theta_n; q; pc_n \vdash (e \mid \bullet) : \tau_n
$$

We need the following premises to prove that $e'_j$ is well-typed.

$$
\Pi'_i \parallel (H^\pi \sqcup pc'' \sqsubseteq pc''_H
$$

(227)

$$
\Pi'_i; \emptyset; \emptyset[q; pc''_H \vdash e : \tau_n
$$

(228)

$$
\Pi'_i \parallel H^\pi \leq \tau_n
$$

(229)

Since high contexts (without TEE’s) have high holes, applying Lemma 24 on (218), we have that

$$
\exists pc''_H. \Pi'_i \parallel H^\pi \sqsubseteq pc'' \sqsubseteq pc''_H
$$

(230)

$$
\Pi_i'; \Gamma_i'; \Theta'_i; p_i'; pc'_i \vdash sp\,e \triangleright q (pl, ch_r[pc_r ; \tau_r], ch_s[pc_s ; \tau_s]). e \triangleright e_n : \tau
$$

(231)

$$
\Pi_i' \parallel H^\pi \leq \tau
$$

(232)
Applying induction to the step relation in the premise:

\[ \Pi' \vdash \emptyset ; \Theta' ; q ; \Phi'' \vdash e : \tau_n \]  

(233)

\[ \Pi \vdash \Phi'' \subseteq \Phi' \]  

(234)

Premise (227) follows from (230) and (234). Premise (229) follows from (226). Premise (228) follows from (233). We thus have that \((e \mid \bullet)\) is well-typed.

The new configuration is also well-formed as the channels inherit parent delegation contexts.

e) Proof for remaining results: Since \(n = m + 1\), we have that \(\Pi_n = \Pi_i\) and so \(\Pi_n \subseteq \Pi_i\). Thus we have the results stated in condition 3 and 4.

**Case** \(j \neq i, n = m\) and \(e_j = (e_{j1} | e_{j2})\): If \(e_j\) is a bracket value, then it must be the case that the left projections are senddel/recv expressions. Without loss of generality, assume the following:

\[ e_i = (T_i[send \nu v \text{ then } e_{ix}] | e_{i2}) \]  

(235)

\[ e_j = (T_j[recv \nu \text{ as } x \text{ in } e_{jx}] | e_{j2}) \]  

(236)

\[ e'_i = (T_i[e_{ix}] | e_{i2}) \]  

(237)

\[ e'_j = (T_j[e_{jx}[x \rightarrow v]] | e_{j2}) \]  

(238)

\[ \Pi_i; \Gamma_i; \Theta_i; p_i; \Phi_i \vdash e_i : \tau_i \]  

(239)

\[ \Pi_j; \Gamma_j; \Theta_j; p_j; \Phi_j \vdash e_j : \tau_i \]  

(240)

We have to show that:

\[ \Pi_1; \Gamma_1; \Theta_1; p_1; \Phi_1 \vdash e_1 : \tau_1 \| \cdots \| \]  

\[ \Pi_2; \Gamma_2; \Theta_2; p_2; \Phi_2 \vdash e_2 : \tau_2 \| \cdots \| \]  

\[ \Pi_3; \Gamma_3; \Theta_3; p_3; \Phi_3 \vdash e_3 : \tau_3 \| \cdots \| \]  

\[ \Pi_m; \Gamma_m; \Theta_m; p_m; \Phi_m \vdash e_m : \tau_m \]  

(243)

(244)

Inverting (239) and (240) using DT-BRACKET (since it is an expression, we cannot invert using DT-BRACKET-VALUES or DT-BRACKET-WHERE), we have that

\[ \Pi_i \vdash (H \comp \Phi \subseteq \Phi'_i) \]  

(241)

\[ \Pi_i; \Gamma_i; \Theta_i; p_i; \Phi'_i \vdash T_i[send \nu v \text{ then } e_{ix}] : \tau_i \]  

(242)

\[ \Pi_i \vdash H \leq \tau_i \]  

(243)

and

\[ \Pi_j \vdash (H \comp \Phi \subseteq \Phi'_j) \]  

(245)

\[ \Pi_j; \Gamma_j; \Theta_j; p_j; \Phi'_j \vdash T_j[recv \nu \text{ as } x \text{ in } e_{jx}] : \tau_j \]  

(246)

\[ \Pi_j \vdash H \leq \tau_j \]  

(247)

Applying induction to the step relation in the premise:

\[ \Pi_1; \Gamma_1; \Theta_1; p_1; \Phi_1 \vdash e_1 : \tau_1 \| \cdots \| \]  

(249)

\[ \Pi_2; \Gamma_2; \Theta_2; p_2; \Phi_2 \vdash T_2[e_{ix} \circ \square] : \tau_i \| \cdots \| \]  

(250)

Replacing (242) and (246) with (249) and (250) we have the proof.

\[ \Pi_1; \Gamma_1; \Theta_1; p_1; \Phi_1 \vdash e_1 : \tau_1 \| \cdots \| \]  

\[ \Pi_2; \Gamma_2; \Theta_2; p_2; \Phi_2 \vdash e'_2 : \tau_2 \| \cdots \| \]  

\[ \Pi_3; \Gamma_3; \Theta_3; p_3; \Phi_3 \vdash e'_2 : \tau_3 \| \cdots \| \]  

\[ \Pi_m; \Gamma_m; \Theta_m; p_m; \Phi_m \vdash e_m : \tau_m \]  

(248)
f) Proof for remaining results: Since \( n = m \), we trivially have the results stated in 3 and 4.

**Case** \( n = m \) and \( e_j = T[e] \) : Applying induction hypothesis to the premise of BD-PAR-STEP-L, we have that \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i : r_i \). We already have \( \Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_2 : r_i \) and so from DT-BRACKET (proving for a more restrictive case) we have

\[
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i' : r_i
\]

Since both \( e_i \) and \( e_j \) take a step, it must be the case that they are \senddel \ and \recv \ pairs. We then have two more cases.

**Case** \( e_j = \senddel v \) : Without loss of generality assume that \( e_j = \senddel v \) then \( e_{sk} \). We then have the following:

\[
e_i = (T_i[\text{recv } v \text{ as } x \in e_{\kappa}] | e_{i2})
\]
\[
e_i' = (T_i[\text{recv } v \text{ as } x \in e_{\kappa}] | e_{i2})
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash (T_i[\text{recv } v \text{ as } x \in e_{\kappa}] | e_{i2}) : r_i
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_2 : r_i
\]
\[
e_j' = T_j[(e_{sk} | \senddel v \text{ then } e_{sk})]
\]
\[
\Pi_j; \Gamma_j; \Theta_j; p_j; pc_j \vdash T_j[\senddel v \text{ then } e_{sk}] : r_j
\]

We have to show that

\[
\Pi_j; \Gamma_j; \Theta_j; p_j; pc_j \vdash T_j[(e_{sk} | \senddel v \text{ then } e_{sk})] : r_j
\]

From (253) and DT-BRACKET (since it is an expression, we cannot invert using DT-BRACKET-VALUES or DT-BRACKET-WHERE), we have the following:

\[
\Pi_i \models (H^* \sqcup pc_i) \preceq pc_i'
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T_i[\text{recv } v \text{ as } x \in e_{\kappa}] : r_i
\]
\[
\Pi_i \vdash H^* \leq r_i
\]

Since holes can be typed under an appropriate \( pc_{i_{ir}} \), invoking Lemma 19 on (258) we have

\[
\Pi_i; \Gamma_i; \Theta_i; p_i; pc_i \vdash T_i[\text{recv } v \text{ as } x \in e_{\kappa}] : r_i \triangleright
\]
\[
\Pi_i; \Gamma_i'; \Theta_i'; p_i'; pc_{i_{ir}} \vdash \text{recv } v \text{ as } x \in e_{\kappa} : r_i'
\]

There are two cases depending on whether or not \( T_i \) includes \runTEE. That is \( T_i = E_i \) or \( T_i \neq E_i \).

**Case** \( T_i = E_i \): Invoking Lemma 24 on (261) we have that

\[
\Pi_i' \models (H^* \sqcup pc_{i_{ir}}) \preceq pc_{i_{ir}}'
\]
\[
\Pi_i' \vdash H^* \leq r_i'
\]

Inverting (263) using DT-RECV we have

\[
\Pi_i'; \Gamma_i'; \Theta_i'; p_i'; pc_{i_{ir}} \vdash \nu: \text{chan}_{p_i' \rightarrow p_i'} pc_{ch} \tau
\]
\[
\Pi; \Gamma; x : \tau; \Theta; p; pc \vdash e : r_i'
\]
\[
\tau_i' \in \{r_i', r_i''\}
\]
\[
\Pi_i' \models pc_{i_{ir}} \preceq pc_{ch}
\]
\[
\Pi_i' \vdash pc_{i_{ir}} \leq r_i'
\]

Applying R-TRANS to (262) and (268), we have

\[
\Pi_i' \vdash H^* \preceq pc_{ch}
\]
Case $T_i \neq E_i$: Consider the premises in (258) and (257). If $T_i$ includes $\text{runTEE}$ and $T_i[\text{recv } \nu \text{ as } x \text{ in } e_i]$ is well-typed under $\Pi''_i$, then $\exists p'_{c''}$ (since nested TEEs aren’t allowed and from Lemma 24 we have monotonic $\text{pc}$) such that

$$\Pi''_i \vdash H^\pi \sqsubseteq \text{pc}_{c''} \quad (275)$$

This implies

$$\Pi''_i; \Gamma''_i; \Theta''_i; p_i; \text{pc}_{c''} \vdash \text{runTEE}(E_i[\text{recv } \nu \text{ as } x \text{ in } e_i]: \text{unit} \square) \quad (276)$$

Recall the premise from DT-TEE and

$$C = \{ch \mid \text{ch} \in \Theta(ch) = \text{chan}_{t=m} \text{ pc}' \tau \land \Pi''_i \vdash \text{pc}_{c'} \sqsubseteq \text{pc}'\}$$

for some $\Pi''_i$ such that $|\text{dom}(\Pi''_i)| = \Pi''_i$. Since $E_i$ cannot contain any $\text{runTEE}$ or spawn, we have $\Theta''_i |C = \Theta'_i$. This implies

$$\Pi''_i \vdash H^\pi \sqsubseteq \text{pc}_{c_h} \quad (277)$$

Let us now focus on $j^{th}$ node. Since holes can be typed under appropriate $\text{pc}$, invoking Lemma 19 on (256), we have

$$\Pi_j; \Gamma_j; \Theta_j; p_j; \text{pc}_j \vdash T_j[\text{send } \nu v \text{ then } e_{s_k}]: \tau_j \triangleright$$

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash \text{send } \nu v \text{ then } e_{s_k} : \tau'_j \square \quad (278)$$

From (278) and DT-SENDDEL we have

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash \nu: \text{chan}_{p_j \rightarrow p'_j} \text{ pc}_{c_h} \tau \quad (279)$$

$$\Pi'_j; \emptyset; \emptyset; p_j; \text{pc}_{s_k} \vdash \nu: \tau \quad (280)$$

$$\Pi'_j \vdash \text{pc}_{s_k} \sqsubseteq \text{pc}_{c_h} \quad (281)$$

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash e_{s_k} : \tau_j \quad (282)$$

$$\tau_j' = \{\tau_j, \tau'_j \square\}$$

$$\Pi'_j \vdash \text{pc}_{c_h} \sqsubseteq \text{pc}_{s_k}'' \quad (284)$$

$$\Pi'_j \vdash \text{pc}_{c_h} \sqsubseteq \text{pc}_{s_k}'' \quad (285)$$

$$\Pi'_j \vdash \text{pc}_{s_k}'' \leq \tau_j' \quad (286)$$

$$\Pi''_j \equiv \Pi'_j \quad (287)$$

$$\Pi''_j \vdash \text{pc}_{c_h} \equiv \text{pc}_{s_k}'' \quad (288)$$

To show that $e'_j$ is well-typed, we first need to find a $\text{pc}_{c_h}$ such that DT-BRACKET holds for $(e_{s_k}; \square | \text{send } \nu v \text{ then } e_{s_k})$. That is, we need the following premises of the DT-BRACKET to type

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash (e_{s_k}; \square | \text{send } \nu v \text{ then } e_{s_k}) : \tau'_j \square \quad (290)$$

$$\Pi'_j \vdash (H^\pi \sqcap \text{pc}_{s_k}) \sqsubseteq \text{pc}_{c_h} \quad (290)$$

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash \text{send } \nu v \text{ then } e_{s_k} : \tau'_j \square \quad (291)$$

$$\Pi'_j; \Gamma'_j; \Theta'_j; p'_j; \text{pc}_{s_k} \vdash e_{s_k}; \square : \tau'_j \square \quad (292)$$

$$\Pi'_j \vdash H^\pi \leq \tau'_j \square \quad (293)$$

Let $\text{pc}_{s_k}'' = \text{pc}_{c_h}$. From premises (274), (277) and given conditions on the upward closure of $H^\pi$ we have

$$\Pi'_j \vdash H^\pi \sqsubseteq \text{pc}_{c_h} \quad (294)$$

Invoking hole substitution (Lemma 22) on (256) using the above premise and (278) we have the required proof.
g) Proof for remaining results: Since \( n = m \), we trivially have the results stated in 3 and 4.

**Case \( e_j = \text{recv } \nu \text{ as } x \in e \):** Given \( e_j = \text{recv } \nu \text{ as } x \in e_n \). We then have the following:

\[
e_i = (T_i[\text{send } \nu \text{ v then } e_{x_k}] | e_{i2})
\]
\[
e'_i = (T_i[e_{x_k}; \Box] | e_{i2})
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}_i \vdash e_i; \tau_i
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}_i \vdash e'_i; \tau_i
\]
\[
e'_j = T_j[(e_n[x \mapsto v]; \Box | \text{recv } \nu \text{ as } x \in e_n)]
\]
\[
\Pi_j; \Gamma_j; p_j; \text{pc}_j \vdash e_j; \tau_j
\]

We have to show that

\[
\Pi_j; \Gamma_j; \Theta_j; p_j; \text{pc}_j \vdash e'_j; \tau_j
\]

From (297) and DT-BRACKET (since it is an expression, we cannot invert using DT-BRACKET-VALUES or DT-BRACKET-WHERE), we have the following:

\[
\Pi_i \vdash (H^\pi \sqcup \text{pc}_i) \sqsubseteq \text{pc}'_i
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}'_i \vdash T_i[\text{send } \nu \text{ v then } e_{x_k}]; \tau_i
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}'_i \vdash e_{i2}; \tau_i
\]
\[
\Pi_i \vdash H^\pi \leq \tau_i
\]

Since holes can be typed under an appropriate \( \text{pc}_{is} \), invoking Lemma 19 on (302) we have

\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}'_i \vdash T_i[\text{send } \nu \text{ v then } e_{x_k}]; \tau_i \triangleright
\]
\[
\Pi_i; \Gamma_i; \Theta_i; p_i; \text{pc}'_i; \text{pc}_{is} \vdash \text{send } \nu \text{ v then } e_{x_k}; \tau'_i
\]

From DT-SENDDEL we have

\[
\Pi''_i; \Gamma''_i; \Theta''_i; p'_i; \text{pc}_{is} \vdash \nu : \text{chan}_{p'_i \rightarrow p'_i}; \text{pc}_{ch} \tau
\]
\[
\Pi''_i; \Theta''_i; p'_i; \text{pc}_{ch} \vdash v ; \tau
\]
\[
\Pi''_i \vdash \text{pc}_{ch} \sqsubseteq \text{pc}_{ch}
\]
\[
\Pi''_i; \Gamma''_i; \Theta''_i; p''_i; \text{pc}''_{is} \vdash e_{x_k} ; \tau''_i
\]
\[
\tau'_i = \{\tau'_i, \tau'_i \Box\}
\]
\[
\Pi''_i \vdash \text{pc}_{is} \sqsubseteq \text{pc}'_{is}
\]
\[
\Pi''_i \vdash \text{pc}_{ch} \sqsubseteq \text{pc}'_{ch}
\]
\[
\Pi''_i \vdash \text{pc}''_{is} \leq \tau''_i
\]
\[
\Pi''_i \vdash \Pi'_i
\]
\[
\Pi''_i \vdash p'_i \succeq \text{pc}_{is}
\]
\[
\Pi''_i \vdash p'_i \succeq \text{pc}_{ch}
\]
\[
\Pi''_i \vdash p'_i \succeq \text{pc}_{ch}
\]

There are two cases depending on whether or not \( T_i \) includes \text{runTEE}. That is \( T_i = E_i \) or \( T_i \neq E_i \).

**Case \( T_i = E_i \):** Invoking Lemma 24 on (305) we have that

\[
\Pi'_i \vdash (H^\pi \sqcup \text{pc}_{is}) \sqsubseteq \text{pc}'_{is}
\]
\[
\Pi'_i; \Gamma'_i; \Theta'_i; p_i; \text{pc}'_{is} \vdash \text{send } \nu \text{ v then } e_{x_k}; \tau'_i
\]
\[
\Pi'_i \vdash H^\pi \leq \tau'_i \Box
\]

Applying \text{R-TRANS} to (319) and (318), we have

\[
\Pi'_i \vdash H^\pi \sqsubseteq \text{pc}_{ch}
\]

**Case \( T_i \neq E_i \):** Consider the premises in (302) and (301). If \( T_i \) includes \text{runTEE} and \( T_i[\text{send } \nu \text{ v then } e_{x_k}] \) is well-typed under \( \text{pc}'_i \), then \( \exists \text{pc}''_i \) (nested TEEs aren’t allowed and for non-TEEs we have monotonic \( \text{pc} \) from Lemma 24) such that

\[
\Pi''_i \vdash H^\pi \sqsubseteq \text{pc}''_i
\]
\[
\Pi''_i; \Gamma''_i; \Theta''_i; p''_i; \text{pc}''_i \vdash \text{runTEE}[E_i[\text{send } \nu \text{ v then } e_{x_k}]] : \text{unit} \Box
\]
\[
\Pi''_i \vdash H^\pi \leq \text{unit} \Box
\]
Recall the premise from DT-TEEG

\[ C = \{ ch \mid ch \in \Theta(ch) = \text{chan}_{\text{r}} = \rho, \text{pc}^j \tau \land \Pi^\prime_{i} \vdash \text{pc}^\prime_{i} \subseteq \text{pc}^j \} \]

Since \( E_i \) cannot contain any \text{runTEE} or \text{spawn}, we have \( \Theta^\prime_{i} \mid \_ = \Theta^\prime_{j} \). This implies

\[ \Pi^\prime_{i} \vdash H^x \subseteq \text{pc}_{ch} \]  (325)

Let us now focus on the \( j^{th} \) node. Since holes can be typed under an appropriate \( \text{pc}_{jr} \), invoking Lemma 19 on (300), we have

\[ \Pi_j; \Gamma_j; \Theta_j; \pi_j; \text{pc} \vdash T_j[\text{recv} \nu \text{ as } x \text{ in } e_{\nu}] : \tau_j \triangleright \]

\[ \Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash \text{recv} \nu \text{ as } x \text{ in } e_{\nu} : \tau_j^\prime \square \]  (326)

Inverting (326) using DT-RECEIVE we have

\[ \Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash \nu : \text{chan}_{\pi_j^\prime - \pi_j} \text{pc}_{ch} \tau \]

\[ \Pi_j^\prime \vdash \text{pc}_{jr} \subseteq \text{pc}_{ch} \]  (327)

To show that \( e_j^\prime \) is well-typed, we first need to find a \( \text{pc}_{jr} \) such that DT-BRACKET holds for \( e_{\nu}[x \mapsto v] | \text{recv} \nu \text{ as } x \text{ in } e_{\nu} \). That is, we need the following premises to get

\[ \Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash (e_{\nu}[x \mapsto v] | \text{recv} \nu \text{ as } x \text{ in } e_{\nu}) : \tau_j. \]

\[ \Pi_j^\prime \vdash (H^x \cup \text{pc}_{jr}) \subseteq \text{pc}_{jr} \]  (329)

Premise (329) follows from (328). We prove (330) as shown below: Inverting (300) using DT-RECV. Summarized as inference rule below.

\[
\begin{array}{c}
\Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash \text{recv} \nu \text{ as } x \text{ in } e_{\nu} : \tau_j^\prime \square \\
\Pi_j^\prime \vdash \text{pc}_{jr} \subseteq \text{pc}_{ch} \\
\Pi_j^\prime \vdash \text{pc}_{jr} \leq \tau_j^\prime \\
\Pi_j^\prime \vdash \pi_j^\prime \triangleright \text{pc}_{jr} \\
\Pi_j^\prime \vdash \pi_j \triangleright \text{pc}_{ch} \\
\Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{ch} \vdash \text{recv} \nu \text{ as } x \text{ in } e_{\nu} : \tau_j^\prime \square
\end{array}
\]

To show (331), we first have to show that

\[ \Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash \nu : \tau \]

For this, we invoke the lemma that states that values sent are well-typed under places that satisfy the clearance requirement (Lemma 17 to (307)). Premise (331) now follows from invoking substitution lemma on the premises of (330). We still need to show that \( \Pi_j^\prime \vdash H^x \leq \tau_j^\prime \square \) holds. Consider the above premises: \( \Pi_j^\prime \vdash \text{pc}_{ch} \subseteq \text{pc}_{jr}^\prime \) and \( \Pi_j^\prime \vdash \text{pc}_{jr} \leq \tau_j^\prime \). From these (329) and DP-HALT, the result follows. We now have all the premises to show

\[ \Pi_j^\prime; \Gamma_j^\prime; \Theta_j^\prime; \pi_j^\prime; \text{pc}_{jr} \vdash (e_{\nu}[x \mapsto v] | \text{recv} \nu \text{ as } x \text{ in } e_{\nu}) : \tau_j^\prime \]  (333)

Invoking hole substitution (Lemma 22) on (300) using the above premise we have the full proof.

\textbf{h) Proof for remaining results:} Since \( n = m \), we trivially have the results stated in 3 and 4.

\textbf{Case BD-PAR-SPAWN :} Similar to the spawn sub case in the proof of BD-PAR-STEP (i.e., Case \( n \neq m \) and \( j \neq i \)).

\textbf{Case BD-PAR-SEND-RECV-L:} Given

\[ e_i = T_i[\{\text{send} \nu \text{ v then } e_{sk} \mid e\}] \]  (334)

\[ e_j = T_j[\{\text{recv} \nu \text{ as } x \text{ in } e_{\nu} \mid e^\prime\}] \]  (335)

\[ e_j^\prime = T_i[\{e_{sk} \mid \square \mid e\}] \]  (336)

\[ e_j^\prime = T_j[\{e_{\nu}[x \mapsto v] \mid \square \mid e^\prime\}] \]  (337)
Also given that \( e_i \) and \( e_j \) are well-typed.

\[
P; \Gamma; \Theta; p_i; pc_i \vdash T_j[(\text{send} \nu v \text{ then } e_{sn} \mid e)]; \tau_i
\]

Invoking Lemma 19 we have that \( (\text{send} \nu v \text{ then } e_{sn}; \nothing \mid e) \) is well typed for some \( pc_s \). That is,

\[
\Pi'_i; \Gamma'_i; \Theta'_i; p_i'; pc'_i \vdash (\text{send} \nu v \text{ then } e_{sn}; \nothing \mid e); \tau'_s \square
\]

So, from the second premise (following DT-SENDDEL) we have:

\[
\Pi'_i; \Gamma'_i; \Theta'_i; p_i'; pc'_i \vdash v: \tau
\]

\[
\Pi'_i; \Gamma; \Theta; p; pc''_s \vdash E_{sn} : \tau''_s
\]

\[
\tau''_s = \{ \tau''_s \}
\]

\[
\Pi'_i \vdash pc''_s \subseteq pc''_s
\]

\[
\Pi'_i \vdash pc''_s \subseteq pc''_s
\]

\[
\Pi'_i \vdash pc''_s \leq \tau''_s
\]

\[
\Pi''_i \subseteq \Pi'_i
\]

\[
\Pi'_i \vdash p_i'' \rhd pc''_s
\]

\[
\Pi'_i \vdash p_i'' \rhd pc''_s
\]

We already have \( H^n \leq \tau''_s \square \). Applying PC reduction (Lemma 11 to the fourth premise, we have \( \Pi'_i; \Gamma'_i; \Theta'_i; p_i'; pc'_i \vdash e_{sn}; \nothing \mid e); \tau''_s \square \). So

\[
\Pi'_i; \Gamma'_i; \Theta'_i ; p_i'; pc'_i \vdash (e_{sn}; \nothing \mid e); \tau''_s \square
\]

Depending on whether or not \( e_j \) is bracketed, we have two more cases.

**Case** \( e_j = (T_j[\text{recv} \nu \text{ as } x \text{ in } e_{\kappa}] \mid e') \):

We are given that

\[
e_j = T_j[(\text{recv} \nu \text{ as } x \text{ in } e_{\kappa}] \mid e')
\]

\[
e'_j = T_j[(e_{\kappa}[x \mapsto v] \mid e')
\]

Also given

\[
P; \Gamma; \Theta; p_j; pc_j \vdash T_j[(\text{recv} \nu \text{ as } x \text{ in } e_{\kappa}] \mid e'); \tau_j
\]

Invoking Lemma 19 we have that \( (\text{recv} \nu \text{ as } x \text{ in } e_{\kappa}] \mid e') \) is well typed for some \( pc_r \). That is,

\[
\Pi'_i \vdash (H^n \cup pc_r) \subseteq pc'_r
\]

\[
\Pi'_i; \Gamma'_i; \Theta'_i; p'_i; pc'_i \vdash \text{recv} \nu \text{ as } x \text{ in } e_{\kappa}; \tau'_r \square
\]

\[
\Pi'_i; \Gamma'_i; \Theta'_i; p'_i; pc'_i \vdash e'; \tau'_r \square
\]

\[
\Pi'_i \vdash H^n \leq \tau'_r \square
\]

So, from the second premise (following DT-RECEIVE) we have:

\[
\Pi'_i; \Gamma'_i; \Theta'_i; p'_i; pc'_i \vdash \text{ch}: \text{chan}_{p'_i \rightarrow p'_i} \text{pc}_{ch} \tau
\]

\[
\Pi'_i; \Gamma'_i; x: \tau; \Theta'_i; p'_i; pc'; e_{\kappa}: \tau'_r
\]

\[
\tau''_r = \{ \tau''_r, \tau''_r \}
\]

\[
\Pi'_i \vdash pc''_r \subseteq pc_{ch}
\]

\[
\Pi'_i \vdash pc''_r \subseteq pc_{ch}
\]

\[
\Pi'_i \vdash pc''_r \leq \tau''_r
\]

\[
\Pi'_i \vdash p'_i \rhd pc''_r
\]

\[
\Pi'_i \vdash p'_i \rhd pc''_r
\]

We now prove the \text{recv} part. First we have to prove that the value \( v \) is well-typed under the new context. From premise (338), we have that \( v \) can be typed under empty variable and channel environment, and a delegation context that enables a place to act for the \( pc \).
Applying Lemma 17 to (338) using (356), it then follows that

$$\Pi_j' ; \emptyset ; p'_j ; pc''_r \vdash v : \tau$$

Weakening channel environment for values (Lemma 12) and variable context in the above judgment, we have:

$$\Pi_j' ; \Gamma_j' ; \Theta_j' ; p'_j ; pc''_r \vdash v : \tau$$

From variable substitution lemma (Lemma 14) we have

$$\Pi_j' ; \Gamma_j' ; \Theta_j' ; p'_j ; pc''_r \vdash \epsilon_n [x \mapsto v] : \tau'_r$$

Thus we have

$$\Pi_j ; \Gamma_j ; \Theta_j ; p_j ; pc_r \vdash (\epsilon_n [x \mapsto v] ; \square | e') : \tau'_i$$

Invoking hole substitution (Lemma 22), we thus have

$$\Pi_i ; \Gamma_i ; \Theta_i ; p_i ; pc_i \vdash T_j[(\epsilon_n ; e)] : \tau_i$$

and

$$\Pi_j ; \Gamma_j ; \Theta_j ; p_j ; pc_j \vdash T_j[(\epsilon_n [x \mapsto v] ; \square | e') : \tau_j$$

Case $e_j = T_j[\text{recv } \nu \text{ as } x \text{ in } e_n]$:

$$e_j = T_j[\text{recv } \nu \text{ as } x \text{ in } e_n] \quad (358)$$

$$e'_j = T_j[(\epsilon_n [x \mapsto v] ; \square | \text{recv } \nu \text{ as } x \text{ in } e_n)] \quad (359)$$

The proof is similar to the case $e_j = \text{recv } \nu \text{ as } v \text{ in } e_k$ in the proof of BD-PAR-STEP-L.

i) Proof for remaining results: Since $n = m$, we trivially have the results stated in 3 and 4.

Case BD-PAR-SEND-RECV-R: Similar to above case except that we prove when right projection takes a step.

Case BD-PAR-RECV-SEND-L: Similar to the proof of BD-PAR-SEND-RECV-L except that we now prove $T_j[((() | \text{send } \nu \text{ then } e_n)]$ is well-typed.

Case BD-PAR-RECV-SEND-R: Similar to the proof of BD-PAR-SEND-RECV-R.

This completes the type preservation proof for the distributed system.

Using the Lemma 25, we will prove Theorem 1 restated as below. The key insight is that a well-typed DFLATE configuration cannot evaluate to bracketed final values on the attacker node $p_j$.

**Theorem 3** (DFLATE noninterference with clearance). Let $e_k$ for $k \in \{1 \ldots m\}$ be DFLATE programs such that $\Pi_{init}; \Gamma_{k}; \Theta_{k}; p_{k}; pc_{k} \vdash e_k : \tau_k$, where $\tau_j = \ell$ says bool $\square$ and $x : H$ says $\tau \in \Gamma_i$ for some $H$, and for all $k$ processes do not downgrade $H$:

$$\forall k \in \{1 \ldots m\}. \forall \ell' \cdot \Pi_{init} \vdash H \subseteq \ell' \iff \hat{\Pi}_i \vdash H \subseteq \ell'$$

The attacker has limited authority :

$$\Pi_{init} \not\vdash H \subseteq \ell$$

then for all $v_z, z \in \{1, 2\}$ such that $\Pi; \Gamma_i; \Theta_i; p_i; pc_i \vdash v_z : H$ says $\tau$, if $\langle p_1, e_1 \rangle \cdots \langle p_i, e_i[x \mapsto v_z] \rangle \cdots \langle p_m, e_m \rangle$ steps to $\langle p_1, e'_{1z} \rangle \cdots \langle p_i, e'_{iz} \rangle \cdots \langle p_j, \tilde{v}_{jz} \rangle \cdots \langle p_m, e'_{nz} \rangle$ we have $\tilde{v}_j = \tilde{v}_{jz}$.

Proof. Given that

$$\langle p_1, e_1 \rangle \cdots \langle p_i, e_i[x \mapsto v_z] \rangle \cdots \langle p_m, e_m \rangle \Rightarrow^* \langle p_1, e'_{1z} \rangle \cdots \langle p_j, \tilde{v}_j \rangle \cdots \langle p_m, e'_{nz} \rangle$$

Also given that

$$\Pi_i ; \Gamma_i, x : H \text{ says } \tau; \Theta_i; p_i; pc_i \vdash e_i : \tau_i$$

From DT-BRACKET-VALUES (note that since they are user-provided inputs, they cannot contain where terms), we have that $\Pi_i ; \Gamma_i; \Theta_i; p_i; pc_i \vdash (v_1 \mid v_2) : H$ says $\tau$. Invoking substitution lemma (Lemma 14) we have that $\Pi_i ; \Gamma_i; \Theta_i; p_i; pc_i \vdash e_i [x \mapsto (v_1 \mid v_2)] : \tau_i$. From the type preservation (Lemma 25) we have that for all $k \in \{1 \ldots n\}$ $\Pi_k; \Gamma_k; \Theta_k; p_k; pc_k \vdash e_k : \tau_k$ such that $\Theta_k \supseteq \Theta_k$. We prove for each case of $\pi$ separately.

From the completeness property (Lemma 5), we have that

$$\langle p_1, e_1 \rangle \cdots \langle p_i, e_i[x \mapsto (v_1 \mid v_2)] \rangle \cdots \langle p_m, e_m \rangle \Rightarrow^* \langle p_1, e'_{1z} \rangle \cdots \langle p_j, \tilde{v}_j \rangle \cdots \langle p_m, e'_{nz} \rangle$$
and from the soundness property (Lemma 2), we have that $|v'_j|_z = v'_j$ for $z = \{1, 2\}$.

From the type preservation lemma, we have that $\Pi_j; \Gamma_j; \Theta'_j; p_j; pc_j \vdash v'_j; \ell$ says bool. We have to prove that

$$|v'_j|_1 = |v'_j|_2$$

Let us assume that $v'_j$ is a bracket value. Then by DT-BRACKET-VALUES (or DT-BRACKET-WHERE), we have the following:

$$\Pi_j; \Gamma_j; \Theta'_j; p_j; pc_j \vdash [v'_j]_1 : \tau_j$$
$$\Pi_j; \Gamma_j; \Theta'_j; p_j; pc_j \vdash [v'_j]_2 : \tau_j$$
$$\Pi_j; \Gamma_j; \Theta'_j; p_j; pc_j \vdash \vec{v}_j : \tau_j$$

From (364), we require that $\Pi_j \vdash H^\pi \subseteq \ell$ (rule DP-LBL). But from condition (361) we are already given that $\Pi_j \not\vdash H^- \subseteq \ell$. Thus $\Pi_j; \Gamma_j; \Theta'_j; p_j; pc_j \vdash v'_j : \tau_j$ cannot be well-typed by DT-BRACKET-VALUES and so $v'_j$ itself isn’t a bracket term.

Note that $v'_j$ can only be a where term. We can prove this by inducting on the structure of the value. The only interesting case is $v = v''$. In this case, we can easily prove that $v''$ cannot be a bracket term because subtypes (bool = unit + unit) cannot protect any principal.

Without loss of generality, let $v'_j = w$ where $p \succ q$. We now need to prove that $w$ is not a bracket term. We prove this by induction on the number of where (either bracket or non-bracket) terms. For base case, where there are zero where terms, we already proved that $v'_j$ is not a bracket term.

**j) Induction Hypothesis:** Let $v'_j = w$ where $n^{-1}(p_{n-1} \succ q_{n-1})$ where $p_n \succ q_n$. For $k = \{0 \ldots n - 1\}$, $w$ where $k(p_k \succ q_k)$ is not a bracket term.

**k) Induction Step:** For the induction step, we need to prove that $v'_j = w$ where $\succ q$ where $n^{-1}(p_{n-1} \succ q_{n-1})$ where $p_n \succ q_n$ is not a bracket term. From the induction hypothesis, we already have that $w$ where $p \succ q$ where $k(p_k \succ q_k)$ where $p_n \succ q_n$ is not a bracket term for $k = \{1 \ldots n\}$. Thus it remains to prove that $w$ where $p \succ q$ is not a bracket term.

**Case $\pi = \rightarrow$:** From condition (361), we have $\Pi_j \not\vdash H^- \subseteq \ell$ and so we have that $\Pi_j \not\vdash \ell \succ H^- \subseteq \Pi_j = \pi, (p \succ q)$. Note that this is the same as $\Pi_j \not\vdash \ell \succ H^- \subseteq \ell$. Running an argument similar to the base case, we have that $w$ cannot be a bracket term (as $\Pi_j \not\vdash \ell \succ \Pi_j$).

**Case $\pi = \leftarrow$:** Similar to the above case, using condition (361), we have $\Pi_j \not\vdash H^- \subseteq \ell$. Again $w$ cannot be a bracket term.

This completes the proof that $v'_j$ isn’t a bracket term and so $|v'_j|_1 = |v'_j|_2$. \qed

We elaborate the erasure/observation function in the below table.

<table>
<thead>
<tr>
<th>$O(\ell, \Pi, A)$</th>
<th>$\ell$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$O(p \succ q, \Pi, A)$</td>
<td>$p \succ q$</td>
</tr>
<tr>
<td>$O(x, \Pi, A)$</td>
<td>$x$</td>
</tr>
<tr>
<td>$O(\eta x e', \Pi, A)$</td>
<td>$\eta x O(e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(\lambda x : \tau)[pc, \Theta, \Pi, \lambda x : \tau] e', \Pi, A)$</td>
<td>$\lambda x : \tau)[pc, \Theta, \Pi, \lambda x : \tau] e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(e' \epsilon'', \Pi, A)$</td>
<td>$O(e', \Pi, A) O(e'', \Pi, A)$</td>
</tr>
<tr>
<td>$O((e_1, e_2), \Pi, A)$</td>
<td>$(O(e_1, \Pi, A), O(e_2, \Pi, A))$</td>
</tr>
<tr>
<td>$O(proj e', \Pi, A)$</td>
<td>$proj O(e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(inj_1 e', \Pi, A)$</td>
<td>$inj_1 O(e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(case e' \ of \ inj_1(x), e_1 \</td>
<td>\ inj_2(x), e_2, \Pi, A)$</td>
</tr>
<tr>
<td>$O(bind x = e' \ in \ e'', \Pi, A)$</td>
<td>$bind x = O(e', \Pi, A) in O(e'', \Pi, A)$</td>
</tr>
<tr>
<td>$O(assume e' \ in \ e'', \Pi, A)$</td>
<td>$assume O(e', \Pi, A) in O(e'', \Pi, A)$</td>
</tr>
<tr>
<td>$O(e' \ where \ v, \Pi, A)$</td>
<td>$O(w(e' \ where \ v, \Pi, A)$</td>
</tr>
<tr>
<td>$O(TEE) e', \Pi, A)$</td>
<td>$O_{tes}(TEE) e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(runTEE) e', \Pi, A)$</td>
<td>$O_{tes}(runTEE) e', \Pi, A)$</td>
</tr>
<tr>
<td>$O(spi \ \eta \ (pl, ch[pc; \tau], ch[pc; \tau]). e' then e'', \Pi, A)$</td>
<td>$spi \ \eta \ (pl, ch[pc; \tau], ch[pc; \tau]). e' then e'', \Pi, A)$</td>
</tr>
<tr>
<td>$O(send ch e' then es, \Pi, A)$</td>
<td>$send ch O(e', \Pi, A) then O(es, \Pi, A)$</td>
</tr>
<tr>
<td>$O(recv \ ch as x in e', \Pi, A)$</td>
<td>$recv \ ch as x in O(e', \Pi, A)$</td>
</tr>
</tbody>
</table>

Erasure functions for sealed values and TEEs are defined in Figure 25. We lift the erasure of expressions to a process as follows.

$O((q, e, \Pi, A)) = O(q, O(e, \Pi, A))$
Duplicates in traces are removed using the below definition. It simply compares the pairwise elements and eliminates the duplicated value if they are the same.

Definition C.1 (Trace Deduplication),

\[
\mathcal{O}(\eta, v, \Pi, A) = \begin{cases} 
\eta \mathcal{O}(v, \Pi, A) & \text{if } \Pi \parallel A \ni \ell \\
\eta \bullet & \text{if } \Pi \not\ni A \ni \ell
\end{cases}
\]

\[
\mathcal{O}_{tcb}(e, \Pi, A) = \begin{cases} 
\text{TEE} \mathcal{O}(e', \Pi, A) & \text{if } \Pi \parallel A \ni t \\
\text{runTEE} \mathcal{O}(e', \Pi, A) & \text{if } \Pi \not\ni A \ni t \\
\text{runTEE} \bullet & \text{if } \Pi \not\ni A \ni t
\end{cases}
\]

\[
\mathcal{O}_{w}(e' \text{ where } v, \Pi, A) = \begin{cases} 
\bullet & \text{if } \mathcal{O}(e', \Pi \cdot v, A) = \bullet \\
\mathcal{O}(e', \Pi \cdot v, A) & \text{o.w}
\end{cases}
\]

Fig. 25: Erasure function for sealed values and TEEs

Two traces are equivalent if they are equal after removing the duplicates.

Definition C.2 (Trace Equivalence). Two traces, \(Tr_1\) and \(Tr_2\), are equivalent, denoted as \(Tr_1 \approx Tr_2\), if they are equal after removing the duplicates. That is

\[
Tr_1 \approx Tr_2 \iff \mathcal{U}(Tr_1) = \mathcal{U}(Tr_2)
\]

Before we prove the noninterference for the stronger model, we prove that if two non-bracketed executions (left and right) compute a value on a process running on the attacker node \(p_j\), then there exists a bracketed execution such that the projections of the traces of the process in the bracketed execution is equal to that of the left and right executions. We first introduce some scheduling terminology.

Definition C.3 (Schedule). Let a system take \(k\) number of steps. Then schedule, \(S\), is the sequence of set of nodes on which the expressions are reduced in each step. If

\[
\langle p_1, e_1^1 \rangle \parallel \cdots \parallel \langle p_i, e_i^1 \rangle \parallel \cdots \parallel \langle p_j, e_j^1 \rangle \parallel \cdots \parallel \langle p_m, e_m^1 \rangle \\
\Rightarrow \ast
\]

\[
\langle p_1, e_1^k \rangle \parallel \cdots \parallel \langle p_i, e_i^k \rangle \parallel \cdots \parallel \langle p_j, e_j^k \rangle \parallel \cdots \parallel \langle p_n, e_n^k \rangle
\]

then, schedule, \(S = (P_i)_{i=1}^{k}\) where \(P_i\) is the set of nodes reduced at \(i^{th}\) step.

Remark C.1. Note that the cardinality of \(P_i\) is at least 1 and at most 2.

Example C.1. Consider the following evaluation.

\[
\langle p, \text{bind } x = v \text{ in send ch } x \text{ then } e_{sn} \rangle \parallel \langle q, \text{recv ch as } y \text{ in } y \rangle
\]

\[
\Rightarrow
\]

\[
\langle p, \text{send ch } v \text{ then } e_{sn} \rangle \parallel \langle q, \text{recv ch as } y \text{ in } y \rangle
\]

\[
\Rightarrow
\]

\[
\langle p, (\) \rangle \parallel \langle q, v \rangle
\]

Then, \(S = \{p\}, \{p, q\}\).

Definition C.4 (Scheduling order of a node). Let \(S\) be the schedule of a system. Then scheduling order of a node \(p\), written, \([S]_p\), is the sequence of elements in the schedule containing \(p\). That is,

\[
[S]_p = \begin{cases} 
\text{hd}(S), [\text{tail}(S)]_p & \text{if } p \in \text{hd}(S) \\
[\text{tail}(S)]_p & \text{if } p \notin \text{hd}(S)
\end{cases}
\]

Example C.2. In the example C.1, \([S]_p = \{p\}, \{p, q\}\) and \([S]_q = \{p, q\}\)

Lemma 26 (Race Freedom). send and recv are race-free.
**Proof Sketch:** DFLATE channels are unidirectional. It has two endpoints and can either send or recv between these endpoints. Thus the order of communication is deterministic and race-free.

**Lemma 27** (Evaluation of a process is deterministic). Let $S_1$ and $S_2$ be two different schedules of a DFLATE program.

\[
S_1 = \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies^* \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, e'_i \rangle \parallel \cdots \parallel \langle p_m, e'_m \rangle
\]

\[
S_2 = \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies^* \langle p_1, e''_1 \rangle \parallel \cdots \parallel \langle p_i, e''_i \rangle \parallel \cdots \parallel \langle p_m, e''_m \rangle
\]

such that $|S_1|_{p_i} = |S_2|_{p_i}$, then $\langle p_i, e'_i \rangle = \langle p_i, e''_i \rangle$

**Proof Sketch:** Given that DFLATE programs are race-free (Lemma 26), evaluation of a process is deterministic.

**Definition C.5** (Commuting Schedules). Two schedules $S_1$ and $S_2$ are said to be commuting if they preserve the scheduling order of all nodes. That is,

\[\forall p, |S_1|_p = |S_2|_p\]

**Example C.3.**

\[
\begin{array}{c|c}
S & |S|_p \\
\hline
S_1 = \{p\}, \{p, q\}, \{r\}, \{p, r\} & \{p\}, \{p, q\}, \{p\}, \{p, r\} \\
S_2 = \{p\}, \{p, q\}, \{p\}, \{p, r\} & \{p\}, \{p, q\}, \{p\}, \{p, r\} \\
S_3 = \{p\}, \{p, q\}, \{r\}, \{p, r\} & \{p\}, \{p, q\}, \{p\}, \{p, r\}
\end{array}
\]

From the second column, we see that $S_1$ and $S_2$ are commuting but $S_3$ does not commute with either $S_1$ or $S_2$.

**Lemma 28** (Trace of a process in commuting schedules). The trace of a process in two commuting schedules is equal (upto stuttering).

**Proof.** Since commuting schedules preserve the scheduling order of a process, trace remains unchanged (up to stuttering). $\square$

A schedule for bracketed execution contains a sequence of pairs of processes scheduled at each step.

**Lemma 29** (Bracketed Schedule is a Prefix of First Execution). Let $S_l$ and $S_r$ be the left and right executions shown below.

\[
S_l = \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i[x \mapsto v_l] \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies^* \langle p_1, e'_1 \rangle \parallel \cdots \parallel \langle p_i, e'_i \rangle \parallel \cdots \parallel \langle p_m, e'_m \rangle
\]

\[
S_r = \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i[x \mapsto v_r] \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies^* \langle p_1, e''_1 \rangle \parallel \cdots \parallel \langle p_i, e''_i \rangle \parallel \cdots \parallel \langle p_m, e''_m \rangle
\]

Let $S_{(\_)}$ be the corresponding bracketed execution shown below:

\[
S_{(\_)} = \langle p_1, e_1 \rangle \parallel \cdots \parallel \langle p_i, e_i[x \mapsto (v_l | v_r)] \rangle \parallel \cdots \parallel \langle p_m, e_m \rangle \implies^* \langle p_1, e''_1 \rangle \parallel \cdots \parallel \langle p_i, e''_i \rangle \parallel \cdots \parallel \langle p_m, e''_m \rangle
\]

If $P$ is the set of processes in $S_{(\_)}$, then $\forall p \in P, |[S_{(\_)}]|_{1:p} \leq |S_1|_p$ where $\leq$ is a prefix where $[S_{(\_)}]|_{1}$ is the left projection of the bracketed schedule. Similar statement holds for right projection of the bracketed schedule.

**Proof.** We induct on the length of the $S_{(\_)}$ which is same as the number of the evaluation steps of the bracketed execution.

**Base Case:** Given $|S_{(\_)}|_{1} = P_1$. Hence

\[|S_{(\_)}|_{1:p} = \begin{cases} P_1 & \text{if } p \in P_1 \\ \emptyset & \text{otherwise} \end{cases}\]

Note that both executions start with the same configuration. Since evaluation of a process for the same number of steps is deterministic (Lemma 27), we have $|S_1|_p = P_1$ if $p \in P_1$ else $\emptyset$. Thus,

\[\forall p \in P, |P_1|_p \leq |S_1|_p\]

This proves the base case.

**Inductive Case:** Let $len(S_{(\_)}) = k + 1$. Given that

\[\forall p, |[S_{(\_)}]|_{1:k} \leq |S_1|_p\]

Let $|S_{(\_)}|_{k+1} = P_{k+1}$. Hence

\[|S_{(\_)}|_{1:p} = \begin{cases} |[S_{(\_)}]|_{1:k} \parallel P_{k+1} & \text{if } p \in P_{k+1} \\ |[S_{(\_)}]|_{1:k} \parallel \emptyset & \text{otherwise} \end{cases}\]

Let $len([S_{(\_)}]|_{1:k}) = k'$. This implies that the bracketed execution has taken $k'$ number of steps for process $p$. From induction hypothesis, we have that $|[S_{(\_)}]|_{1:k} \leq |S_1|_p$. That implies, $|[S_{(\_)}]|_{1:k} |_{p} = |S_1|_p |_{1:k'}$. That is, the left execution has also taken $k'$ number of steps on node $p$. Thus the configuration is same for process $p$ (Lemma 27).
If \( |P_{k+1}| = 2\), then \( \exists q\) such that \( q \neq p \) and \( q \in P_{k+1}\). Following similar reasoning, we have that the configuration for process \( q\) is also same in both bracketed and left execution. Thus they take step in the same way and so \([|S_{(1)}|1 : k + 1]|_{p} = |S_{1}|p[1 : k + 1]\). Thus \([|S_{(1)}|1 : k + 1]|_{p} \leq |S_{1}|p\).

Similar proof follows for right execution. Hence the proof.

We now prove the noninterference for stronger confidentiality attacker stated in Theorem 2 (and restated in an equivalent way below). Note that the statement expands the definition of trace equivalence using the observation function.

**Theorem 4 (DFLATE Noninterference with Stronger Observational Model).** Let \( e_{k} \) for \( k \in \{1 \ldots m\} \) be DFLATE programs such that \( \Pi_{init}; \Gamma_{i}; \Theta_{i}; p_{i}; p_{c} \vdash e_{k} : \tau_{k} \) and \( x : H \rightarrow \) says \( \hat{\tau} \in \Gamma_{i} \) for some \( H \), and processes do not downgrade \( H \rightarrow \):  

\[
\Pi_{init} \not\vdash p_{j} \Rightarrow H \rightarrow
\]

For all \( v_{z}, z \in \{1, 2\} \) such that \( \Pi_{i}; \Gamma_{i}; \Theta_{i}; p_{i}; p_{c} \vdash v_{z} : H \rightarrow \) says \( \hat{\tau} \), if  

\[
\mathcal{S}_{l} = \langle p_{1}, e_{1} \rangle \cdots \langle p_{l}, e_{l} \rangle[1 : x \mapsto v_{l} \rangle \cdots | \langle p_{m}, e_{m} \rangle \Rightarrow^{s} \langle p_{1}, e_{1}' \rangle \cdots \langle p_{l}, e_{l}' \rangle[1 : x \mapsto v_{l} \rangle \cdots | \langle p_{m}, e_{m}' \rangle
\]

and  

\[
\mathcal{S}_{r} = \langle p_{1}, e_{1} \rangle \cdots \langle p_{l}, e_{l} \rangle[1 : x \mapsto v_{r} \rangle \cdots | \langle p_{m}, e_{m} \rangle \Rightarrow^{s} \langle p_{1}, e_{1}' \rangle \cdots \langle p_{l}, e_{l}' \rangle[1 : x \mapsto v_{r} \rangle \cdots | \langle p_{m}, e_{m}' \rangle
\]

then traces of the process executing on node \( p_{j} \), as observed by the attacker on node \( p_{j} \), are equivalent in both executions. That is,

\[
\mathcal{O}((p_{j}, e_{j}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}), \ldots, \mathcal{O}(p_{j}, v_{j}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}) = \mathcal{O}((p_{j}, e_{j}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}), \ldots, \mathcal{O}(p_{j}, v_{j}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-})
\]

where \( \epsilon^{s} \) represents the \( s^{th} \) step of the system.

**Proof.** We have that the initial configuration of both left and right executions can be projected from bracketed execution. That is,

\[
\langle p_{1}, e_{1} \rangle \cdots \langle p_{l}, e_{l} \rangle[1 : x \mapsto v_{l} \rangle \cdots | \langle p_{m}, e_{m} \rangle = \langle p_{1}, e_{1} \rangle \cdots \langle p_{l}, e_{l} \rangle[1 : x \mapsto v_{r} \rangle \cdots | \langle p_{m}, e_{m} \rangle
\]

and from the soundness property (Lemma 2), we have that \( |v_{jr}|^{1} = v_{jl} \) and \( |v_{jr}|^{2} = v_{jr} \). Now, consider any intermediate step: \( (p_{j}, e_{j}') \). From type preservation (Lemma 25), we have \( \Pi_{j}; \Gamma_{j}; \Theta_{j}; p_{j}; p_{c} \vdash e'_{j} : \tau_{j} \). We have to prove that:

\[
\mathcal{O}((p_{j}, e_{j}'), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}) = \mathcal{O}((p_{j}, e_{j}'), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-})
\]

It is clear that \( e'_{j} \) itself is not a bracket expression as it would violate the clearance requirement. That is, assume that \( e'_{j} \) is a bracket expression. Then as per DT-BRACKET, we have \( \Pi_{j} \vdash H \rightarrow \subseteq pc' \) for some \( pc' \) and \( \Pi_{j}; \Gamma_{j}; \Theta_{j}; p_{j}; p_{c} \vdash e'_{j} : \tau_{j} \) (analogously, \( \Pi_{j}; \Gamma_{j}; \Theta_{j}; p_{j}; p_{c} \vdash e'_{j} : \tau_{j} \)). From the clearance premise, we have \( \Pi_{j} \vdash p_{j} \Rightarrow pc' \). By transitivity, we thus have \( \Pi_{j} \vdash p_{j} \Rightarrow pc' \). However, we are given that \( \Pi_{j} \not\vdash p_{j} \Rightarrow H \rightarrow \) which is a contradiction. Thus \( e'_{j} \) itself is not a bracket.

Now, assume that:

\[
\mathcal{O}((p_{j}, e_{j}'), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}) \neq \mathcal{O}((p_{j}, e_{j}'_{2}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-})
\]

Then there should be some subterm \( e \) of \( e'_{j} \) such that

\[
\mathcal{O}((p_{j}, e_{1}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-}) \neq \mathcal{O}((p_{j}, e_{2}), \tilde{\Pi}_{D_{(j)}, p_{j}}^{-})
\]
We have 2 cases: either $e$ itself is some bracketed value $(\hat{v}_1 \mid \hat{v}_2)$ that is well-typed by DT-BRACKET-VALUES or $e$ is some well-typed $\text{TEE}^t(e_1 \mid e_2)$ or run$\text{TEE}^t(e_1 \mid e_2)$.

**Case 1:** Let $(\hat{v}_1 \mid \hat{v}_2)$ be a well-typed bracket term. That is, $\Pi''; \Gamma''; \Theta''; p_j; pc'' \vdash (\hat{v}_1 \mid \hat{v}_2) : \tau$. Then from DT-BRACKET-VALUES, we require that $\Pi'' \vdash H'' \leq \tau$. This implies that for $k \in \{1, 2\}$, $\hat{v}_k \in \{(1), (v', v''), (p \gg q), \eta_{p' \nu}, v', v''; \Box, (v_1 where v_2 \mid v_3 where v_4)\}$ as other values cannot be protected.

**What happens for $(v_1 where v_2 \mid v_3 where v_4)$?** Note that if $(v_1 where v_2 \mid v_3 where v_4)$ is well-typed under DT-BRACKET-WHERE, then $\mathcal{O}((v_1 where v_2 \mid v_3 where v_4), \hat{\Pi}_{D_1(j)}, \ell^+)$ has to be $\bullet$. This is because, if $\Pi''_j; \Gamma''_j; \Theta''_j; p_j; pc_j \vdash (v_1 where v_2 \mid v_3 where v_4) : \tau$, then investing by DT-BRACKET-WHERE, we have

$$
\Pi''_j; \Gamma''_j; \Theta''_j; p_j; pc_j \vdash v_1 where v_2 : \tau
$$

$$
\Pi''_j; \Gamma''_j; \Theta''_j; p_j; pc_j \vdash v_3 where v_4 : \tau \Pi''_j \vdash H'' \subseteq \ell'
$$

From the last premise, we have that $v_1 and v_3$ are protected. From the grammar of values, we have that $v_1, v_3 \in \{(), (p \gg q), \eta_{p' \nu}, v', v'' \mid v_1 where v_2 \mid v_3 where v_4\}$. Using induction, we can prove that if $v's$ type is protected, then erase of $v$ is $\bullet$.

If $v_k = \eta_{p' \nu}, v'$, that is, $(\hat{v}_1 \mid \hat{v}_2) = (\eta_{p' \nu}, v' \mid \eta_{p' \nu}, v'')$, then we require that $\Pi''_j \vdash H'' \subseteq \ell'$. Since all delegations in $\Pi''_j$ belong to $\hat{\Pi}_{D_1(j)}$ and $\hat{\Pi}_{D_2(j)}$, we have that $\mathcal{O}(\eta_{p' \nu}, v', \hat{\Pi}_{D_1(j)}, \ell^+) = \eta_{p' \nu}, \bullet$ and $\mathcal{O}(\eta_{p' \nu}, v'', \hat{\Pi}_{D_2(j)}, \ell^+) = \eta_{p' \nu}, \bullet$. Hence they are equal after erasure. Remaining terms can be proved using induction that the erase results in same terms.

**Case 2:** Let the expression be $\text{TEE}^t(e_1 \mid e_2)$. Then from DT-BRACKET, we require that $\Pi'_j \vdash pc' \gg H''$ and $\Pi'_j; \Gamma'_j; \Theta'_j; t; pc' \vdash e_i ; \tau$ for $i \in \{1, 2\}$. Then from the clearance (Lemma 7), we have that $\Pi'_j \vdash t^+ \gg H''$. This implies that $\Pi'_j \vdash t^+ \gg H''$ and so $\mathcal{O}(\text{TEE}^t e_1, \hat{\Pi}_{D_1(j)}, \ell^+) = \text{TEE}^t \bullet$ and $\mathcal{O}(\text{TEE}^t e_2, \hat{\Pi}_{D_2(j)}, \ell^+) = \text{TEE}^t \bullet$. Hence they are equal after erasure. Similarly for run$\text{TEE}^t(e_1 \mid e_2)$.

Repeatedly applying type preservation will thus give that

$$
\mathcal{O}(p_j, \{e_j\}_1, \hat{\Pi}_{D_1(j)}, p_j^-), \ldots, \mathcal{O}(p_j, \{v_j\}_1, \hat{\Pi}_{D_1(j)}, p_j^-) \approx \mathcal{O}(p_j, \{e_j\}_2, \hat{\Pi}_{D_2(j)}, p_j^-), \ldots, \mathcal{O}(p_j, \{v_j\}_2, \hat{\Pi}_{D_2(j)}, p_j^-) (368)
$$

However, we still need to prove that the trace for $p_j$ node in bracketed execution equals the trace in corresponding executions. Consider the following two executions: given individual execution with schedule $S_1$ and bracketed execution projected on the left side with schedule, $S_1 \mid \_$. Further, we assume that the length of the $S_1$ is the minimum number of steps required to reduce $[e_s']_i$ on node $p_j$ to a value. Note that this may incur evaluating other nodes due to communication. However, the schedule for the bracketed execution eliminates evaluation on unnecessary nodes.

We now argue that $S_1 \mid \_$ commutes with $S_1$ for the first $s$ steps. Lemma 29 allows us to reschedule $S_1$ to $S_{\ell'}$ such that $[S_1 \mid \_][1 : s] = S_{\ell'}[1 : s]'$ (upto stuttering). The schedule can be completed such that it satisfies the property

$$
\forall p, [S_1]_p = [S_{\ell'}]_p
$$

Note that such a construction exists but we elide the actual details. Since $S_1$ and $S_{\ell'}$ are commuting, their traces for node $p_j$ are same (Lemma 28).

Also note that trace of node $p_j$ after $s$ steps in $S_{\ell'}$ is simply a chain of $(p_j, v''_j)$ and deduplication removes them completely. So we only consider the trace generated by the schedule $S_{\ell'}[1 : s]$. However, this is equal to the $S_1 \mid \_$. This implies that if both executions start in the same configuration, they take same number of steps on each node and thus end in same configuration. Thus traces generated by $S_{\ell'}$ (and hence $S_1$) and $S_1 \mid \_$ are equal.

Similarly, we can prove that trace of node $p_j$ generated by the second execution $S_\ell$ and bracketed execution $S_1 \mid \_$ is same. This implies that we can now use traces of bracketed execution to reason about the trace equivalence. From (368), we already proved that the projected traces of a bracketed execution are equal. Hence proved.

**APPENDIX D**

**FLAM and FLAC**

The following lemma establishes a tight connection between FLAM and FLAC.

**Lemma 30** (FLAC implies FLAM). Let $\Pi = \mathcal{H}(c)$. If $\Pi; pc; \ell \vdash \gg q$, then $\mathcal{H}; c; pc; \ell \vdash \gg q$.

**Proof.** Proof is by induction on the derivation of the robust assumption $\Pi; pc; \ell \vdash \gg q$. Interesting case is R-ASSUME.

**Case R-Assume:** From the premises, we have, that
\[
\langle p \triangleright q \mid \ell \land \nabla(q) \rangle \in \Pi \tag{369}
\]
\[
\Pi; \ell, p \vdash \ell \leq \tau \tag{370}
\]
\[
\Pi; \ell \vdash \nabla(q) \tag{371}
\]
\[
\Pi; \ell \vdash \nabla(p^-) \triangleright \nabla(q^-) \tag{372}
\]

From (369) and DEL, we have that

\[
\mathcal{H}; c; p \vdash \ell \triangleright p \tag{373}
\]

From (370) and [ Weaken], we get that \( \mathcal{H}; c; p \vdash \ell \land \nabla(q) \vdash p \triangleright q \). From the (371), (372) and R-LIFT we thus have

\[
\mathcal{H}; c; p \vdash p \triangleright q \tag{374}
\]

Case R-Static: Since \( L \models p \triangleright q \), we have that

\[
\mathcal{H}; c; \bot \triangleright \top \vdash p \triangleright q \tag{375}
\]

From FLAM R-STATIC, we thus have, \( \mathcal{H}; c; p \vdash p \triangleright q \).

Case R-Conj: Straightforward.

Case R-DisjL: Straightforward.

Case R-Trans: Straightforward.

Case R-Weaken: Straightforward.